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# Datasheet

## Kyocera

**TCG121WXLPA PNN-AN30**

KY-01-041

# SPEC for Mass Production

Spec No.	TQ3C-8EAF0-E1YAM51-01
Date	April 28, 2023

## **TYPE : TCG121WXLPA PNN-AN30**

< 12.1 inch WXGA transmissive color TFT with LED backlight,  
constant current circuit for LED backlight >

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12. Reliability test data
13. Outline drawing



KYOCERA CORPORATION

This specification is subject to change without notice.  
Consult Kyocera before ordering.

Original Issue Date	Designed by: Engineering dept.			Confirmed by: dept.
	Prepared	Checked	Approved	Approved
July 12, 2022	<i>Y. Takakura</i>	<i>I. Kawajiri</i>	<i>A. Iwasaki</i>	<i>T. Sawada</i>

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## **Warning**

1. This Kyocera LCD module has been specifically designed for use only in electronic devices and industrial machines in the area of audio control, office automation, industrial control, home appliances, etc. The module should not be used in applications where the highest level of safety and reliability are required and module failure or malfunction of such module results in physical harm or loss of life, as well as enormous damage or loss. Such fields of applications include, without limitation, medical, aerospace, communications infrastructure, atomic energy control. Kyocera expressly disclaims any and all liability resulting in any way to the use of the module in such applications.
2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, damages, liabilities, awards, costs, and expenses, including legal expenses, resulting from or arising out of Customer's use, or sale for use, or Kyocera modules in applications.

## **Caution**

1. Kyocera may ask you to approve that Kyocera will scrap the tooling or stop supplying the LCD, after one-year time frame from your latest purchase, of the LCD. In the case you request Kyocera to keep/maintenance the tooling even after one-year time frame from your latest purchase, we would like to bear the maintenance cost, of which details shall be discussed separately between you and Kyocera Corporation.
2. Please note that we may not be able to respond to new environmental regulations after receiving the final mass production order for this product.

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### Revision record

Date		Designed by : Engineering dept.			Confirmed by : QA dept.
		Prepared	Checked	Approved	Approved
April 28, 2023		<i>Y. Takakura</i>	<i>I. Kawajiri</i>	<i>A. Iwasaki</i>	<i>T. Sawada</i>
Rev.No.	Date	Page	Descriptions		
01	Apr. 28, 2023	7	6. Optical characteristics Revise the minimum contrast ratio.		
		9, 10	7. Interface signals Change LCD connector and matching connector.		
		11	7-3. Data mapping (6bit input / 8bit mode) Modify the data mapping diagram.		
		18	7-5. Data mapping (6bit input / 6bit mode) Modify the data mapping diagram.		
		-	Revise the outline drawing.		

## 1. Application

This document defines the specification of TCG121WXLPA PNN-AN30 (RoHS Compliant).

## 2. Construction and outline

LCD	: Transmissive color dot matrix type TFT
Backlight system	: LED
Polarizer	: Anti-Glare treatment
Interface	: LVDS
Additional circuit	: Timing controller, Power supply (3.3V input) : Constant current circuit for LED Backlight(12V input)

## 3. Mechanical specifications

Item	Specification	Unit
Outline dimensions 1)	277.7(W)×(182.5)(H)×9.8(D)	mm
Active area	261.12(W)×163.2(H) (30.8cm/12.1 inch(Diagonal))	mm
Dot format	1280×(R,G,B)(W)×800(H)	dot
Dot pitch	0.068(W)×0.204(H)	mm
Base color 2)	Normally Black	-
Mass	680	g

1) Projection not included. Please refer to outline for details.

2) Due to the characteristics of the LCD material, the color varies with environmental temperature.

## 4. Absolute maximum ratings

### 4-1. Electrical absolute maximum ratings

Item		Symbol	Min.	Max.	Unit
Supply voltage(+3.3V)		V <sub>DD</sub>	-0.3	4.0	V
Supply voltage(+12V)		V <sub>IN</sub>	-0.3	14.0	V
Input signal Voltage 1)	RxINi+, RxINi- (i=0,1,2,3)	V <sub>I1</sub>	-0.3	2.8	V
	CK IN+, CK IN-	V <sub>I2</sub>	-0.3	2.8	V
	SELLVDS, BITSEL, SC	V <sub>I3</sub>	-0.3	V <sub>DD</sub> +0.5	V
	BLBRT,BLEN	V <sub>I4</sub>	-0.3	V <sub>in</sub>	V

1) V<sub>DD</sub> must be supplied correctly within the range described in 5-1.

### 4-2. Environmental absolute maximum ratings

Item	Symbol	Min.	Max.	Unit
Operating temperature(Ambient) 1)	T <sub>OP(Ambient)</sub>	-20	70	°C
Operating temperature(Panel) 2)	Top (Panel)	-20	70	°C
Storage temperature 3)	T <sub>STO</sub>	-30	80	°C
Operating humidity 4)	H <sub>OP</sub>	10	5)	%RH
Storage humidity 4)	H <sub>STO</sub>	10	5)	%RH
Vibration	-	6)	6)	-
Shock	-	7)	7)	-

1) Operating temperature means a temperature which operation shall be guaranteed. Since display performance is evaluated at 25°C, another temperature range should be confirmed.

2) Panel / Backboard surface temperature (all the surface)

3) Temp. = -30°C < 48h , Temp. = 80°C < 168h

Store LCD at normal temperature/humidity. Keep them free from vibration and shock.

An LCD that is kept at a low or a high temperature for a long time can be defective due to other conditions, even if the low or high temperature satisfies the standard.

(Please refer to “Precautions for Use” for details.)

4) Non-condensing

5) Temp. ≤ 40°C, 85%RH Max.

Temp. > 40°C, Absolute humidity shall be less than 85%RH at 40°C.

6)

Frequency	10~55 Hz	Acceleration value (0.3~9 m/s <sup>2</sup> )
Vibration width	0.15mm	
Interval	10-55-10 Hz	1 minute

2 hours in each direction X, Y, Z (6 hours total)

EIAJ ED-2531

7) Acceleration: 490 m/s<sup>2</sup>, Pulse width: 11 ms

3 times in each direction: ±X, ±Y, ±Z

EIAJ ED-2531

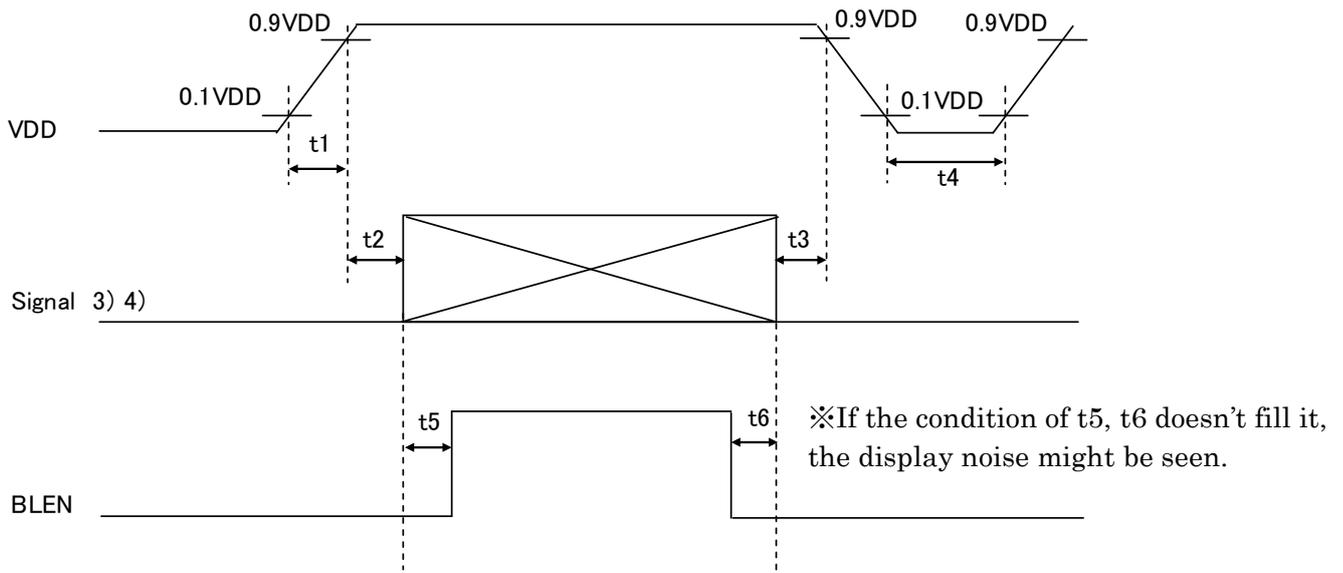
## 5. Electrical characteristics

### 5-1. LCD

Temp. = -20~70°C

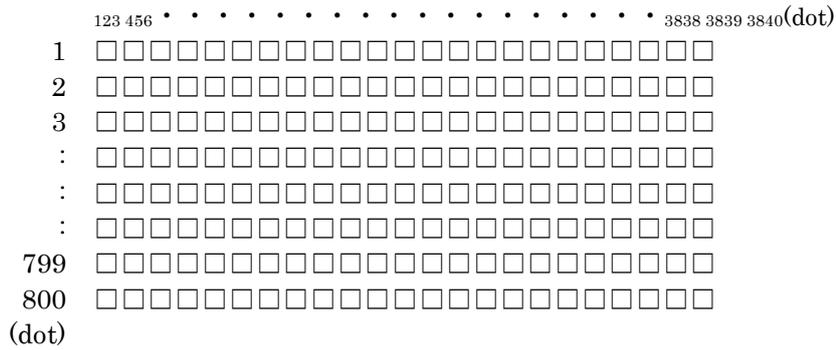
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	1) $V_{DD}$	-	3.0	3.3	3.6	V
Current consumption	$I_{DD}$	2)	-	375	490	mA
Permissive input ripple voltage	$V_{RP}$	$V_{DD}=3.3V$	-	-	100	mVp-p
Input signal voltage	3) $V_{IL}$	"Low" level	0	-	0.8	V
	$V_{IH}$	"High" level	2.0	-	$V_{DD}$	V
Input leak current	$I_{OL}$	$V_{I3}=0V$	-10	-	10	$\mu A$
	$I_{OH}$	$V_{I3}=3.3V$	-	-	400	$\mu A$
LVDS Input voltage	4) $V_L$	-	0	-	1.9	V
Differential input voltage	4) $V_{ID}$	-	100	350	600	mV
Differential input threshold voltage	4) 5) $V_{TL}$	"Low" level	$V_{CM}-100$	-	-	mV
	$V_{TH}$	"High" level	-	-	$V_{CM}+100$	mV
Terminator	$R_1$	-	-	100	-	$\Omega$
$V_{DD}$ -turn-on conditions	1) $t_1$	-	0.1	-	10	ms
	$t_2$	-	0	-	-	ms
	$t_3$	-	0	-	-	ms
	$t_4$	-	1.0	-	-	s
	$t_5$	-	200	-	-	ms
	$t_6$	-	200	-	-	ms

#### 1) $V_{DD}$ -turn-on conditions



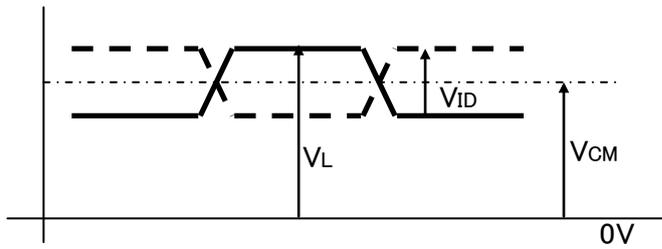
2) Display pattern:

$V_{DD} = 3.3V$ , Temp. = 25°C



3) Input signal : SELLVDS, BITSEL, SC

4) Input signal : RxIN3+, RxIN3-, RxIN2+, RxIN2-, RxIN1+, RxIN1-, RxIN0+, RxIN0-  
CK IN+, CK IN-



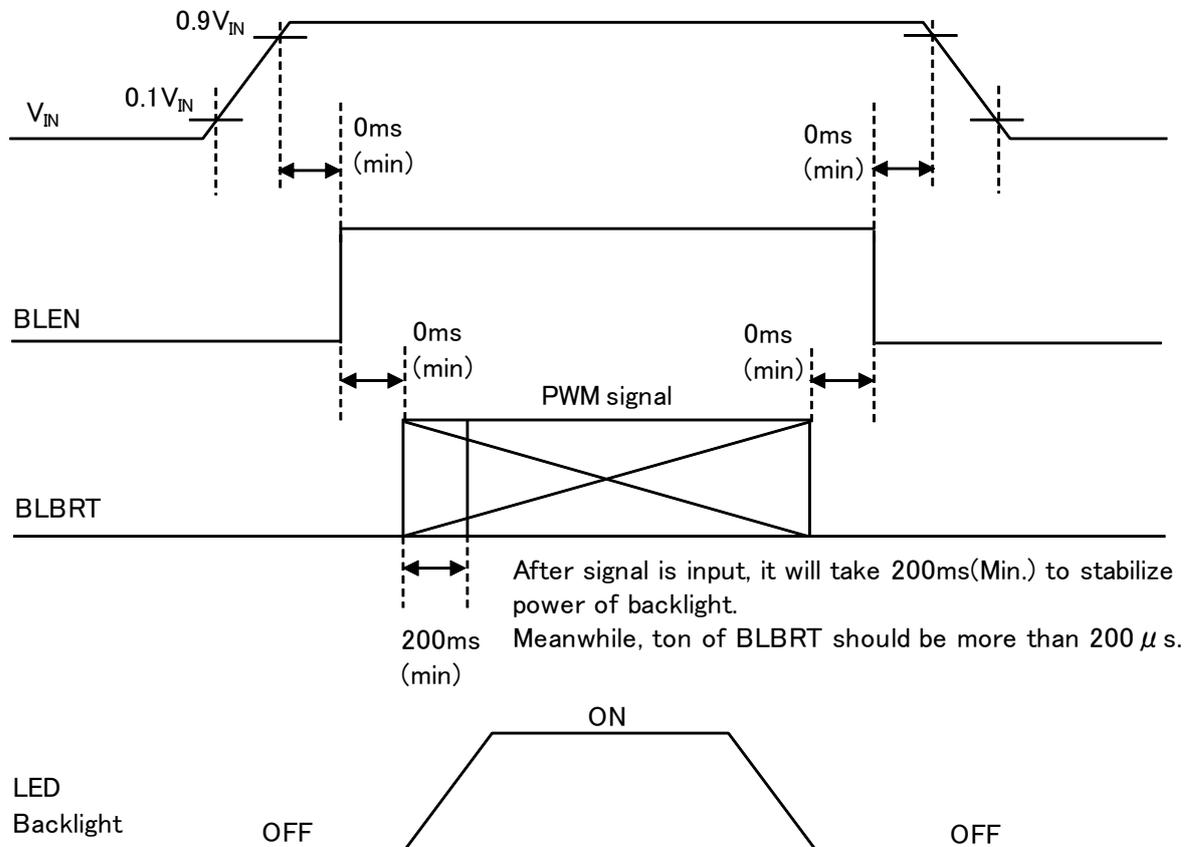
5)  $V_{CM}$  : LVDS Common mode voltage ( $V_{CM}=1.25V$ )

5-2. Constant current circuit for LED Backlight

Temp. = -20~70°C

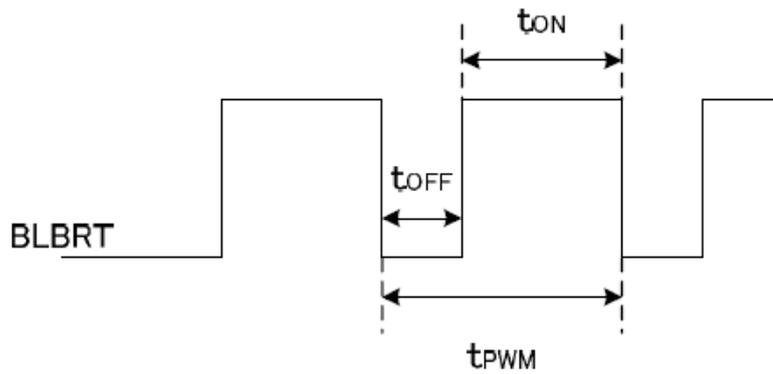
Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Supply voltage	1) $V_{IN}$	-	10.8	12.0	13.2	V
Current consumption	$I_{IN}$	2)	-	630	900	mA
Permissible input ripple voltage	$V_{RP\_BL}$	$V_{IN}=12.0V$	-	-	100	mVp-p
BLBRT Input signal voltage	$V_{IL\_BLBRT}$	"Low" level	0	-	0.4	V
	$V_{IH\_BLBRT}$	"High" level	1.5	-	$V_{IN}$	V
BLBRT Input pull-down resistance	$R_{IN\_BLBRT}$	-	-	100	-	kΩ
BLEN Input signal voltage	$V_{IL\_BLEN}$	"Low" level	0	-	0.6	V
	$V_{IH\_BLEN}$	"High" level	1.5	-	$V_{IN}$	V
BLEN Input pull-down resistance	$R_{IN\_BLEN}$	-	-	100	-	kΩ
PWM Frequency	3) $f_{PWM}$	-	200	—	20k	Hz
PWM Duty ratio	3) $D_{PWM}$	$f_{PWM}=200Hz$	1	—	100	%
		$f_{PWM}=2kHz$	1	—	100	%
		$f_{PWM}=20kHz$	10	—	100	%
Operating life time	4), 5) $T$	Temp.=25°C	-	50,000	-	h

1)  $V_{IN}$ -turn-on conditions



2)  $V_{IN} = 12V$ , Temp. = 25°C,  $D_{PWM} = 100\%$

3) PWM timing diagram



Please do not set  $t_{OFF}$  with  $0 \mu s < t_{OFF} < 1 \mu s$ .

In case of lower frequency, the deterioration of the display quality, flicker etc., may occur.

- 4) When brightness decrease 50% of minimum brightness.  
The average life of a LED will decrease when the LCD is operating at higher temperatures.
- 5) Life time is estimated data. (Condition :  $D_{PWM} = 100\%$ ,  $T_a = 25^\circ C$  in chamber).

## 6. Optical characteristics

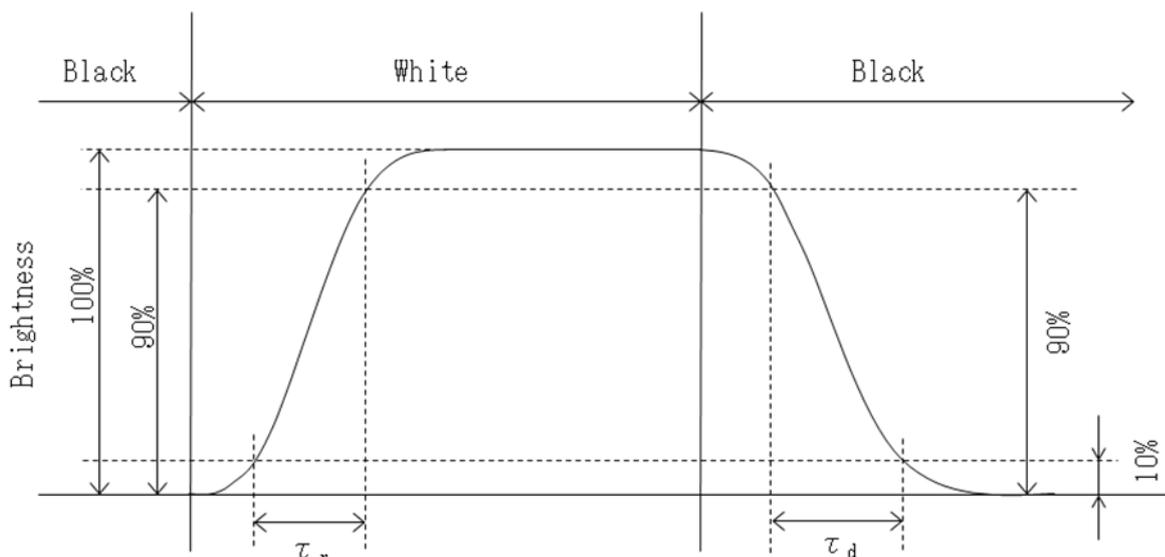
Measuring spot =  $\phi$  6.0mm, Temp. = 25°C

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	
Response time	Rise	$\tau_r$	$\theta = \phi = 0^\circ$	—	18	—	ms
	Down	$\tau_d$	$\theta = \phi = 0^\circ$	—	12	—	ms
Viewing angle range	$\theta$ UPPER	$CR \geq 10$	—	85	—	deg.	
	$\theta$ LOWER		—	85	—		
	$\phi$ LEFT		—	85	—	deg.	
	$\phi$ RIGHT		—	85	—		
Contrast ratio	CR	$\theta = \phi = 0^\circ$	500	750	—	-	
Brightness	L	$D_{PWM} = 100\%$	700	1000	—	cd/m <sup>2</sup>	
Chromaticity coordinates	Red	x	$\theta = \phi = 0^\circ$	0.555	0.605	0.655	-
		y		0.310	0.360	0.410	
	Green	x	$\theta = \phi = 0^\circ$	0.290	0.340	0.390	
		y		0.520	0.570	0.620	
	Blue	x	$\theta = \phi = 0^\circ$	0.110	0.160	0.210	
		y		0.080	0.130	0.180	
	White	x	$\theta = \phi = 0^\circ$	0.255	0.305	0.355	
		y		0.285	0.335	0.385	

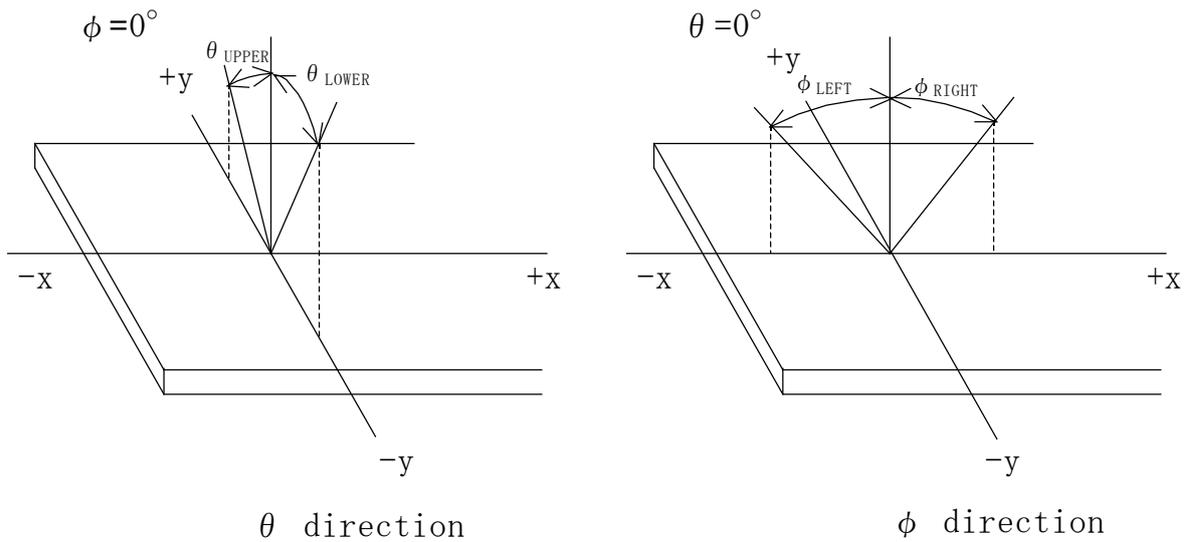
### 6-1. Definition of contrast ratio

$$CR(\text{Contrast ratio}) = \frac{\text{Brightness with all pixels "White"}}{\text{Brightness with all pixels "Black"}}$$

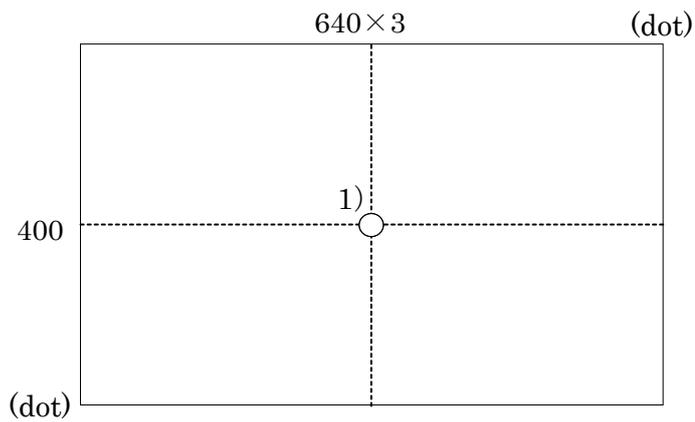
### 6-2. Definition of response time



6-3. Definition of viewing angle



6-4. Brightness measuring point



- 1) Rating is defined as the white brightness at center of display screen.
- 2) 5 minutes after LED is turned on. (Ambient Temp.=25°C)

## 7. Interface signals

### 7-1. LCD

No.	Symbol	Description	Note
1	SC	Scan direction control (GND or Open : Normal, High : Reverse)	1)
2	BITSEL	Bit data select signal (GND or Open: 8bit mode, High: 6bit mode)	2)
3	RxIN3+	LVDS receiver signal CH3(+)	LVDS
4	RxIN3-	LVDS receiver signal CH3(-)	LVDS
5	GND	GND	
6	CK IN+	LVDS receiver signal CK(+)	LVDS
7	CK IN-	LVDS receiver signal CK(-)	LVDS
8	GND	GND	
9	RxIN2+	LVDS receiver signal CH2(+)	LVDS
10	RxIN2-	LVDS receiver signal CH2(-)	LVDS
11	GND	GND	
12	RxIN1+	LVDS receiver signal CH1(+)	LVDS
13	RxIN1-	LVDS receiver signal CH1(-)	LVDS
14	GND	GND	
15	RxIN0+	LVDS receiver signal CH0(+)	LVDS
16	RxIN0-	LVDS receiver signal CH0(-)	LVDS
17	GND	GND	
18	SELLVDS	Mode select signal(LVDS Data mapping)	
19	V <sub>DD</sub>	+3.3V power supply	
20	V <sub>DD</sub>	+3.3V power supply	

LCD connector : DF19G-20P-1H(52) (HIROSE)

Matching connector : DF19G-20S-1C(06) (HIROSE)

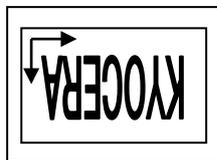
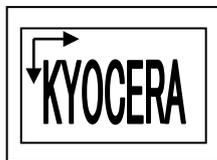
LVDS receiver : Embedded in ASIC

Matching LVDS transmitter : THC63LVDM83D(Thine Electronics) or compatible

#### 1) Scan direction

SC : GND or Open

SC : High



#### 2) BITSEL

Number of colors

8 bit mode: 16.2M colors

6 bit mode: 262k colors

7-2. LED

No.	Symbol	Description	Note
1	V <sub>IN</sub>	(+12V) power supply	
2	V <sub>IN</sub>	(+12V) power supply	
3	V <sub>IN</sub>	(+12V) power supply	
4	BLEN	ON/OFF terminal voltage	
5	BLBRT	PWM signal(Brightness adjustment)	
6	GND	GND	
7	GND	GND	
8	GND	GND	

LCD connector : SM08B-SHLS-G-TF(LF)(SN) (JST)

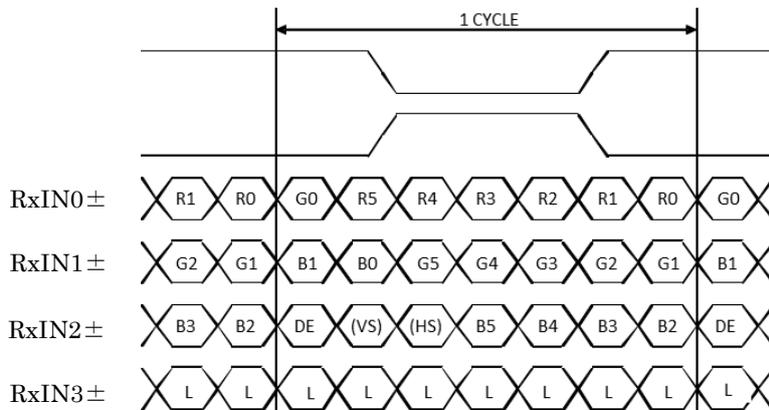
Matching connector : SHLP-08V-S-B (JST)

7-3. Data mapping (6bit input / 8bit mode)

1) Location of BITSEL, SELLVDS (THC63LVDM83D (Thine Electronics) or compatible)

Transmitter		2Pin BITSEL = "L" or OPEN 18Pin SELLVDS = "L" or OPEN	2Pin BITSEL = "L" or OPEN 18Pin SELLVDS = "H"
Pin No.	Data		
51	TA0	—	R0(LSB)
52	TA1	—	R1
54	TA2	—	R2
55	TA3	—	R3
56	TA4	—	R4
3	TA5	—	R5(MSB)
4	TA6	—	G0(LSB)
6	TB0	—	G1
7	TB1	—	G2
11	TB2	—	G3
12	TB3	—	G4
14	TB4	—	G5(MSB)
15	TB5	—	B0(LSB)
19	TB6	—	B1
20	TC0	—	B2
22	TC1	—	B3
23	TC2	—	B4
24	TC3	—	B5(MSB)
27	TC4	—	(HS)
28	TC5	—	(VS)
30	TC6	—	DE
50	TD0	—	GND
2	TD1	—	GND
8	TD2	—	GND
10	TD3	—	GND
16	TD4	—	GND
18	TD5	—	GND
25	TD6	—	GND

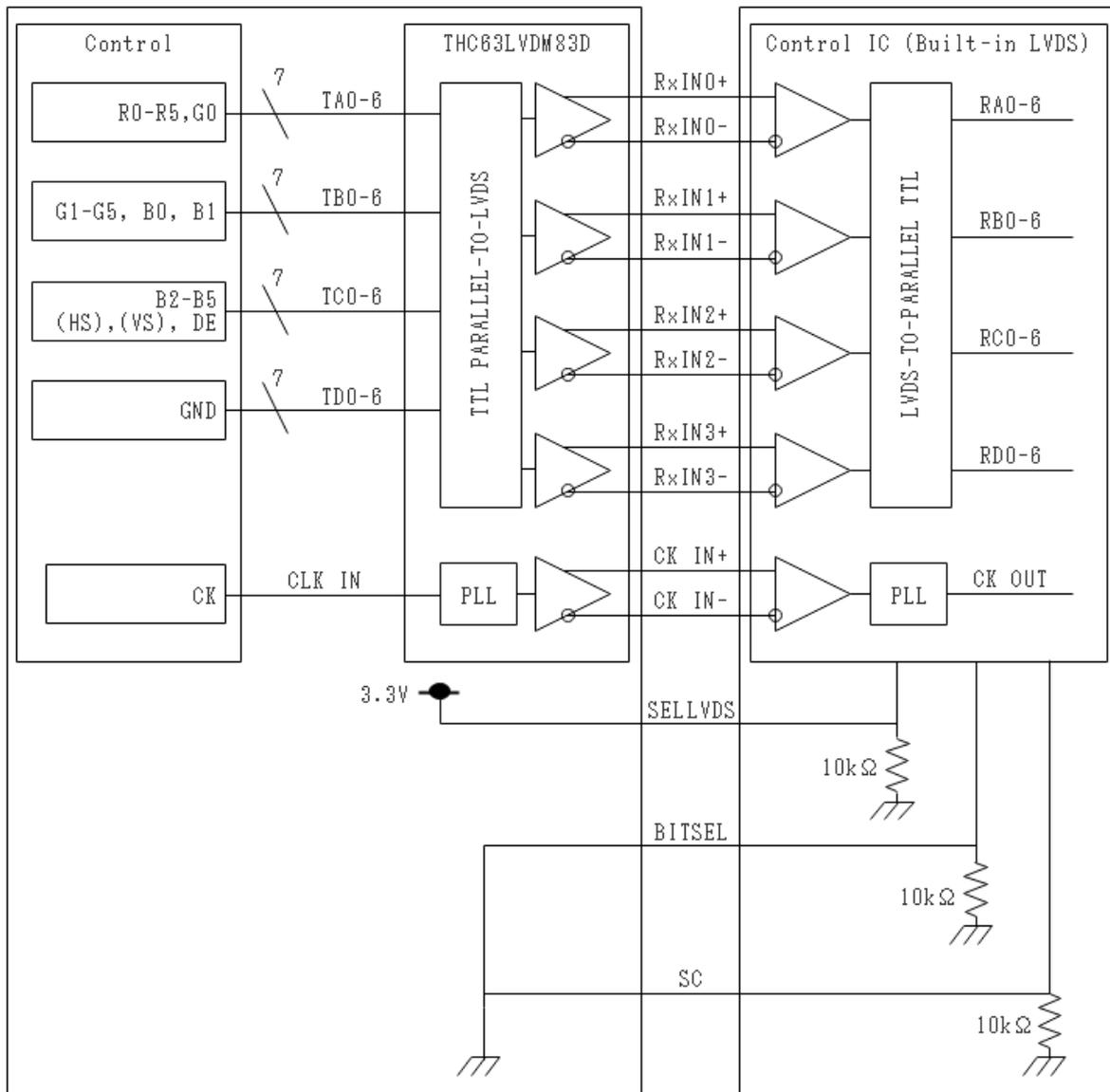
BITSEL=L (GND) or OPEN  
SELLVDS=H (3.3V)



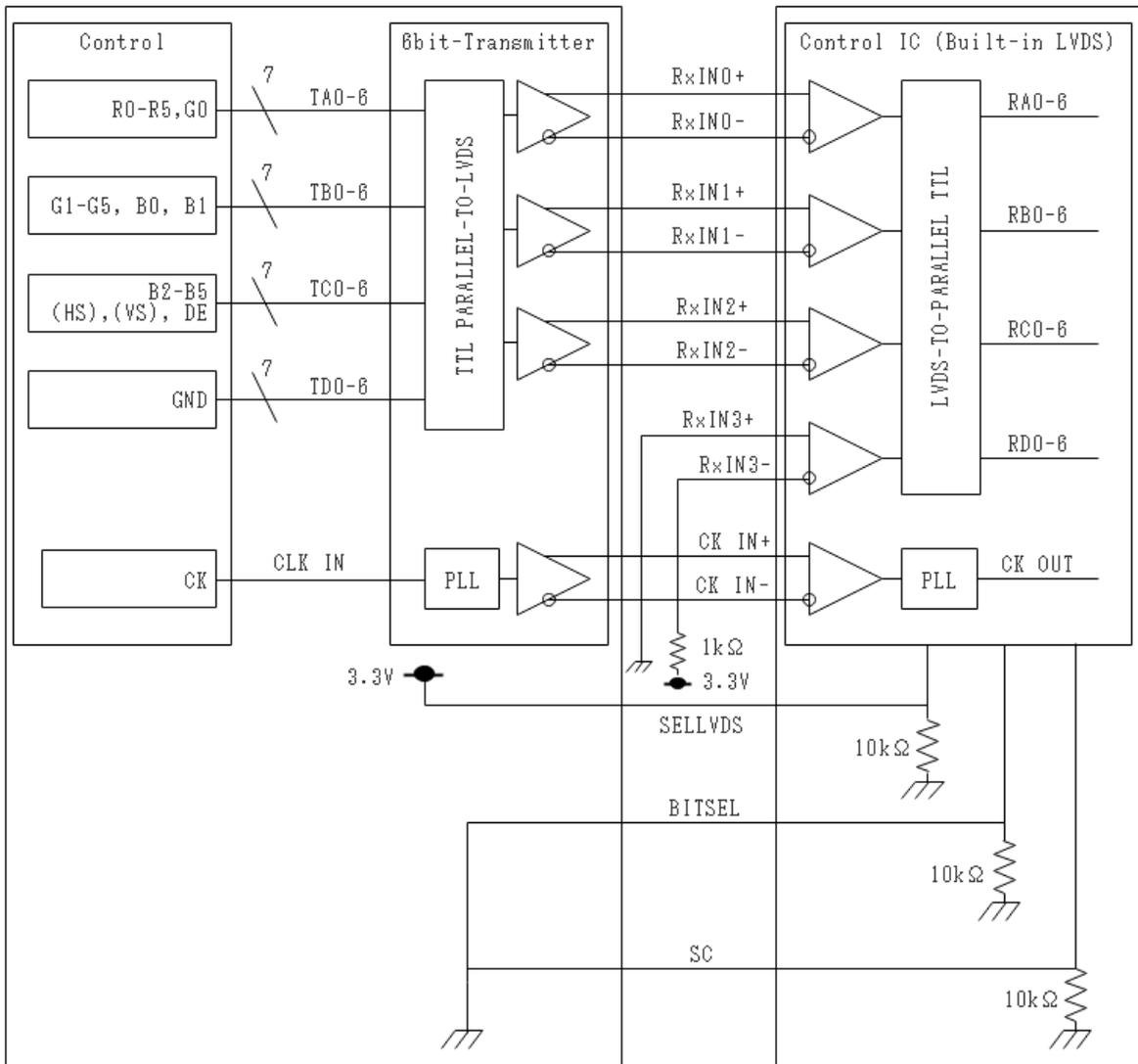
DE : DATA ENABLE  
HS : H<sub>SYNC</sub>  
VS : V<sub>SYNC</sub>

2) Block diagram

BITSEL=L (GND) or OPEN  
SELLVDS=H (3.3V)



When using "6-bit Transmitter", please connect the unused channel of the control IC receiver as described in the diagram below.

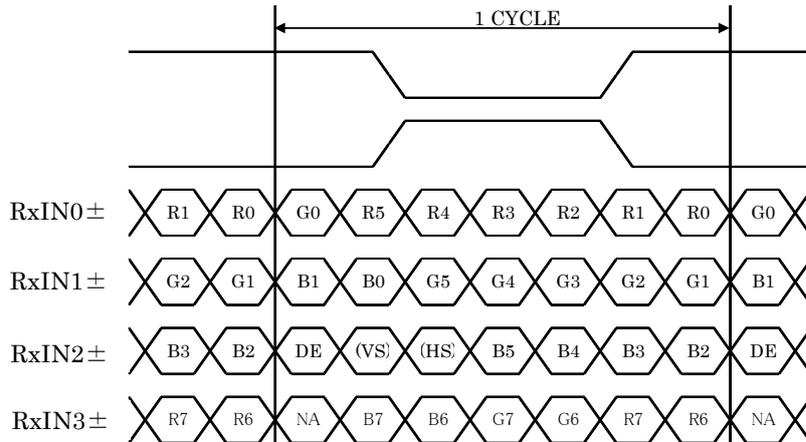


7-4. Data mapping (8bit input / 8bit mode)

1) Location of BITSEL, SELLVDS (THC63LVDM83D (THine Electronics) or compatible)

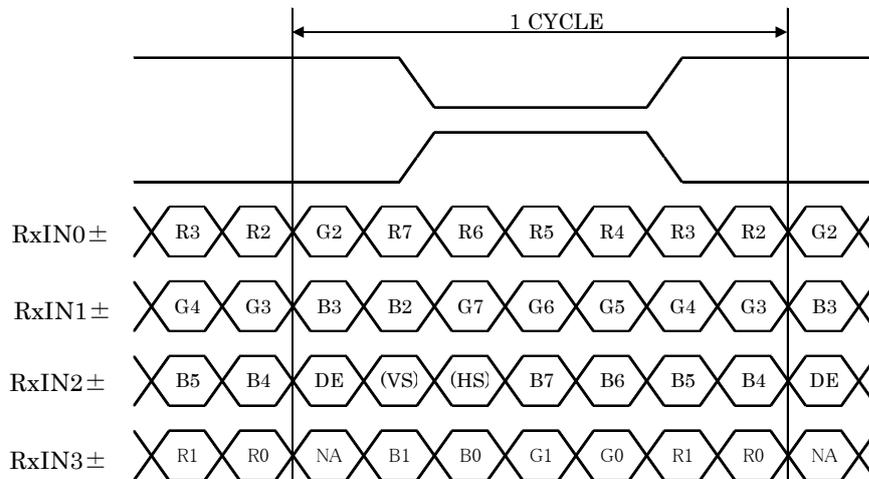
Transmitter		2Pin BITSEL = "L" or OPEN 18Pin SELLVDS = "L" or OPEN	2Pin BITSEL = "L" or OPEN 18Pin SELLVDS = "H"
Pin No.	Data		
51	TA0	R0(LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7(MSB)
4	TA6	G0(LSB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7(MSB)
15	TB5	B0(LSB)	B2
19	TB6	B1	B3
20	TC0	B2	B4
22	TC1	B3	B5
23	TC2	B4	B6
24	TC3	B5	B7(MSB)
27	TC4	(HS)	(HS)
28	TC5	(VS)	(VS)
30	TC6	DE	DE
50	TD0	R6	R0(LSB)
2	TD1	R7(MSB)	R1
8	TD2	G6	G0(LSB)
10	TD3	G7(MSB)	G1
16	TD4	B6	B0(LSB)
18	TD5	B7(MSB)	B1
25	TD6	(NA)	(NA)

BITSEL=L (GND) or OPEN  
SELLVDS=L (GND) or OPEN



DE : DATA ENABLE  
HS : H<sub>SYNC</sub>  
VS : V<sub>SYNC</sub>

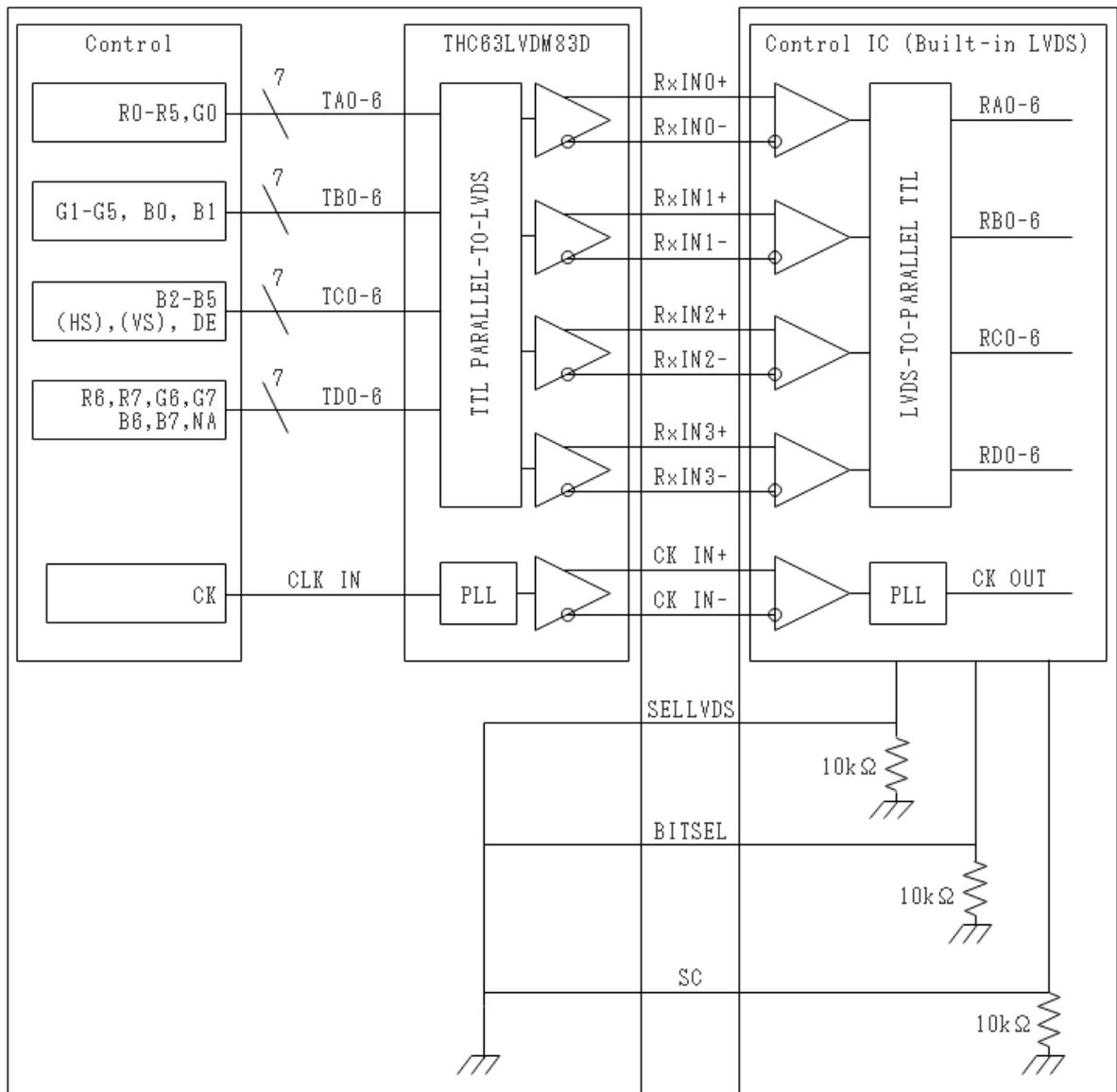
BITSEL=L (GND) or OPEN  
SELLVDS=H (3.3V)



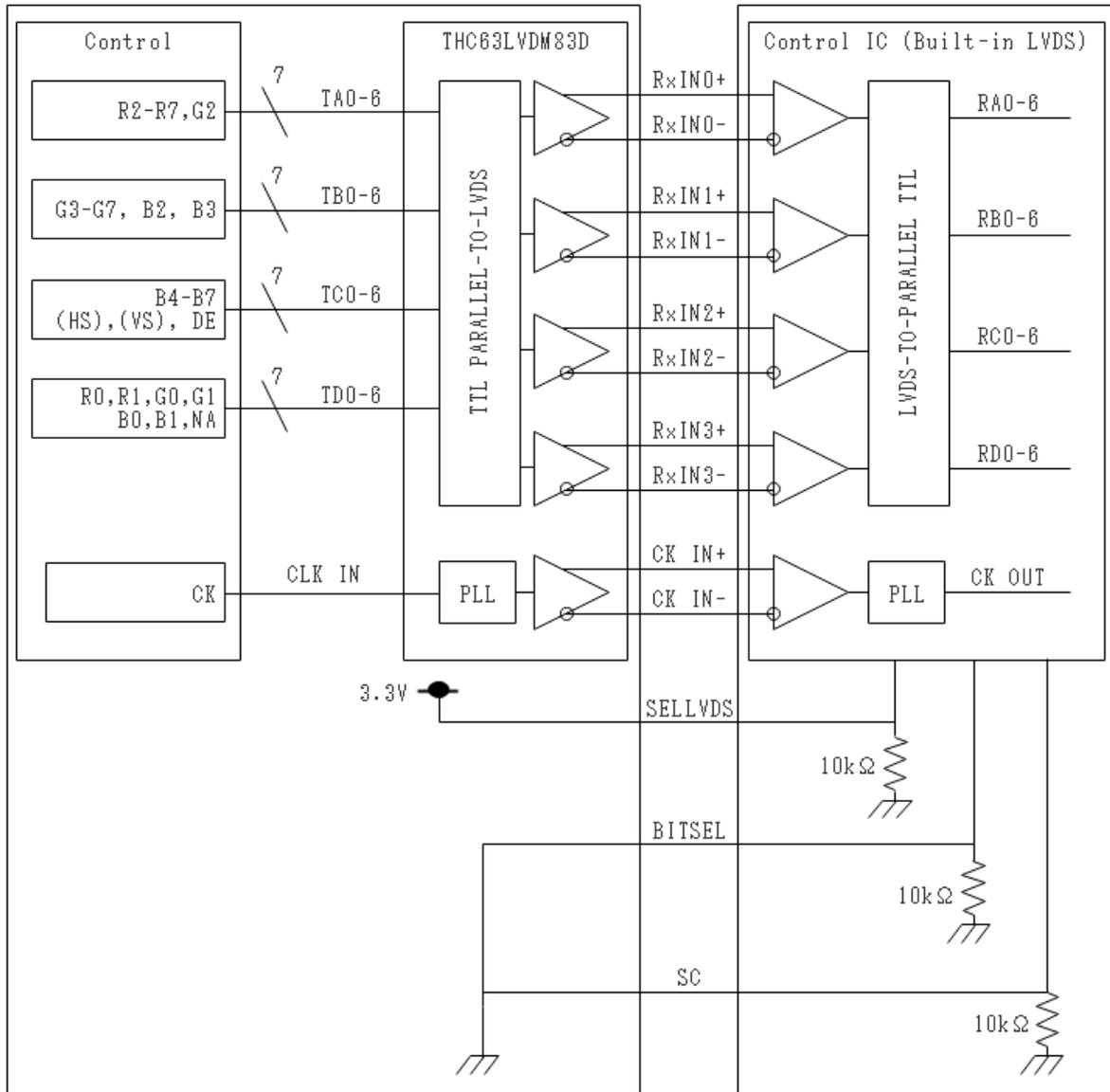
DE : DATA ENABLE  
HS : H<sub>SYNC</sub>  
VS : V<sub>SYNC</sub>

2) Block diagram

BITSEL=L (GND) or OPEN  
SELLVDS= L (GND) or OPEN



BITSEL=L(GND) or OPEN  
SELLVDS=H(3.3V)



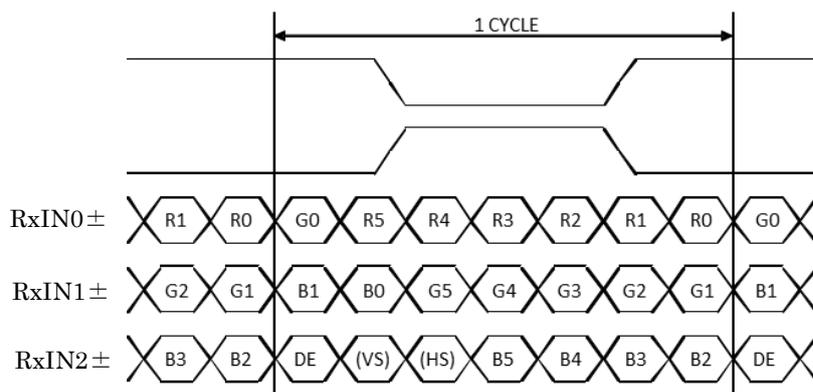
7-5. Data mapping (6bit input / 6bit mode)

1) Location of BITSEL, SELLVDS (THC63LVDM83D (Thine Electronics) or compatible)

Transmitter		2Pin BITSEL = "H" 18Pin SELLVDS = "L" or OPEN	2Pin BITSEL = "H" 18Pin SELLVDS = "H"
Pin No.	Data		
44	TA0	R0(LSB)	—
45	TA1	R1	—
47	TA2	R2	—
48	TA3	R3	—
1	TA4	R4	—
3	TA5	R5(MSB)	—
4	TA6	G0(LSB)	—
6	TB0	G1	—
7	TB1	G2	—
9	TB2	G3	—
10	TB3	G4	—
12	TB4	G5(MSB)	—
13	TB5	B0(LSB)	—
15	TB6	B1	—
16	TC0	B2	—
18	TC1	B3	—
19	TC2	B4	—
20	TC3	B5(MSB)	—
22	TC4	(HS)	—
23	TC5	(VS)	—
25	TC6	DE	—

BITSEL=H (3.3V)

SELLVDS=L (GND) or OPEN



DE : DATA ENABLE

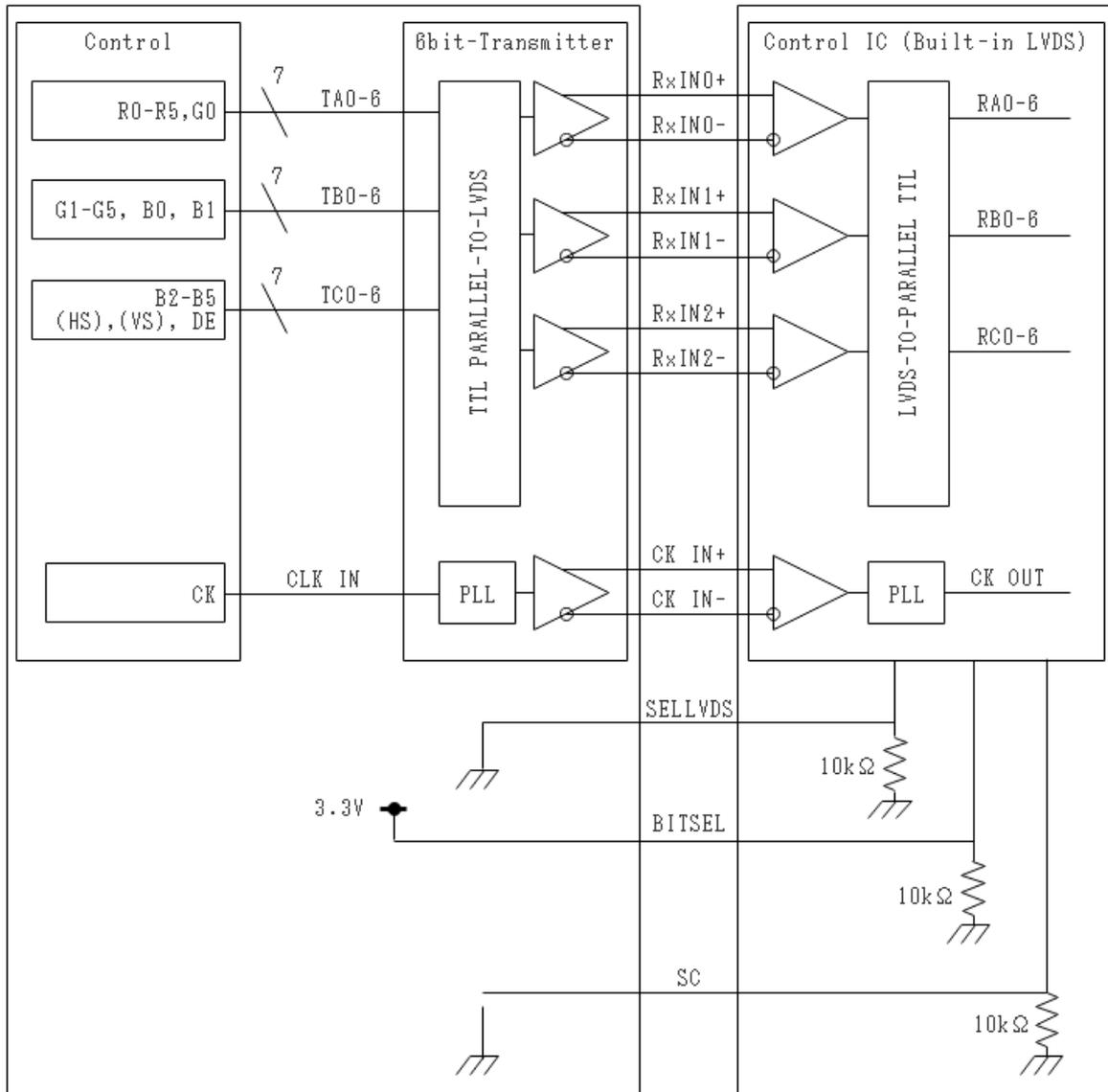
HS : H<sub>SYNC</sub>

VS : V<sub>SYNC</sub>

2) Block diagram

BITSEL=H (3.3V)

SELLVDS=L (GND) or OPEN



## 8. Input timing characteristics

### 8-1. Timing characteristics 1)

Item		Symbol	Min.	Typ.	Max.	Unit	Note
Clock (CK)	Frequency	1/Tc	60	71.1	80	MHz	
Enable signal (DE)	Horizontal Period	Th	1300	1440	1800	Dot	2)
			16.25	20.25	-	$\mu s$	
	Horizontal display period	Thd	1280				
	Vertical Period	Tv	803	823	1024	Line	
	Vertical display period	Tvd	800				
Refresh rate		fv	50	60	70	-	3)

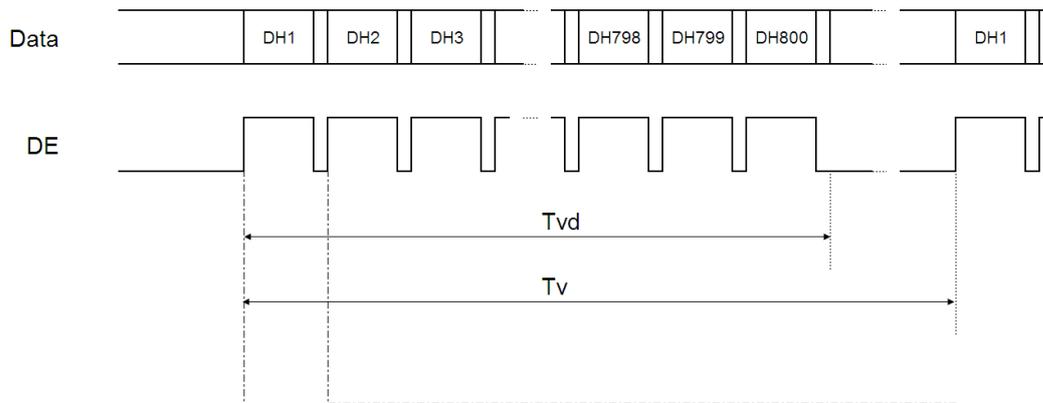
1) If the display is used under the condition which is out of specifications such as higher clock frequency than specified value, there is a possibility phenomenon such as display error including white display, malfunction and no image may occur.

Please use the display under the conditions written in the specification.

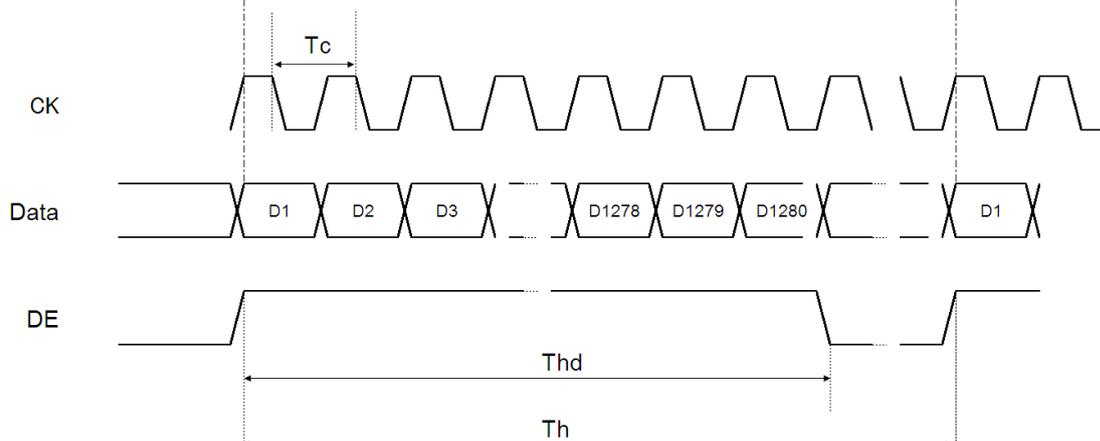
2) Please set a clock frequency, a vertical dormant period, and the horizontal dormant period so that the Horizontal Period should not reach less than Min. value.

3) If the refresh rate reach less than Min. value, the deterioration of the display quality, flicker etc., may occur. ( $fv=1/Tv$ )

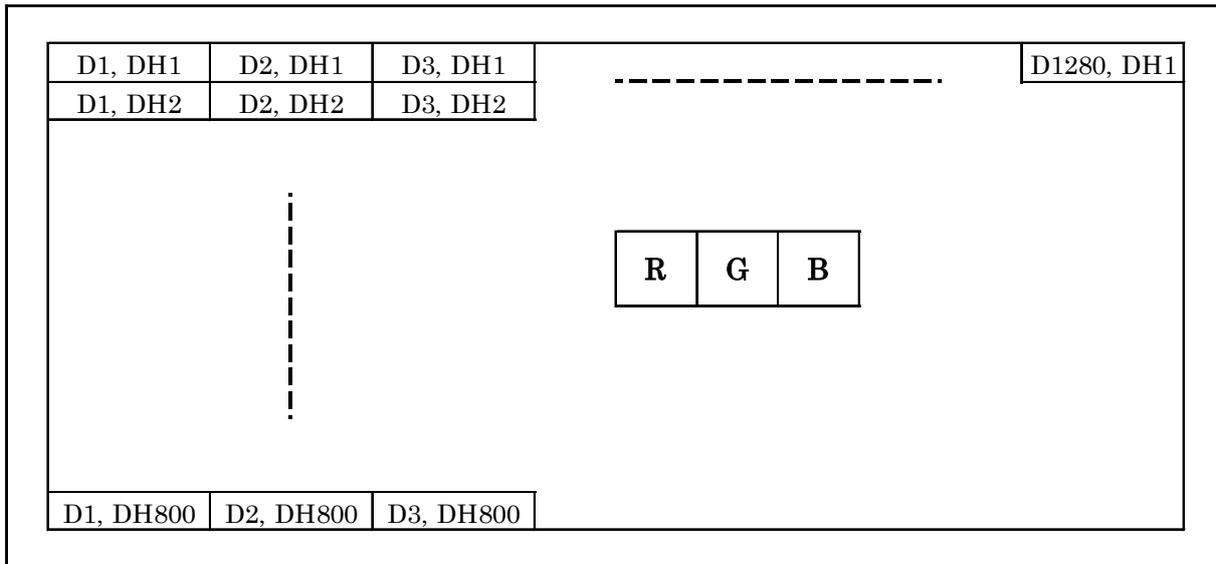
### Vertical Timing Diagram



### Horizontal Timing Diagram



8-2. Input data signals and display position on the screen





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## 11. Precautions for use

### 11-1. Installation of the LCD

- 1) Please ground in order to stabilize brightness and display quality.
- 2) A transparent protection plate shall be added to protect the LCD and its polarizer.
- 3) The LCD shall be installed so that there is no pressure on the LSI chips.
- 4) Since this product is wide viewing product, occurrence level of in-plane unevenness by the external stress is different compared to current normal viewing product. So there is a possibility that in-plane unevenness will be occurred by over twist, strain giving by attaching to LCD, and over pressure to touch panel. Please be careful of stress when designing the housing.
- 5) A transparent protection sheet is attached to the polarizer.  
Please remove the protection film slowly before use, paying attention to static electricity.

### 11-2. Static electricity

- 1) Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required.
- 2) Workers should use body grounding. Operator should wear ground straps.

### 11-3. LCD operation

- 1) The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2) Please select the best display pattern based on your evaluation because flicker, lines or nonuniformity or unevenness can be visible depending on display patterns.

### 11-4. Storage

- 1) The LCD shall be stored within the temperature and humidity limits specified.  
Store in a dark area, and protect the LCD from direct sunlight or fluorescent light.
- 2) Always store the LCD so that it is free from external pressure onto it.

### 11-5. Usage

- 1) **DO NOT** store in a high humidity environment for extended periods. Polarizer degradation bubbles, and/or peeling off of the polarizer may result.
- 2) The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3) The LCD screen may be cleaned by wiping the screen surface with a soft cloth or cotton pad using a little Ethanol.
- 4) Water may cause damage or discoloration of the polarizer. Clean condensation or moisture from any source immediately.
- 5) Always keep the LCD free from condensation during testing. Condensation may permanently spot or stain the polarizer.
- 6) Do not disassemble LCD because it will result in damage.
- 7) This Kyocera LCD has been specifically designed for use in general electronic devices, but not for use in a special environment such as usage in an active gas. Hence, when the LCD is supposed to be used in a special environment, evaluate the LCD thoroughly beforehand and do not expose the LCD to chemicals such as an active gas.
- 8) Please do not use solid-base image pattern for long hours because a temporary afterimage may appear. We recommend using screen saver etc. in cases where a solid-base image pattern must be used.
- 9) Liquid crystal may leak when the LCD is broken. Be careful not to let the fluid go into your eyes and mouth. In the case the fluid touches your body; rinse it off right away with water and soap.

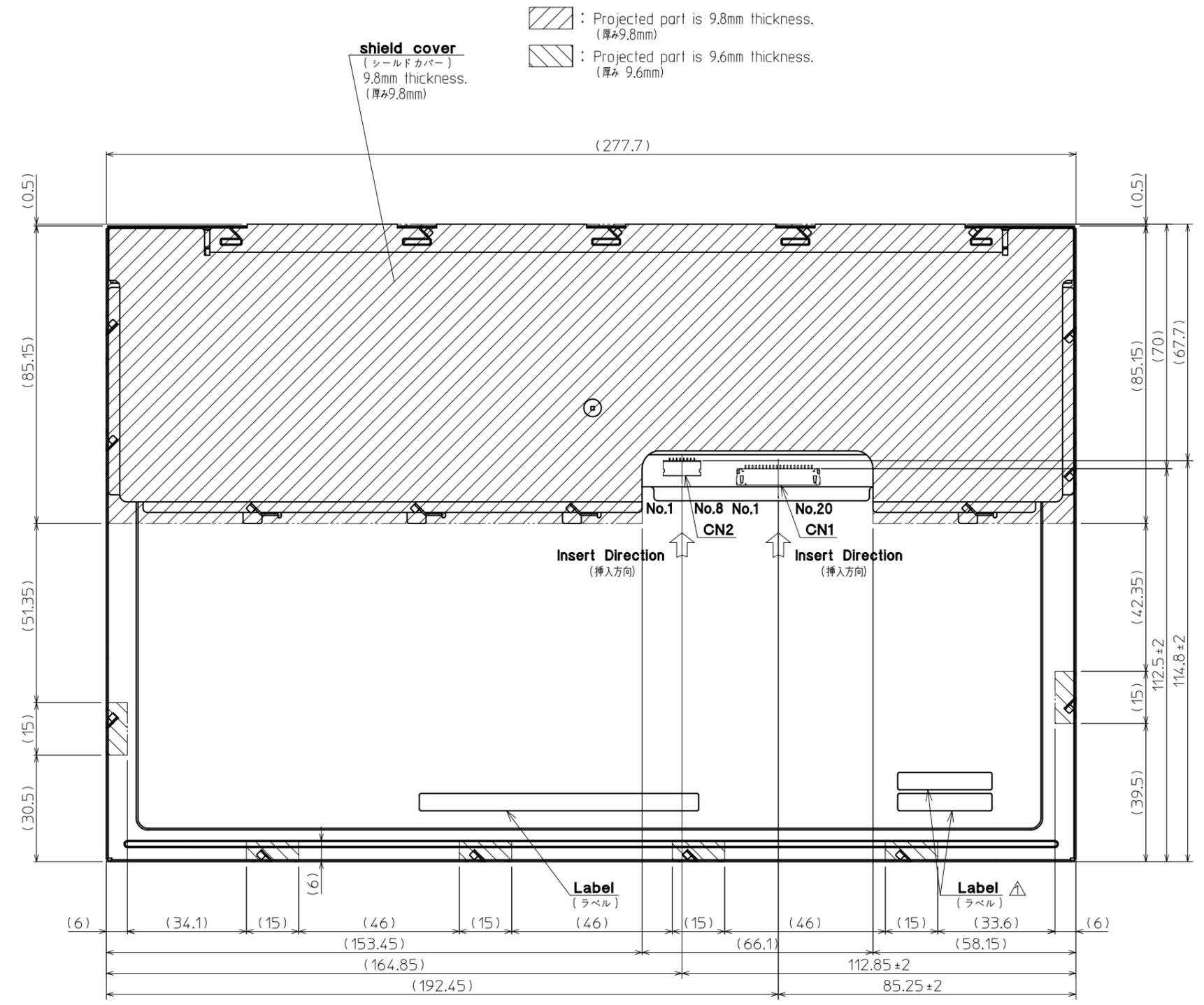
## 12. Reliability test data

Test item	Test condition	Test time	Judgement
High temp. atmosphere	80°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Low temp. atmosphere	-30°C	240h	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. humidity atmosphere	40°C 90% RH	240h	Display function : No defect Display quality : No defect Current consumption : No defect
Temp. cycle	-30°C 0.5h R.T. 0.5h 80°C 0.5h	10cycles	Display function : No defect Display quality : No defect Current consumption : No defect
High temp. operation	70°C	500h	Display function : No defect Display quality : No defect Current consumption : No defect

- 1) Each test item uses a test LCD only once. The tested LCD is not used in any other tests.
- 2) The LCD is tested in circumstances in which there is no condensation.
- 3) The reliability test is not an out-going inspection.
- 4) The result of the reliability test is for your reference purpose only.  
The reliability test is conducted only to examine the LCD's capability.



No	Description	Drawn	Checked	Checked	Approved
△	・Change the position to stick label. (ラベル貼付位置を変更)	'20.06.25 伊原	'20.07.13 倉元		'20.07.14 宇佐美
△	・1/2 Revision up.	'23.04.24 蔭山	'23.04.25 本間		'23.04.25 倉元



: Projected part is 9.8mm thickness.  
(厚み9.8mm)  
 : Projected part is 9.6mm thickness.  
(厚み 9.6mm)

Note. (注記)  
 1. Tolerance without indication : ±0.5  
 (指示無き公差)

Material 材質	Treatment 処理	Approved '14.05.20	Checked	Checked '14.05.20	Drawn 圓福	Scale 1:1	Title TCG121WXLPA PNN		Year-Month-Day '14.05.19	Size 2
Quantity 製作数	Description; 備考	朝倉	倉元	倉元	圓福	Outline Dimensions			Drawing No. 121A8079800-2	2/2

Spec No.	TQ3C-8EAF0-E2YAM51-00
Date	July 12, 2022

**KYOCERA INSPECTION STANDARD**

**TYPE : TCG121WXLPA PNN-AN30**

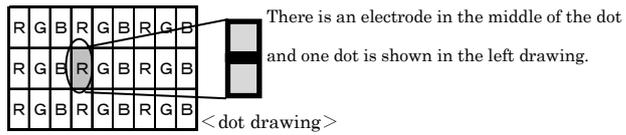
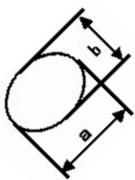
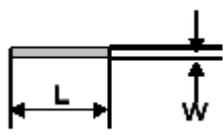
KYOCERA CORPORATION

Original Issue Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved
July 12, 2022	<i>Y. Takakura</i>	<i>I. Kawajiri</i>	<i>A. Iwasaki</i>	<i>Y. Aritsubo</i>	<i>M. Kinouchi</i>



## Visuals specification

### 1) Note

		Note	
General	<p>1. Customer identified anomalies not defined within this inspection standard shall be reviewed by Kyocera, and an additional standard shall be determined by mutual consent.</p> <p>2. This inspection standard about the image quality shall be applied to any defect within the active area and shall not be applicable to outside of the area.</p> <p>3. Inspection conditions</p> <p>Luminance : 500 Lux min.</p> <p>Inspection distance : 300 mm.</p> <p>Temperature : 25 ± 5°C</p> <p>Direction : Directly above</p>		
Definition of inspection item	Dot defect	Bright dot defect	<p>The dot is constantly “on” when power applied to the LCD, even when all “Black” data sent to the screen.</p> <p>Inspection tool: 5% Transparency neutral density filter.</p> <p>Count dot: If the dot is visible through the filter.</p> <p>Don't count dot: If the dot is not visible through the filter.</p> 
		Black dot defect	The dot is constantly “off” when power applied to the LCD, even when all “White” data sent to the screen.
		White dot (Circular/foreign particle)	Pixel works electrically, however, circular/foreign particle makes dot appear to be “on” even when all “Black” data is sent to the screen.
		Adjacent dot	Adjacent dot defect is defined as two or more bright dot defects or black dot defects.
	External inspection	Bubble, Scratch, Foreign particle (Polarizer, Cell, Backlight)	Visible operating (all pixels “Black” or “White”) and non operating.
	Appearance inspection	Does not satisfy the value at the spec.	
Definition of size	<p>Definition of circle size</p>  <p>a: major axis, b: minor axis</p> <p><math>d = (a + b) / 2</math></p>	<p>Definition of linear size</p> 	

2) Standard

Classification		Inspection item		Judgement standard				
Defect (in LCD glass)	Single dot	Bright dot defect		Acceptable number	: 4			
				Bright dot spacing	: 5 mm or more			
			Black dot defect		Acceptable number	: 5		
					Black dot spacing	: 5 mm or more		
	Adjacent dot	2 dots	Bright dot defect		Acceptable number	: 2		
			Black dot defect		Acceptable number	: 3		
		3 or more dots		Acceptable number	: 0			
Total dot defects				Acceptable number	: 5 Max			
Others	White dot, Dark dot (Circle)		Size (mm)		Acceptable number			
			$d \leq 0.2$		(Neglected)			
			$0.2 < d \leq 0.4$		5			
			$0.4 < d \leq 0.5$		3			
			$0.5 < d$		0			
External inspection (Defect on Polarizer or between Polarizer and LCD glass)	Polarizer (Scratch)		Width (mm)		Length (mm)		Acceptable number	
			$W \leq 0.1$		-		(Neglected)	
			$0.1 < W \leq 0.3$		$L \leq 5.0$		(Neglected)	
					$5.0 < L$		0	
			$0.3 < W$		-		0	
	Polarizer (Bubble)		Size (mm)		Acceptable number			
			$d \leq 0.2$		(Neglected)			
			$0.2 < d \leq 0.3$		5			
			$0.3 < d \leq 0.5$		3			
			$0.5 < d$		0			
	Foreign particle (Circular shape)		Size (mm)		Acceptable number			
			$d \leq 0.2$		(Neglected)			
			$0.2 < d \leq 0.4$		5			
			$0.4 < d \leq 0.5$		3			
			$0.5 < d$		0			
Foreign particle (Linear shape) Scratch		Width (mm)		Length (mm)		Acceptable number		
		$W \leq 0.03$		-		(Neglected)		
		$0.03 < W \leq 0.1$		$L \leq 2.0$		(Neglected)		
				$2.0 < L \leq 4.0$		3		
		$0.1 < W$		$4.0 < L$		0		
		-		(According to circular shape)				
Color variation (Mura)				Not to be significantly visible. Consultation shall be held as necessary.				

Document No.	TQ3C-8EAF0-E3YAM51-00
Date	July 12, 2022

**KYOCERA PACKAGING STANDARD**

**TYPE : TCG121WXL PANN-AN30**

KYOCERA CORPORATION

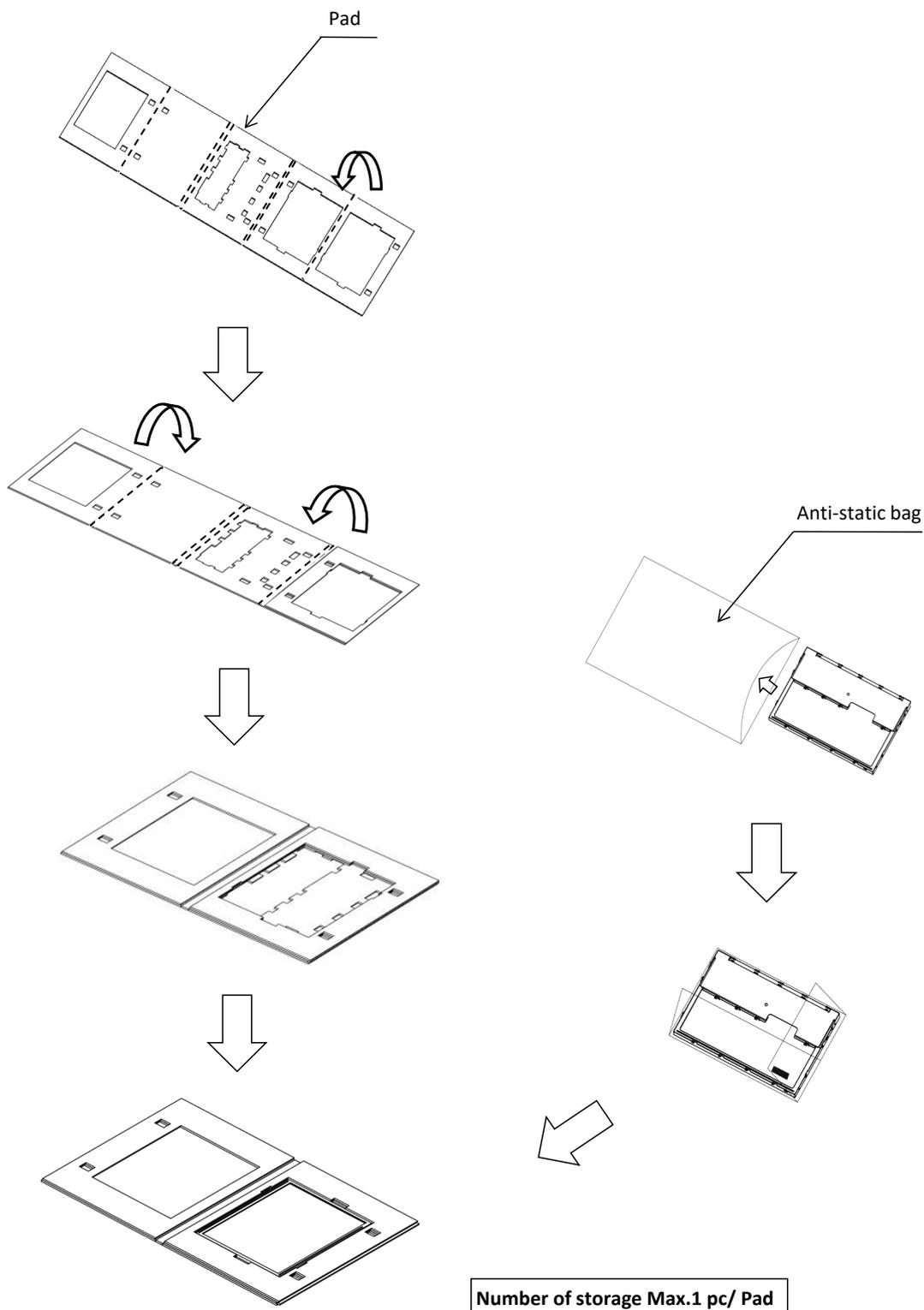
Original Issue Date	Designed by : Engineering dept.			Confirmed by : QA dept.	
	Prepared	Checked	Approved	Checked	Approved
July 12, 2022	<i>Y. Takakura</i>	<i>I. Kawajiri</i>	<i>A. Iwasaki</i>	<i>Y. Aritsubo</i>	<i>M. Kinouchi</i>

**Revision record**

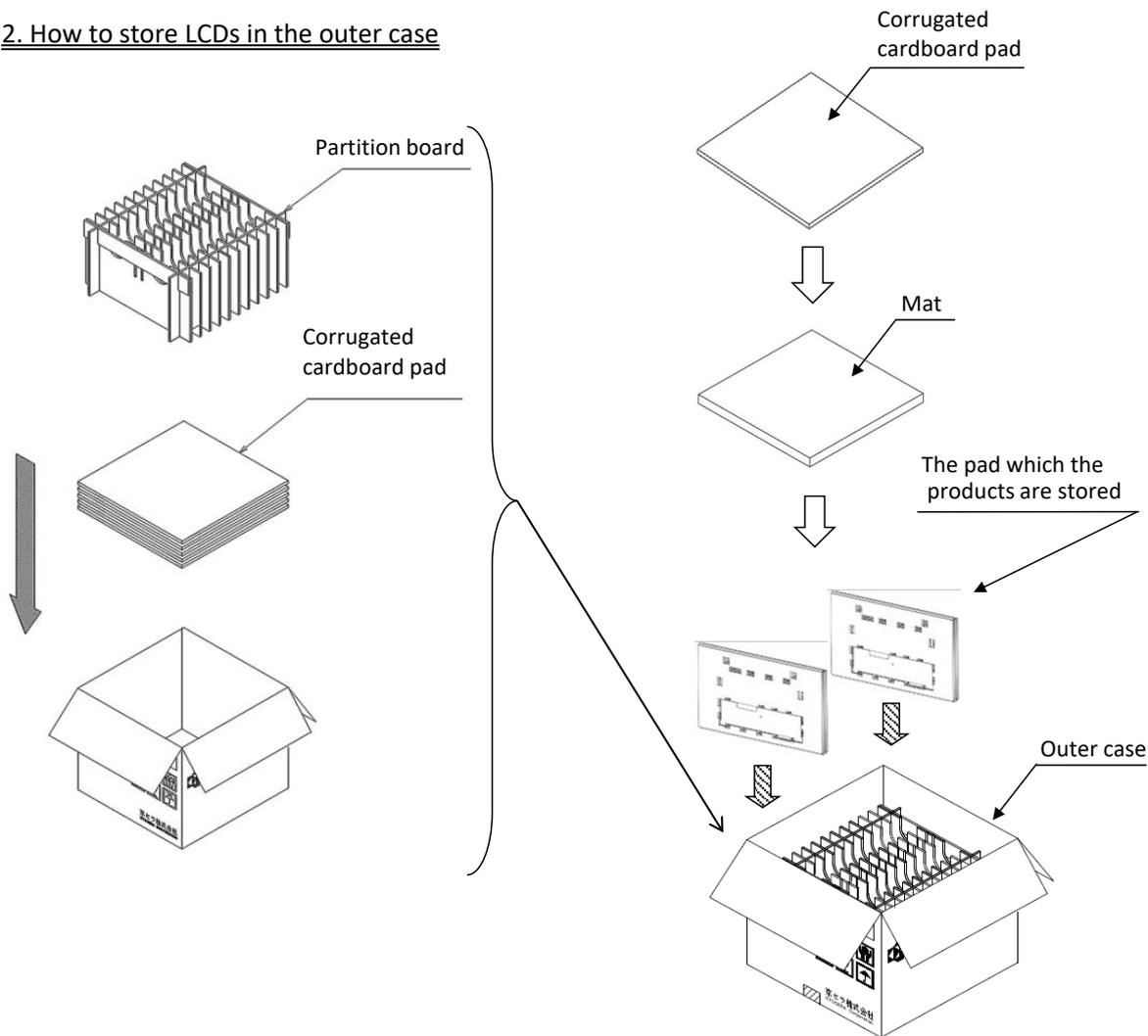
Date	Designed by : Engineering dept.			Confirmed by : QA dept.
	Prepared	Checked	Approved	Approved

RevNo.	Date	Page	Descriptions

1. How to store LCDs in the pad

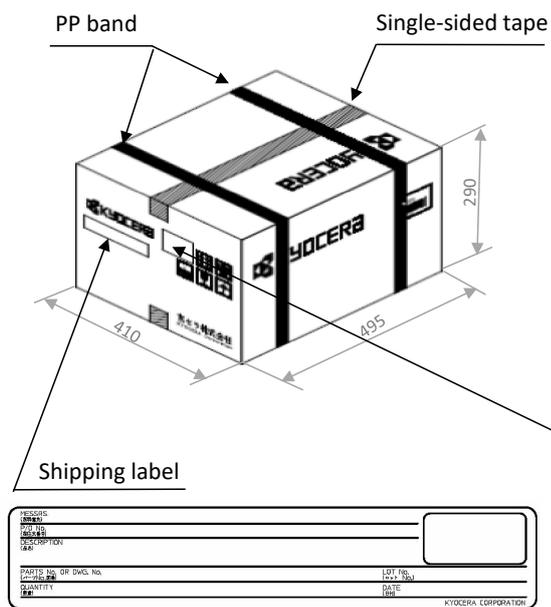


2. How to store LCDs in the outer case



Number of storage Max.10 pcs/ Outer case

3. Location of the labels



客先 (Customer)		京セラ株式会社
品名 (Part No.)		
数量 (QTY)		
検査者 (Check)		
検査者 (Check)		

Our company network supports you worldwide with offices in Germany, Austria, Switzerland, the UK and the USA. For more information please contact:

## Headquarters

Germany



**FORTEC**  
GROUP

**FORTEC Elektronik AG**  
Augsburger Str. 2b  
82110 Germering

Phone: +49 89 894450-0  
E-Mail: [info@fortecag.de](mailto:info@fortecag.de)  
Internet: [www.fortecag.de](http://www.fortecag.de)

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## Fortec Group Members

Austria



**FORTEC**  
INTEGRATED

**Distec GmbH Office Vienna**  
Nuschinggasse 12  
1230 Wien

Phone: +43 1 8673492-0  
E-Mail: [info@distec.de](mailto:info@distec.de)  
Internet: [www.distec.de](http://www.distec.de)

Germany



**FORTEC**  
INTEGRATED

**Distec GmbH**  
Augsburger Str. 2b  
82110 Germering

Phone: +49 89 894363-0  
E-Mail: [info@distec.de](mailto:info@distec.de)  
Internet: [www.distec.de](http://www.distec.de)

Switzerland



**ALTRAC**  
A FORTEC GROUP MEMBER

**ALTRAC AG**  
Bahnhofstraße 3  
5436 Würenlos

Phone: +41 44 7446111  
E-Mail: [info@altrac.ch](mailto:info@altrac.ch)  
Internet: [www.altrac.ch](http://www.altrac.ch)

United Kingdom



**FORTEC**  
UNITED KINGDOM

**Display Technology Ltd.**  
Osprey House, 1 Osprey Court  
Hinchingbrooke Business Park  
Huntingdon, Cambridgeshire, PE29 6FN

Phone: +44 1480 411600  
E-Mail: [info@displaytechnology.co.uk](mailto:info@displaytechnology.co.uk)  
Internet: [www.displaytechnology.co.uk](http://www.displaytechnology.co.uk)

USA



 **APOLLO DISPLAY  
TECHNOLOGIES**  
A FORTEC GROUP MEMBER

**Apollo Display Technologies, Corp.**  
87 Raynor Avenue, Unit 1  
Ronkonkoma, NY 11779

Phone: +1 631 5804360  
E-Mail: [info@apolloDisplays.com](mailto:info@apolloDisplays.com)  
Internet: [www.apolloDisplays.com](http://www.apolloDisplays.com)