













Datasheet

Tianma

NL13676BC25-03F

15.6" TFT Display

NL-60-121

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TFT COLOR LCD MODULE

NL13676BC25-03F

40cm (15.6 Type) WXGA (1366×768) LVDS Interface (1port)



DOD-PP-2566 (2nd edition)

This DATA SHEET is updated document from DOD-PP-2000(1).

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INTRODUCTION

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Examples: Aerospace system (except seat entertainment monitor), nuclear control system, life support system, etc.

The quality grade of this product is the "Standard" unless otherwise specified in this document.

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1. OUTLINE

1.1 STRUCTURE AND PRINCIPLE

Color LCD module NL13676BC25-03F is composed of the amorphous silicon thin film transistor liquid crystal display (a-Si TFT LCD) panel structure with driver LSIs for driving the TFT (Thin Film Transistor) array and a backlight.

The a-Si TFT LCD panel structure is injected liquid crystal material into a narrow gap between the TFT array glass substrate and a color-filter glass substrate.

Color (Red, Green, Blue) data signals from a host system (e.g. signal generator, etc.) are modulated into best form for active matrix system by a signal processing board, and sent to the driver LSIs which drive the individual TFT arrays.

The TFT array as an electro-optical switch regulates the amount of transmitted light from the backlight assembly, when it is controlled by data signals. Color images are created by regulating the amount of transmitted light through the TFT array of red, green and blue dots.

1.2 APPLICATIONS

• For industrial use

1.3 FEATURES

- High luminance
- High contrast
- LVDS interface
- Selectable 8-bit or 6-bit digital signals for data of RGB
- LED backlight
- Acquisition product for UL60950-1/CSA C22.2 No.60950-1-03 (File number: E170632)
- Compliant with the European RoHS directive (2011/65/EU)

2. GENERAL SPECIFICATIONS

Display area	344.232 (H) × 193.536 (V) mm
Diagonal size of display	40cm (15.6 inches)
Drive system	a-Si TFT active matrix
Display color	16,777,216 colors (At 8-bit input, FRC terminal= Low or Open) 262,144 colors (At 6-bit input, FRC terminal= High)
Pixel	1,366 (H) × 768 (V) pixels
Pixel arrangement	RGB (Red dot, Green dot, Blue dot) vertical stripe
Dot pitch	$0.084 (H) \times 0.252 (V) mm$
Pixel pitch	$0.252 (H) \times 0.252 (V) mm$
Module size	363.8 (W) × 215.9 (H) × 12.65 (D) mm (typ.)
Weight	1,070g (typ.)
Contrast ratio	900:1 (typ.)
Viewing angle	 At the contrast ratio ≥10:1 Horizontal: Right side 80° (typ.), Left side 80° (typ.) Vertical: Up side 80° (typ.), Down side 80° (typ.)
Designed viewing direction	 Viewing direction without image reversal: Up side (12 o'clock) Viewing direction with contrast peak: Down side (6 o'clock) Viewing angle with optimum grayscale (γ≒ 2.2): Normal axis (perpendicular)
Polarizer surface	Antiglare
Polarizer pencil-hardness	3H (min.) [by JIS K5600]
Color gamut	At LCD panel center 60% (typ.) [against NTSC color space]
Response time	$Ton + Toff (10\% \leftrightarrow 90\%)$ 18ms (typ.)
Luminance	At IL= $90mA/One \ circuit$ 1,100cd/m ² (typ.)
Signal system	LVDS interface (1port) (Receiver: THC63LVDF84B, THine Electronics Inc. or equivalent) [8-bit/6-bit digital signals for data of RGB colors, Dot clock (CLK), Data enable (DE)]
Power supply voltage	LCD panel signal processing board: 3.3V
Backlight	LED backlight
Power consumption	At IL= 90mA/One circuit, Checkered flag pattern 16W (typ.)

3. BLOCK DIAGRAM



Note1: Relation between GND (Signal ground) and FG (Frame ground) in the LCD module is as follows.

 GND-FG
 Connected

Note2: GND and FG must be connected to customer equipment's ground, and it is recommended that these grounds to be connected together in customer equipment.

Note3: Backlight in detail



Backlight

4. DETAILED SPECIFICATIONS

4.1 MECHANICAL SPECIFICATIONS

Parameter	Specification		Unit
Module size	$363.8 \pm 0.5 \text{ (W)} \times 215.9 \pm 0.5 \text{ (H)} \times 12.65 \pm 0.5 \text{ (D)}$	Note1	mm
Display area	344.232 (H) × 193.536 (V)	Note1	mm
Weight	1,070 (typ.), 1,170 (max.)		g

Note1: See "8. OUTLINE DRAWINGS".

4.2 ABSOLUTE MAXIMUM RATINGS

	Parameter		Symbol	Rating	Unit	Remarks
Power supply voltage	LCD panel signal	processing board	VCC	-0.3 to +4.0	V	
Input voltage	Display Not		VD	-0.3 to 1.98	V	Ta= 25°C
for signals	Function Not		VF	-0.3 to +4.0	v	
Backlight	Forward	current	IL	100	mA	per one circuit
:	Storage temperature		Tst	-30 to +80	°C	-
Operating	tommonotumo	Front surface	TopF	-20 to +70	°C	Note3
Operating	temperature	Rear surface	TopR	-20 to +70	°C	Note4
				≤ 95	%	$Ta \leq 40^{\circ}C$
	Relative humidity		RH	≤ 85	%	$40^{\circ}C < Ta \leq 50^{\circ}C$
	Note5		КП	≤ 55	%	$50^{\circ}C < Ta \le 60^{\circ}C$
				≤ 36	%	$60^{\circ}C < Ta \le 70^{\circ}C$
	Absolute humidity Note5		AH	≤70 Note6	g/m ³	Ta > 70°C

Note1: D0+/-, D1+/-, D2+/-, D3+/-, CLK+/-

Note2: FRC, MSL

Note3: Measured at LCD panel surface (including self-heat) Note4: Measured at LCD module's rear shield surface (including self-heat)

Note5: No condensation

Note6: Water amount at $Ta = 70^{\circ}C$ and RH = 36%

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4.3 ELECTRICAL CHARACTERISTICS

4.3.1 LCD panel signal processing board

The puller signal proce	Jobing C	oura					(Ta=25°C)
Parameter		Symbol	min.	typ.	max.	Unit	Remarks
Power supply voltage		VCC	3.0	3.3	3.6	V	-
Power supply current		ICC	-	480 Note1	800 Note2	mA	at VCC= 3.3V
Permissible ripple voltage		VRPC	-	-	300	mVp-p	for VCC
Differential input threshold voltage	High	VTH	-	-	+100	mV	at VCM= 1.25V
	Low	VTL	-100	-	-	mV	Note3
Terminating resistance		RT	-	100	-	Ω	-
Input voltage for	High	VFH	2.25	-	VCC	V	
FRC and MSL signals	Low	VFL	0	-	0.40	v	-
Input current for	High	IFH	-	-	500	μΑ	
FRC and MSL signals	Low	IFL	0	-	-	μΑ	-

Note1: Checkered flag pattern [by IEC 61747-6] Note2: Pattern for maximum current

Note3: Common mode voltage for LVDS receiver

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4.3.2 Backlight

					(Note	e1, Note2, Note3)
Parameter	Symbol	min.	typ.	max.	Unit	Remarks
Forward current	IL	-	90.0	95.0	mA	Ta= 25°C
Forward Voltage		24.4	27.1	29.8		Ta= +25°C at IL= 90 mA /One circuit
	VL	23.2	-	-	v	Ta= +70°C at IL= 90 mA /One circuit
	VL.	-	-	31.4	v	Ta= -20°C at IL= 90 mA /One circuit
		-	-	31.5		Ta= -20°C at IL= 95 mA /One circuit

Note1: Please drive with constant current.

Note2: The above specifications are for one LED circuit of the backlight.

Note3: The luminance uniformity may be changed depending on the current variation between 6 circuits. It is recommended that the current value difference amongst the circuits be less than 5%.

4.3.3 Power supply voltage ripple

This product works even if the ripple voltage levels are over the permissible values as the following table, but there might be noise on the display image.

Power sup	ply voltage	Ripple voltage Note1 (Measure at input terminal of power supply)	Unit
VCC	3.3V	≤ 300	mVp-p

Note1: The permissible ripple voltage includes spike noise.

4.3.4 Fuse

Demonster	Fu	ise	Datina	Easter annual	Dementer
Parameter	Туре	Supplier	Rating	Fusing current	Remarks
VCC	FCC16152AB	KAMAYA	1.5A	3.0A	Note1
VCC	FCC10152AB	ELECTRIC Co., Ltd.	36V	3.0A	note1

Note1: The power supply's rated current must be more than the fusing current. If it is less than the fusing current, the fuse may not blow in a short time, and then nasty smell, smoke and so on may occur.

4.4 POWER SUPPLY VOLTAGE SEQUENCE

4.4.1 LCD panel signal processing board



^{*} These signals should be measured at the terminal of 100Ω resistance.

- Note1: If there is a voltage variation (voltage drop) at the rising edge of VCC below 3.0V, there is a possibility that a product does not work due to a protection circuit.
- Note2: Display signals (D0+/-, D1+/-, D2+/-, D3+/- and CLK+/-) and function signals (FRC and MSL) must be set to Low or High-impedance, except the VALID period (See above sequence diagram), in order to avoid the circuitry damage.
 If some of display and function signals of this product are cut while this product is working,

even if the signal input to it once again, it might not work normally. If a customer stops the display and function signals, VCC also must be shut down.

4.4.2 LED driver



- Note1: These are the display and function signals for LCD panel signal processing board.
- Note2: The backlight should be turned on within the VALID period of display and function signals, in order to avoid unstable data display.

4.5 CONNECTIONS AND FUNCTIONS FOR INTERFACE PINS

4.5.1 LCD panel signal processing board

CN1 socket (LCD module side): 185083-20121 (P-TWO ELECTRIC TECHNOLOGY CO., LTD.) Adaptable plug: DF14-20S-1.25C (Hirose Electric Co., Ltd. (HRS))

Pin No.	Symbol	Signal	Input data s		Input data signal:	Remarks							
FIII NO.	Symbol	Sigilar	MAP A	MAP B	6-bit	Kemarks							
1	VCC												
2	VCC	Power supply		Power supply		Note1							
3	GND	Ground		Ground									
4	N.C.	-		Keep this pin Ope	en.	-							
5	D0-	D		DO		N							
6	D0+	Pixel data	R2-R7, G2	R5, G0	Note2								
7	GND	Ground		Ground									
8	D1-	D			- D0 D1	N							
9	D1+	Pixel data	G3-G7, B2-B3	GI-G	5, B0-B1	Note2							
10	GND	Ground		Ground		Note1							
11	D2-	D' 11.		DO	B5, DE								
12	D2+	Pixel data	B4-B7, DE	Note2									
13	GND	Ground		Ground		Note1							
14	CLK-			D. 1 1 1									
15	CLK+	Pixel clock		Pixel clock		Note2							
16	GND	Ground		Ground		Note1							
17	D3- / GND	Pixel data	R0-R1	R6-R7		N. C							
18	D3+ / GND	/ Ground	G0-G1 B0-B1	G6-G7 B6-B7	Ground	Note2							
19	FRC	Selection of the number of colors	Low of	r Open	High	-							
20	MSL	Selection of LVDS input map	High	Low or Open	High	Note3 Note4							

Note1: All GND and VCC terminals should be used without any non-connected lines.

Note2: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.

Note3: See "4.5.4 Connection between receiver and transmitter for LVDS".

Note4: See "4.6 DISPLAY COLORS AND INPUT DATA SIGNALS".

4.5.2 Backlight

CN2 socket (LCD module side): SM10B-SHLS-TF(LF)(SN) (J.S.T. Mfg. Co., Ltd.) Adaptable plug: SHLP-10V-S-B (J.S.T. Mfg. Co., Ltd.)

Adaptable plug.		511LI -10 V-5-D	(J.J.I. Mig. CO., Ltd.)
Pin No.	Symbol	Signal	Remarks
1	A1	Anode1	-
2	A2	Anode2	-
3	A3	Anode3	-
4	A4	Anode4	-
5	K1	Cathode1	-
6	K2	Cathode2	-
7	K3	Cathode3	-
8	K4	Cathode4	-
9	K5	Cathode5	-
10	K6	Cathode6	-

4.5.3 Position of socket





4.5.4 Connection between receiver and transmitter for LVDS

(1) Input data signal: 8-bit, MAP A (FRC: Low or Open, MSL: High)

Note1: Recommended transmitter: THC63LVDM83C(5S) (THine Electronics Inc.) or equivalent

Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7

- Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
- Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.

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(2) Input data signal: 8-bit, MAP B (FRC: Low or Open, MSL: Low or Open)



Note2: LSB (Least Significant Bit) - R0, G0, B0 MSB (Most Significant Bit) - R7, G7, B7

- Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
- Note4: Input signals to TC4, TC5 and TD6 are not used inside the product, but do not keep them open to avoid noise problem.

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(3) Input data signal: 6-bit (FRC: High, MSL: High)

- Note1: Recommended transmitter: THC63LVDM83C(5S) (THine Electronics Inc.) or equivalent
- Note2: LSB (Least Significant Bit) R0, G0, B0 MSB (Most Significant Bit) R5, G5, B5
- Note3: Twist pair wires with 100Ω (Characteristic impedance) should be used between LCD panel signal processing board and LVDS transmitter.
- Note4: Input signals to TC4, TC5 and TD0-6 are not used inside the product, but do not keep them open to avoid noise problem.

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4.5.5 Input data mapping

(1) Input data signal: 8-bit, MAP A



(2) Input data signal: 8-bit, MAP B



(3) Input data signal: 6-bit



4.6 DISPLAY COLORS AND INPUT DATA SIGNALS

4.6.1 Combinations of input data signals, FRC and MSL signal

This product can display equivalent of 16,777,216 colors and 262,144 colors by combination of input data signals, FRC and MSL signal. See the following table.

Combination	Input data signals	Input data CN1- mapping Pin No.17 and 13		FRC terminal	MSL terminal	Display colors	Remarks
1	8-bit	MAP A	D3+/-	Low or Open	High	16,777,216	Note1
2	8-bit	MAP B	D3+/-	Low or Open	Low or Open	16,777,216	Note1
3	6-bit	-	GND	High	High	262,144	Note2

Note1: See "4.6.2 16,777,216 colors".

Note2: See "4.6.3 262,144 colors".

4.6.2 16,777,216 colors

This product can display equivalent of 16,777,216 colors with 256 gray scales by combination ① or ②. (See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".) Also the relation between display colors and input data signals is as follows.

D' 1	1								Dat	a sig	nal	(0: I	Low	leve	el, 1:	: Hig	gh le	vel)							
Display	/ colors	R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Ors	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Basic Colors	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
Sic	Green	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Ba	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
е		0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	↑ ,				:	:								:								:			
d gr	\downarrow				:	:								:								:			
Re	bright	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	D 1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
y sc	dark	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
gra.	↑ 													:								:			
Green gray scale	↓ ↓ · ↓ .	0	0	0	0	:	0	0	0	1	1	1	1	:	1	0	1	0	0	0	0	:	0	0	0
Gre	bright	0 0	0	1	1	1	1	1	1	0 1	1 0	0 0													
	Green	0	0	0	0	0	0	0	0 0	1	1 1	1	1	1 1	1	1	1	0	0	0	0	0	0	0	0
		0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
ale	-اب -ا	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
/ sc	dark ↑	0	U	U	U.	. 0	U	U	U	0	U	U	U	. 0	0	U	U	0	U	U	U	. 0	U	1	U
Blue gray scale						•																			
ne (↓ bright	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
BI	origin	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

4.6.3 262,144 colors

This product can display 262,144 colors with 64 gray scales by combination ③. (See "**4.6.1 Combinations of input data signals, FRC and MSL signal**".) Also the relation between display colors and input data signals is as follows.

Display colors							Dat	a sign	al (0:	Low	level	, 1: H	igh le	vel)					
Display	/ colors	R 5	R4	R 3	R 2	R 1	R 0	G5	G4	G3	G2	G1	G 0	B 5	B4	B 3	B 2	B 1	B 0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
ors	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
Basic colors	Magenta	1	1	1	1	1	1	0	0	0	0	0	0	1	1	1	1	1	1
Isic	Green	0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
$\mathbf{B}a$	Cyan	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
e		0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0
scal	dark	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0
Red gray scale	1			:	:						:						:		
d gr	\downarrow			:	:						:						:		
Red	bright	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
		1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ale		0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
/ sc	dark	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
gray	↑			:							:						:		
Green gray scale	\downarrow	0	0		:	0	0				:	0		0	0	0	:	0	0
Gre	bright	0	0	0	0	0	0	1	1	1	1	0	1	0	0	0	0	0	0
•	Green	0	0	0	0	0	0	1	1	1	1	1	0	0	0	0	0	0	0
		0	0	0	0	0	0	1	1	1	1	1	1	0	0	0	0	0	0
	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Blue gray scale		0 0	0	0	0 0	0	0	0	0	0 0	0	0	0	0	0 0	0	0	0	1 0
	dark ≁	0	0	0	. 0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	↑ I																		
	↓ 1	0	0	0	. 0	0	0	0	0	0	. 0	0	0	1	1	1	: 1	0	1
Blı	bright	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0
	Blue	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1
	2.40	Ŭ	ÿ	5	3	v	0	Ÿ	ÿ	5	0	0	v	-	-	-			-

4.7 DISPLAY POSITIONS

The following table is the coordinates per pixel.

C (0,	0) B					
(C(0, 0))	C(1, 0)	• • •	C(X, 0)	• • •	C(1364, 0)	C(1365, 0)
C(0, 1)	C(1, 1)	• • •	C(X, 1)	• • •	C(1364, 1)	C(1365, 1)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	• • •
•	•	•	•	•	•	•
C(0, Y)	C(1, Y)	• • •	C(X, Y)	• • •	C(1364, Y)	C(1365, Y)
•	•	•	•	•	•	•
•	•	• • •	•	• • •	•	•
•	•	•	•	•	•	•
C(0, 766)	C(1,766)	• • •	C(X, 766)	• • •	C(1364, 766)	C(1365, 766)
C(0, 767)	C(1, 767)	• • •	C(X, 767)	• • •	C(1364, 767)	C(1365, 767)

4.8 INPUT SIGNAL TIMINGS

4.8.1 Outline of input signal timings

• Horizontal signal

Note1



Note1: This diagram indicates virtual signal for set up to timing. Note2: See "**4.8.3 Input signal timing chart**" for the pulse number.

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4.8.2 Timing characteristics

1.0.2 111		,ues					(Note	1, Note2, Note3)	
	Paramete	er	Symbol	min.	typ.	max.	Unit	Remarks	
	Free	quency	1/tc	71.0	75.4	79.8	MHz	13.263ns (typ.)	
CLK	Dut	y ratio	-				-		
	Rise tim	-		-	ns	-			
	CLK-DATA	Setup time	-	-			ns		
DATA	CLK-DATA	Hold time	-				ns	-	
	Rise time, Fall time		-				ns		
		Cycle	th	16.542	20.678	26.88	μs	48.360kHz (typ.)	
	Horizontal	Cycle	ui	1,446	1,560	1,936	CLK	48.500KHZ (typ.)	
		Display period	thd	1,366			CLK	-	
	T7 .1 1		tv	14.29	16.67	20.00	ms	60.0Hz (typ.)	
DE	Vertical (One frame)	Cycle	ιv	778	806	-	Н	00.0HZ (typ.)	
	(One frame)	Display period	tvd	768			Н	-	
	CLK-DE	Setup time	-				ns		
	CLK-DE	Hold time	-	-			ns	-	
	Rise time, Fall time		-	1			ns		

Note1: Definition of parameters is as follows.

tc = 1CLK, th = 1H

Note2: See the data sheet of LVDS transmitter.

Note3: Vertical cycle (tv) should be specified in integral multiple of Horizontal cycle (th).

4.8.3 Input signal timing chart

Horizontal timing



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4.9 OPTICS

4.9.1 Optical characteristics

-								(Note1,	Note2)
Paramete	r	Condition	Symbol	min.	typ.	max.	Unit Measuri		Remarks
Luminanc	e	White at center $\theta R=0^\circ$, $\theta L=0^\circ$, $\theta U=0^\circ$, $\theta D=0^\circ$	L	800	1,100	-	cd/m ²	BM-5A	-
Contrast ra	tio	White/Black at center $\theta R=0^{\circ}, \ \theta L=0^{\circ}, \ \theta U=0^{\circ}, \ \theta D=0^{\circ}$	CR	540	900	-	-	BM-5A	Note3
Luminance unit	formity	White $\theta R=0^\circ, \ \theta L=0^\circ, \ \theta U=0^\circ, \ \theta D=0^\circ$	LU	-	1.25	1.4	-	BM-5A	Note4
	White	x coordinate	Wx	0.263	0.313	0.363	-		
	white	y coordinate	Wy	0.279	0.329	0.379	-		
	Red	x coordinate	Rx	-	0.631	-	-		
Chromaticity		y coordinate	Ry	-	0.357	-	-		
Chromatienty	Green	x coordinate	Gx	-	0.344	-	-	SR-3	Note5
		y coordinate	Gy	-	0.608	-	-	5K-5	Notes
	Blue	x coordinate	Bx	-	0.153	-	-		
	Diuc	y coordinate	By	-	0.089	-	-		
Color gam	ut	$\theta R=0^{\circ}, \ \theta L=0^{\circ}, \ \theta U=0^{\circ}, \ \theta D=0^{\circ}$ at center, against NTSC color space	С	55	60	-	%		
Desponse t	ma	White to Black	Ton	-	3	5	ms	BM-5A	Note6
Response ti	me	Black to White	Toff	-	15	21	ms	-10000	Note7
	Right	$\theta U=0^{\circ}, \ \theta D=0^{\circ}, \ CR\geq 10$	θR	70	80	-	0		
Viewing angle	Left	$\theta U=0^{\circ}, \ \theta D=0^{\circ}, \ CR\geq 10$	θL	70	80	-	0	EZ	Note8
viewing angle	Up	$\theta R = 0^{\circ}, \ \theta L = 0^{\circ}, \ CR \ge 10$	θU	70	80	-	0	Contrast	INDIEO
	Down	$\theta R=0^{\circ}, \ \theta L=0^{\circ}, \ CR\geq 10$	θD	70	80	$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	0		

Note1: These are initial characteristics.

Note2: Measurement conditions are as follows.

Ta= 25°C, VCC= 3.3V, IL= 90mA/One circuit, Display mode: WXGA,

Horizontal cycle= 1/48.360kHz, Vertical cycle= 1/60.0Hz, FRC= Low or Open

Optical characteristics are measured at luminance saturation 20minutes after the product works in the dark room. Also measurement methods are as follows.



- Note3: See "4.9.2 Definition of contrast ratio".
- Note4: See "4.9.3 Definition of luminance uniformity".
- Note5: These coordinates are found on CIE 1931 chromaticity diagram.
- Note6: Product surface temperature: $TopF= 35^{\circ}C$
- Note7: See "4.9.4 Definition of response times".
- Note8: See "4.9.5 Definition of viewing angles".

4.9.2 Definition of contrast ratio

The contrast ratio is calculated by using the following formula.

Contrast ratio (CR) = Luminance of white screen Luminance of black screen

4.9.3 Definition of luminance uniformity

The luminance uniformity is calculated by using following formula.

 $Luminance uniformity (LU) = \frac{Maximum luminance from (1) to (9)}{Minimum luminance from (1) to (9)}$

The luminance is measured at near the 9 points shown below.

		◀	— н —	
		H/10	H/2	H/10
	V/10	0	@	3
 V	V/2	4	(5)	©
	V/10	 		

4.9.4 Definition of response times

Response time is measured at the time when the luminance changes from " white " to " black ", or " black " to " white " on the same screen point, by photo-detector. Ton is the time when the luminance changes from 90% down to 10%. Also Toff is the time when the luminance changes from 10% up to 90% (See the following diagram.).







5. ESTIMATED LUMINANCE LIFETIME

The luminance lifetime is the time from initial luminance to half-luminance.

This lifetime is the estimated value, and is not guarantee value.

	Estimated luminance lifetime (Life time expectancy) Note1, Note2, Note3	Unit	
LED elementary substance	25°C (Ambient temperature of the product) Continuous operation, IL= 90mA/One circuit	100,000	
	70°C (Temperature at LCD panel surface and rear shield surface) Continuous operation, IL= 90mA/One circuit	70,000	h

Note1: Life time expectancy is mean time to half-luminance.

Note2: Estimated luminance lifetime is not the value for LCD module but the value for LED elementary substance.

Note3: By ambient temperature, the lifetime changes particularly. Especially, in case the product works under high temperature environment, the lifetime becomes short.

6. RELIABILITY TESTS

Test item	Condition	Judgment	Note1	
High temperature and humidity (Operation)	 60 ± 2°C, RH= 90%, 240hours Display data is black. 			
High temperature (Operation)	 70 ± 3°C, 240hours Display data is black. 			
Heat cycle (Operation)	 (1) -20 ± 3°C1hour 70 ± 3°C1hour (2) 50cycles, 4 hours/cycle (3) Display data is Black. 			
Thermal shock (Non operation)	 30 ± 3°C30minutes 80 ± 3°C30minutes 100cycles, 1hour/cycle Temperature transition time is within 5 minutes. 	No display malfunctions		
ESD (Operation)	 150pF, 150Ω, ±10kV 9 places on a panel surface Note2 10 times each place at 1 sec interval 			
Dust (Operation)	 Sample dust: No. 15 (by JIS-Z8901) 15 seconds stir 8 times repeat at 1 hour interval 			
Vibration (Non operation)	e			
Mechanical shock (Non operation)	 ① 294m/s², 11ms ② ±X, ±Y, ±Z directions ③ 3 times each direction 	 No physical damages 		

Note1: Display and appearance are checked under environmental conditions equivalent to the inspection conditions of defect criteria.

Note2: See the following figure for discharge points.



7. PRECAUTIONS

7.1 MEANING OF CAUTION SIGNS

The following caution signs have very important meaning. **Be sure to read "7.2 CAUTIONS" and "7.3 ATTENTIONS"!**



This sign has the meaning that a customer will be injured or the product will sustain damage if the customer practices wrong operations.



This sign has the meaning that a customer will be injured if the customer practices wrong operations.

7.2 CAUTIONS

* Do not shock and press the LCD panel and the backlight! There is a danger of breaking, because they are made of glass. (Shock: Equal to or no greater than 294m/s² and equal to or no greater than 11ms, Pressure: Equal to or no greater than 19.6 N $(\phi 16 \text{mm jig}))$



7.3.1 Handling of the product

- ① Take hold of both ends without touching the circuit board when the product (LCD module) is picked up from inner packing box to avoid broken down or misadjustment, because of stress to mounting parts on the circuit board.
- ② Do not hook nor pull cables such as lamp cable, and so on, in order to avoid any damage.
- ③ When the product is put on the table temporarily, display surface must be placed downward.
- ④ When handling the product, take the measures of electrostatic discharge with such as earth band, ionic shower and so on, because the product may be damaged by electrostatic.
- (5) The torque for product mounting screws must never exceed 0.343N·m. Higher torque might result in distortion of the bezel. And the length of product mounting screws must be ≤ 2.8 mm.
- (6) The product must be installed using mounting holes without undue stress such as bends or twist (See outline drawings). And do not add undue stress to any portion (such as bezel flat area). Bends or twist described above and undue stress to any portion may cause display mura.
- ⑦ Do not press or rub on the sensitive product surface. When cleaning the product surface, wipe it with a soft dry cloth.
- ^③ Do not push or pull the interface connectors while the product is working.
- When handling the product, use of an original protection sheet on the product surface (polarizer) is
 recommended for protection of product surface. Adhesive type protection sheet may change color
 or characteristics of the polarizer.
- ① Usually liquid crystals don't leak through the breakage of glasses because of the surface tension of thin layer and the construction of LCD panel. But, if you contact with liquid crystal by any chance, please wash it away with soap and water.

7.3.2 Environment

- ① Do not operate or store in high temperature, high humidity, dewdrop atmosphere or corrosive gases. Keep the product in packing box with antistatic pouch in room temperature to avoid dusts and sunlight, when storing the product.
- ② In order to prevent dew condensation occurred by temperature difference, the product packing box must be opened after enough time being left under the environment of an unpacking room. Evaluate the storage time sufficiently because dew condensation is affected by the environmental temperature and humidity. (Recommended leaving time: 6 hours or more with the original packing state after a customer receives the package)
- ③ Do not operate in high magnetic field. If not, circuit boards may be broken.
- ④ This product is not designed as radiation hardened.

7.3.3 Characteristics

The following items are neither defects nor failures.

- ① Characteristics of the LCD (such as response time, luminance, color uniformity and so on) may be changed depending on ambient temperature. If the product is stored under condition of low temperature for a long time, it may cause display mura. In this case, the product should be operated after enough time being left under condition of operating temperature.
- ② Display mura, flickering, vertical streams or tiny spots may be observed depending on display patterns.
- ③ Do not display the fixed pattern for a long time because it may cause image sticking. Use a screen saver, if the fixed pattern is displayed on the screen.
- (4) The display color may be changed depending on viewing angle because of the use of condenser sheet in the backlight.
- ⑤ Optical characteristics may be changed depending on input signal timings.

7.3.4 Others

- ① All VCC and GND terminals should be used without any non-connected lines.
- ② Do not disassemble a product or adjust variable resistors.
- ③ Pack the product with the original shipping package, in order to avoid any damages during transportation, when returning the product to NLT.
- ④ The information of China RoHS (II) six hazardous substances or elements in this product is as follows.

	China RoHS (II) six hazardous substances or elements									
Lead (Pb)	Mercury (Hg)	Cadmium (Cd)	Hexavalent Chromium (Cr VI)	Polybrominated Biphenys (PBB)	Polybrominated Biphenyl Ethers (PBDE)					
×	0	0	0	0	0					

Note1: O: This indicates that the poisonous or harmful material in all the homogeneous materials for this part is equal or below the limitation level of GB/T26572-2011 standard regulation.

 \times : This indicates that the poisonous or harmful material in all the homogeneous materials for this part is above the limitation level of GB/T26572-2011 standard regulation.

☆

8. OUTLINE DRAWINGS

8.1 FRONT VIEW





Note1: The values in parentheses are for reference.

Note2: The torque for product mounting screws must never exceed 0.343N·m.

The length of product mounting screws from surface of plate must be ≤ 2.8 mm.

Note3: Module depth excludes projection such as hook, and so on.





Unit: mm

8.2 REAR VIEW



Note2: The torque for product mounting screws must never exceed 0.343N·m.

The length of product mounting screws from surface of plate must be ≤ 2.8 mm.

Note3: Module depth excludes projection such as hook, and so on.



Nameplate label

Unit: mm



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