FORTEC
GROUP

Datasheet

JDI

TX26D207VM0AAA Rev.B

KO-01-000R1.1

FORTEC
UNITED STATES

FORTEC
INTEGRATED

FORTEC
UNITED KINGDOM

The information contained in this document has been carefully researched and is, to the best of our knowledge, accurate. However, we assume no liability for any product failures or damages, immediate or consequential, resulting from the use of the information provided herein. Our products are not intended for use in systems in which failures of product could result in personal injury. All trademarks mentioned herein are property of their respective owners. All specifications are subject to change without notice.

Japan Display Inc.

TENTATIVE

FOR MESSRS : _____

DATE : Oct. 18th,2024

TECHNICAL DATA

TX26D207VM0AAA

Contents

ACCEPTED BY: _____

PROPOSED BY: Oblack Tsai

2. RECORD OF REVISION

DATE	SHEET No.	SUMMARY

3. GENERAL DATA

3.1 DISPLAY FEATURES

This module is a 10.1” WXGA of 16:9 format amorphous silicon TFT. The pixel format is vertical stripe and sub pixels are arranged as R(red), G(green), B(blue) sequentially. This display is RoHS compliant, and COG (chip on glass) technology and LED backlight are applied on this display.

Part Name	TX26D207VM0AAA
Module Dimensions	231(W) mm × 153.5 (H) mm × 11.07 (D) mm (max)
LCD Active Area	216.96(W)mm x 135.6(H)mm
Pixel Pitch	0.1695 (W) mm × 0.1695 (H) mm
Resolution	1280× 3 (RGB) (W) × 800 (H) dots
Color Pixel Arrangement	RGB Vertical Stripe
LCD Type	Transmissive Type, Normally Black
Display Type	Active Matrix
Number of Colors	16.7M Colors (8-bit RGB)
Backlight	Light Emitting Diode (LED)
Weight	340g(typ)
Interface	LVDS；20pins
Power Supply Voltage	3.3V for LCD；12V for Backlight
Viewing Direction	Super Wide Version

4. ABSOLUTE MAXIMUM RATINGS

Item	Symbol	Min.	Max.	Unit	Remarks
Supply Voltage	V_{DD}	-0.5	4.5	V	-
Input Voltage of Logic	V_I	-0.5	4.5	V	Note 1
Operating Temperature	Top	-30	85	°C	Note 2
Storage Temperature	Tst	-40	90	°C	Note 2
Backlight Input Voltage	V_{LED}	-	15	V	-

Note 1: The rating is defined for the signal voltage of the interface such as CLK and pixel data pairs.

Note 2: The maximum rating is defined as above based on the panel surface temperature, which might be different from ambient temperature after assembling the panel into the application. Moreover, some temperature-related phenomenon as below needed to be noticed:

- Background color, contrast and response time would be different in temperatures other than 25°C.
- Operating under high temperature will shorten LED lifetime.

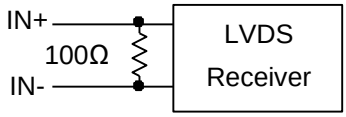
5. ELECTRICAL CHARACTERISTICS

5.1 LCD CHARACTERISTICS

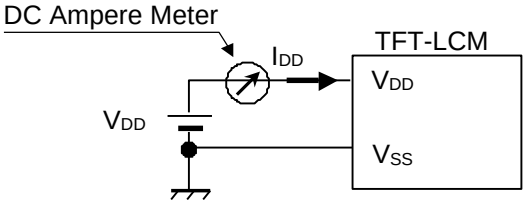
$T_a = 25\ ^{\circ}C,\ V_{SS} = 0V$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Power Supply Voltage	V_{DD}	-	3.0	3.3	3.6	V	-
Differential Input Voltage for LVDS Receiver Threshold	V_I	V_{IH}	-	-	+100	mV	Note 1
		V_{IL}	-100	-	-		
Power Supply Current	I_{DD}	V_{DD}-V_{SS} =3.3V	-	155	-	mA	Note 2,3
Frame Frequency	f_{Frame}	-	-	60	-	Hz	Note 4
CLK Frequency	f_{CLK}	-	-	71	-	MHz	

Note 1: VCM 1.2V is common mode voltage of LVDS transmitter and receiver. The input terminal of LVDS transmitter is terminated with 100Ω.



Note 2: An all white check pattern is used when measuring I_{DD}. f_{Frame} is set to 60Hz.



Note 3: (TBD) fuse is applied in the module for I_{DD}. For display activation and protection purpose, power supply is recommended larger than (TBD) to start the display and break fuse once any short circuit occurred.

Note 4: For LVDS transmitter input.

5.2 BACKLIGHT CHARACTERISTICS

$T_a = 25\ ^\circ C$

Item	Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
LED Input Voltage	V_{LED}	Backlight Unit	10.8	12	13.2	V	Note 1
LED Forward Current (Dim Control)	I_{LED}	100% duty	-	620	-	mA	Note 2
		0% duty	-	TBD	-		
LED Lifetime	-	I_{LED}=620 mA	-	70K	-	hrs	Note 3

Note 1: Fig. 5.1 shows the LED backlight circuit.

Note 2: Dimming function can be obtained by applying PWM signal from the display interface CN3. The recommended PWM signal is 1K ~ 10KHz with 3.3 V amplitude.

Note 3: The estimated lifetime is specified as the time to reduce 50% brightness by applying 620mA at 25°C.

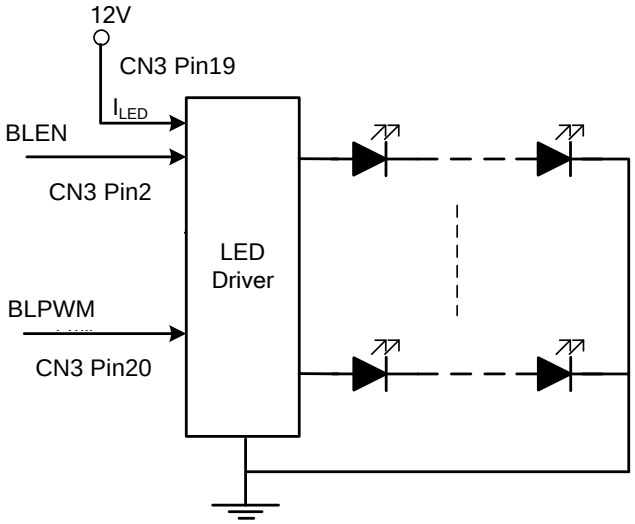


Fig 5.1

6. OPTICAL CHARACTERISTICS

The optical characteristics are measured based on the conditions as below:

- Supplying the signals and voltages defined in the section of electrical characteristics.
- The ambient temperature is 25°C.
- In the dark room less than 100 lx, the equipment has been set for the measurements as shown in Fig 6.1.

$T_a = 25\ ^{\circ}C$, $f_{Frame} = 60$ Hz, $V_{DD} = 3.3$V

Item		Symbol	Condition	Min.	Typ.	Max.	Unit	Remarks
Brightness of White		-	I_{LED}= 620mA $\phi = 0^{\circ}, \theta = 0^{\circ}$	800	1000	-	cd/m^2	Note 1
Brightness Uniformity		-		70	-	-	%	Note 2
Contrast Ratio		CR		1000	1500	-	-	Note 3
Response Time		Tr + Tf	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	-	25	ms	Note 4
NTSC Ratio		-	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	65	-	%	-
Viewing Angle		θ x	$\phi = 0^{\circ}$, CR ≥ 10	-	85	-	Degree	Note 5
		θ x′	$\phi = 180^{\circ}$, CR ≥ 10	-	85	-		
		θ y	$\phi = 90^{\circ}$, CR ≥ 10	-	85	-		
		θ y′	$\phi = 270^{\circ}$, CR ≥ 10	-	85	-		
Color Chromaticity	Red	X	$\phi = 0^{\circ}, \theta = 0^{\circ}$	-	(0.632)	-	-	Note 6
		Y		-	(0.320)	-		
	Green	X		-	(0.328)	-		
		Y		-	(0.620)	-		
	Blue	X		-	(0.158)	-		
		Y		-	(0.146)	-		
	White	X		-	(0.320)	-		
		Y		-	(0.360)	-		

Note 1: The brightness is measured from the center point of the panel, P5 in Fig. 6.2, for the typical value.

Note 2: The brightness uniformity is calculated by the equation as below:

$$\text{Brightness uniformity} = \frac{\text{Min. Brightness}}{\text{Max. Brightness}} \times 100\%$$

which is based on the brightness values of the 9 points measured by BM-5 as shown in Fig. 6.2.

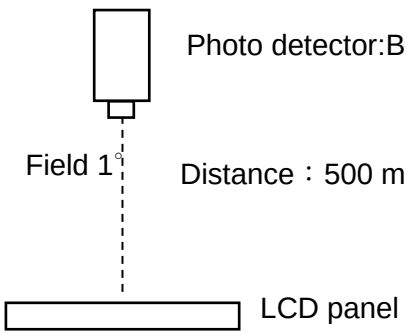


Fig. 6.1

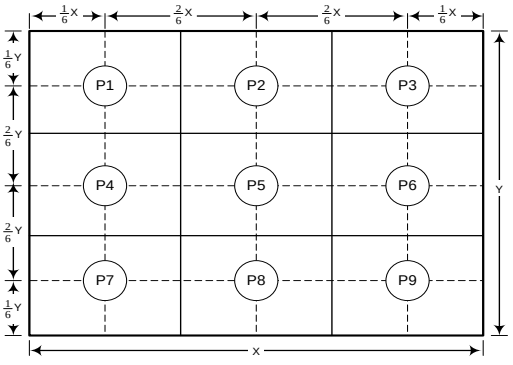


Fig. 6.2

Note 3: The Contrast ratio is measured from the center point of the panel, P5, and defined as the following equation:

$$CR = \frac{\text{Brightness of White}}{\text{Brightness of Black}}$$

Note 4: The definition of response time is shown in Fig. 6.3. The rising time is the period from 10% brightness to 90% brightness when the data is from black to white. Oppositely, Falling time is the period from 90% brightness rising to 10% brightness.

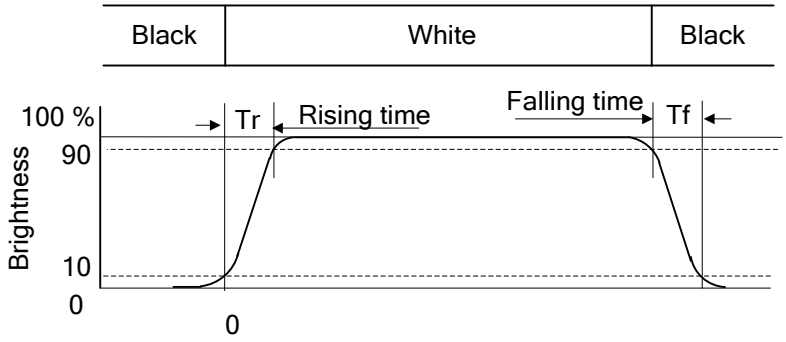


Fig. 6.3

Note 5: The definition of viewing angle is shown in Fig. 6.4. Angle ϕ is used to represent viewing directions, for instance, $\phi = 270^\circ$ means 6 o'clock, and $\phi = 0^\circ$ means 3 o'clock. Moreover, angle θ is used to represent viewing angles from axis Z toward plane XY.

The display is super wide viewing angle version, so that the best optical performance can be obtained from every viewing direction.

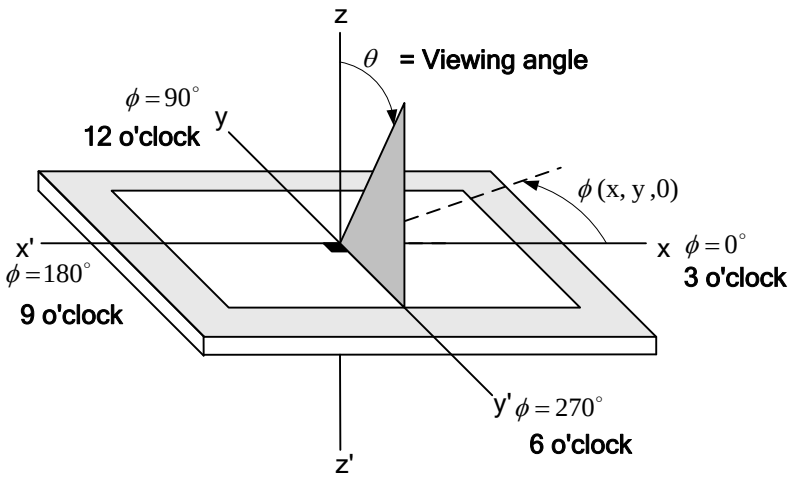
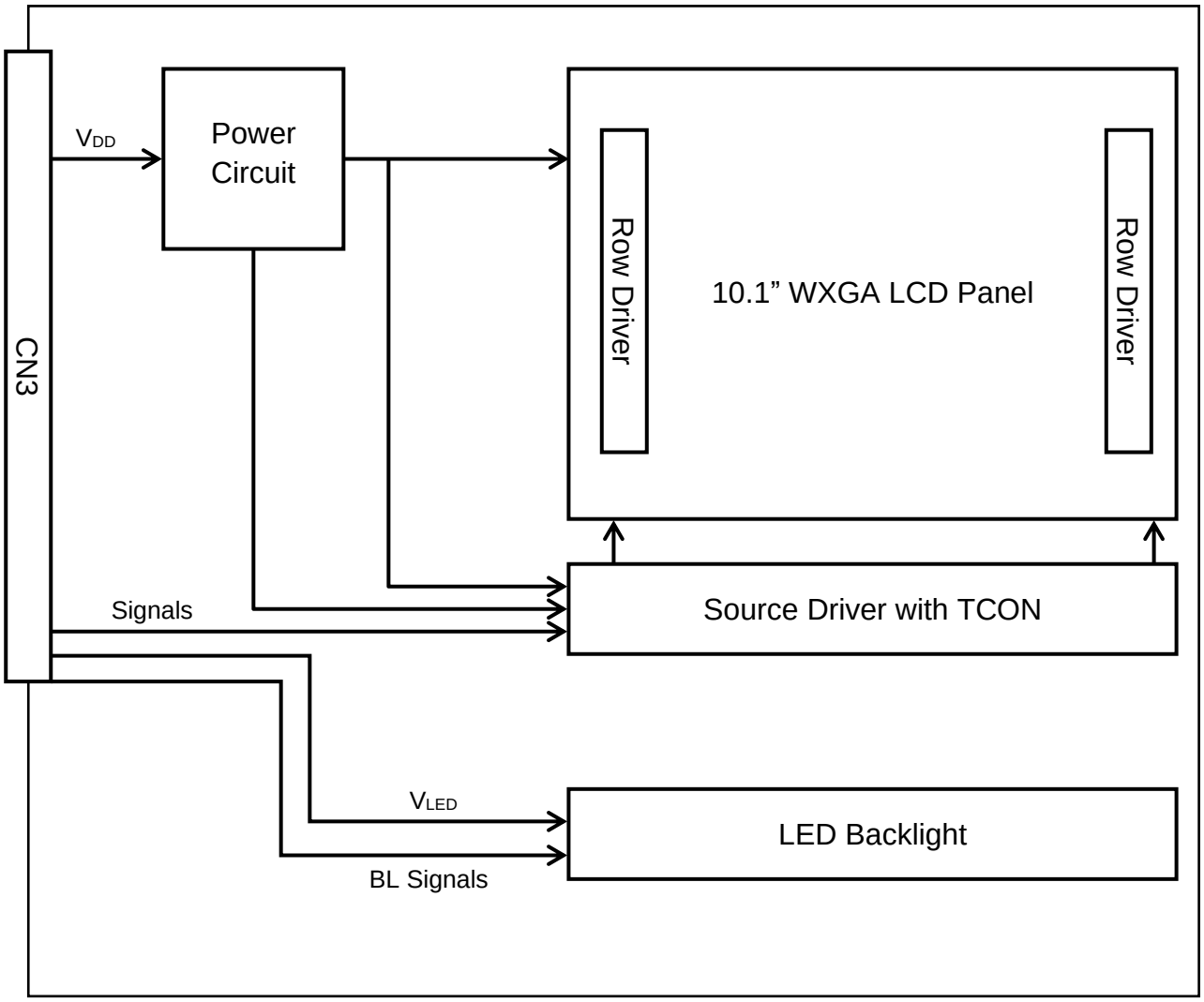


Fig. 6.4

Note 6: The color chromaticity is measured from the center point of the panel, P5, as shown in Fig. 6.2.

7. BLOCK DIAGRAM



Note :

1) Signals are UD/LR, CLK and pixel data pairs.
2) BL signals are BLEN and BLPWM.

8. LCD INTERFACE

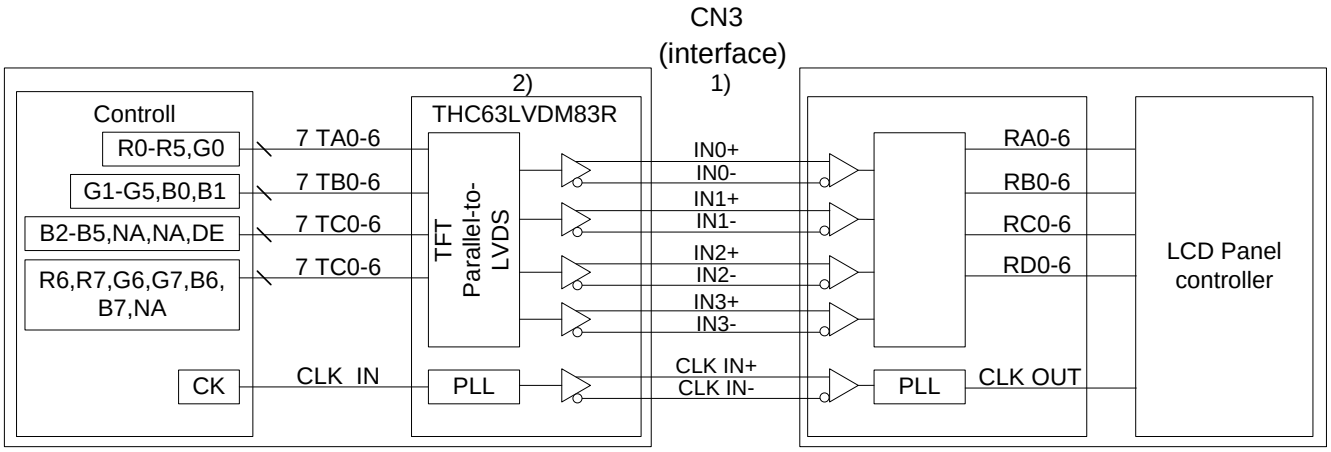
8.1 INTERFACE PIN CONNECTIONS

The display interface connector (CN3) is FI-SEB20P-HF13E made by JAE and pin assignment is as below:

Pin No.	Signal	Signal	Pin No.	Signal	Signal
1	V_{DD}	Power Supply for Logic	11	IN2-	B2~B5, DE
2	BLEN	Backlight Enable	12	IN2+	
3	V_{SS}	GND	13	V_{SS}	GND
4	UD/LR	Horizontal Display mode Control High :Normal, Low or OPEN :Reverse	14	CLK IN-	Pixel Clock
5	IN0-	R0~R5, G0	15	CLK IN+	
6	IN0+		16	V_{SS}	GND
7	V_{SS}	GND	17	IN3-	R6, R7, G6, G7, B6, B7
8	IN1-	G1~G5, B0~B1	18	IN3+	
9	IN1+		19	V_{LED}	Backlight power input
10	V_{SS}	GND	20	BLPWM	Backlight Dimming

Note 1: IN n- and IN n+ (n=0, 1, 2, 3), CLK IN- and CLK IN+ should be wired by twist-pairs.

8.2 LVDS INTERFACE



Note 1: LVDS cable impedance should be 100 ohms per signal line when each 2-lines (+, -) is used in differential mode.

Note 2: The recommended transmitter, THC63LVDM83R, is made by Thine or equivalent, which is not contained in the module.

8.3 TIMING CHART

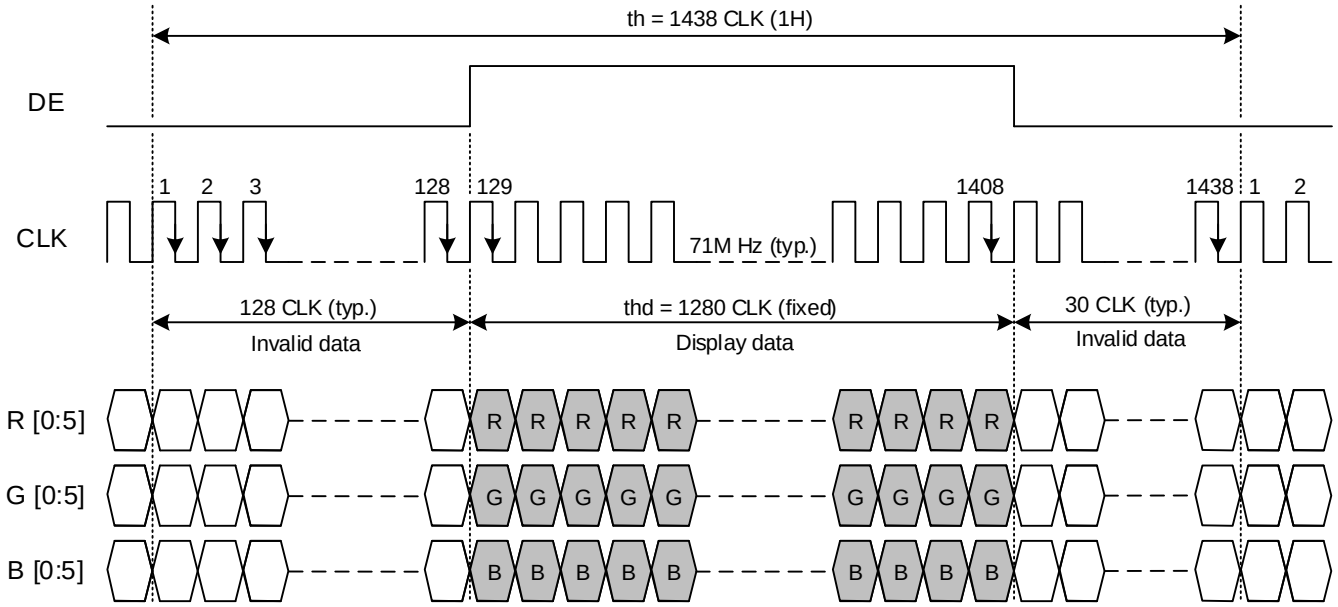


Fig. 8.1 Horizontal Timing

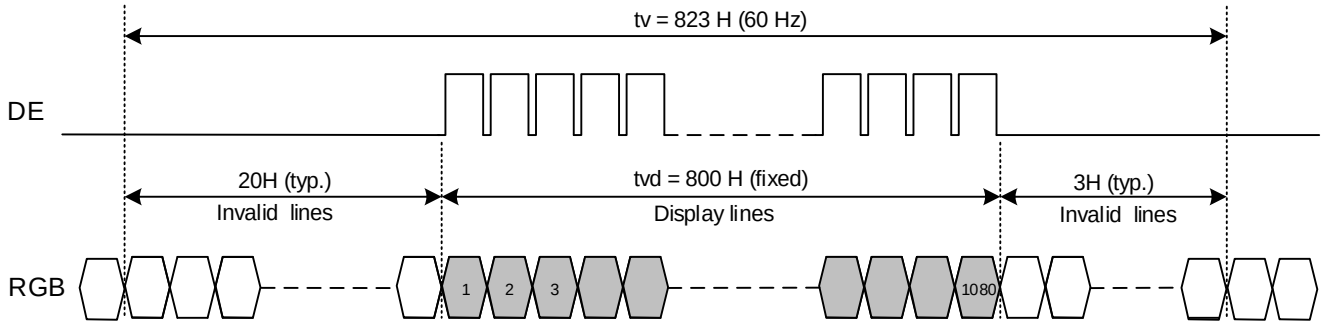


Fig. 8.2 Vertical Timing

8.4 TIMING TABLE

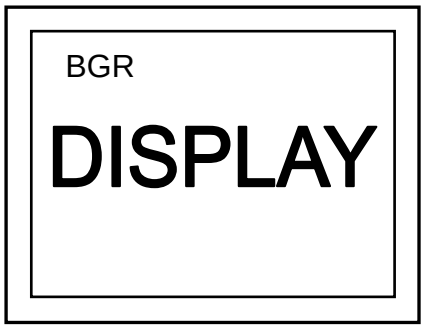
The column of timing sets including minimum, typical, and maximum as below are based on the best optical performance, frame frequency (f_{Frame}) = 60 Hz to define.

A. DE MODE

Item		Symbol	Min.	Typ.	Max.	Unit
Horizontal	CLK Frequency	fclk	-	71	-	M Hz
	Display Data	thd	1280			CLK
	Cycle Time	th	-	1438	-	
Vertical	Display Data	tvd	800			H
	Cycle Time	tv	-	823	-	

8.5 DISPLAY MODE CONTROL

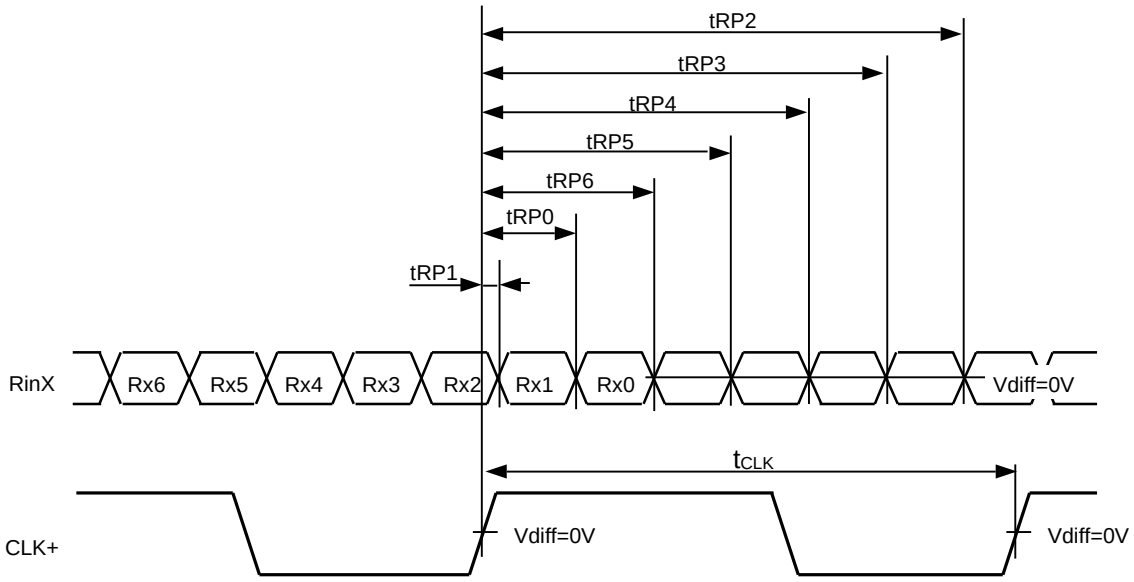
Scan direction is available to be switched as below by setting CN3's UD/ LR pin.



UD/LR : High

UD/LR :Low or Open

8.6 LVDS RECEIVER TIMING



RinX= (RinX+)-(RinX-) (X=0, 1, 2)

Item		Symbol	Min.	Typ.	Max.	Unit
CLK	Cycle frequency	1/tcLK	-	71	-	MHz
RinX (X=0,1,2)	0 data position	tRP0	-	1/7* t_{CLK}	-	ns
	1st data position	tRP1	-	0	-	
	2nd data position	tRP2	-	6/7* t_{CLK}	-	
	3rd data position	tRP3	-	5/7* t_{CLK}	-	
	4th data position	tRP4	-	4/7* t_{CLK}	-	
	5th data position	tRP5	-	3/7* t_{CLK}	-	
	6th data position	tRP6	-	2/7* t_{CLK}	-	

8.7 POWER SEQUENCE

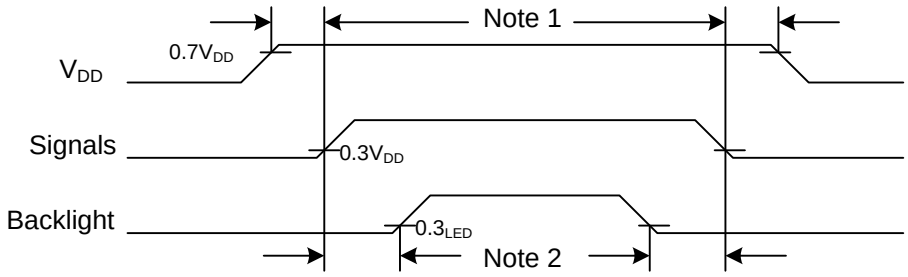


Fig 9.4 Power Sequence

Note 1: In order to avoid any damages, V_{DD} has to be applied before all other signals. The opposite is true for power off where V_{DD} has to be remained on until all other signals have been switch off. The recommended time period is 1 second.

Note 2: In order to avoid showing uncompleted patterns in transient state. It is recommended that switching the backlight on is delayed for 1 second after the signals have been applied. The opposite is true for power off where the backlight has to be switched off 1 second before the signals are removed.

Note 3: In order to avo id high Inrush current, V_{DD} rising time need to set more than 0.5ms.

8.8 DATA INPUT for DISPLAY COLOR

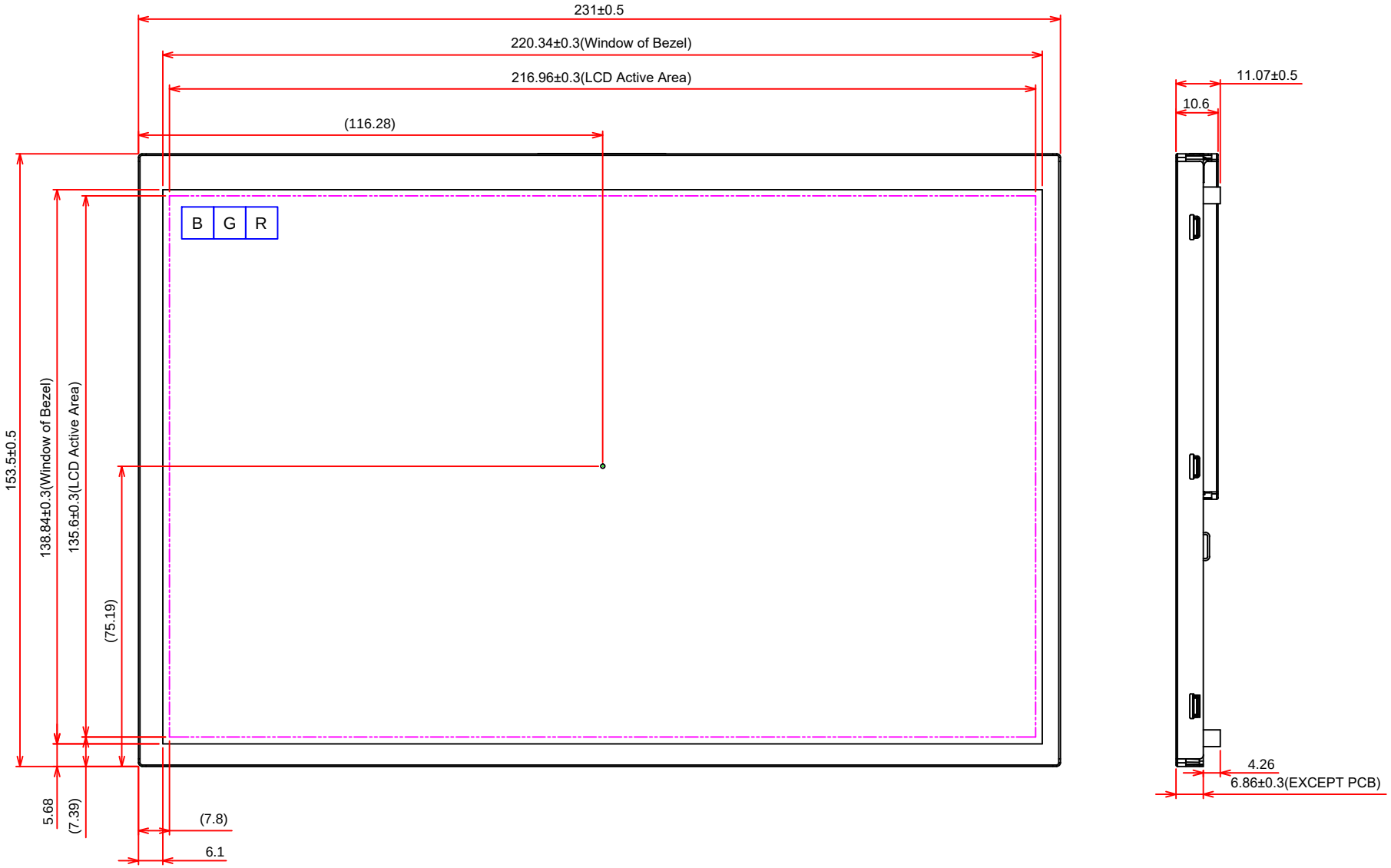
Input color		Red Data								Green Data								Blue Data							
		R7	R6	R5	R4	R3	R2	R1	R0	G7	G6	G5	G4	G3	G2	G1	G0	B7	B6	B5	B4	B3	B2	B1	B0
		MSB							LSB	MSB							LSB	MSB							LSB
Basic Color	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Cyan	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
	Magenta	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1
	Yellow	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1	1
Red	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(1)	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(2)	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Red(253)	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(254)	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red(255)	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Green	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Green(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0
	Green(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Green(253)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1	0	0	0	0	0	0	0	0
	Green(254)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0
	Green(255)	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0
Blue	Black	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue(1)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1
	Blue(2)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0
	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:	:
	Blue(253)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	0	1
	Blue(254)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	0
	Blue(255)	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1	1	1

Note 1: Definition of gray scale : Color(n) Number in parenthesis indicates gray scale level. Larger number corresponds to brighter level.

Note 2: Data Signal : 1 : High, 0 : Low

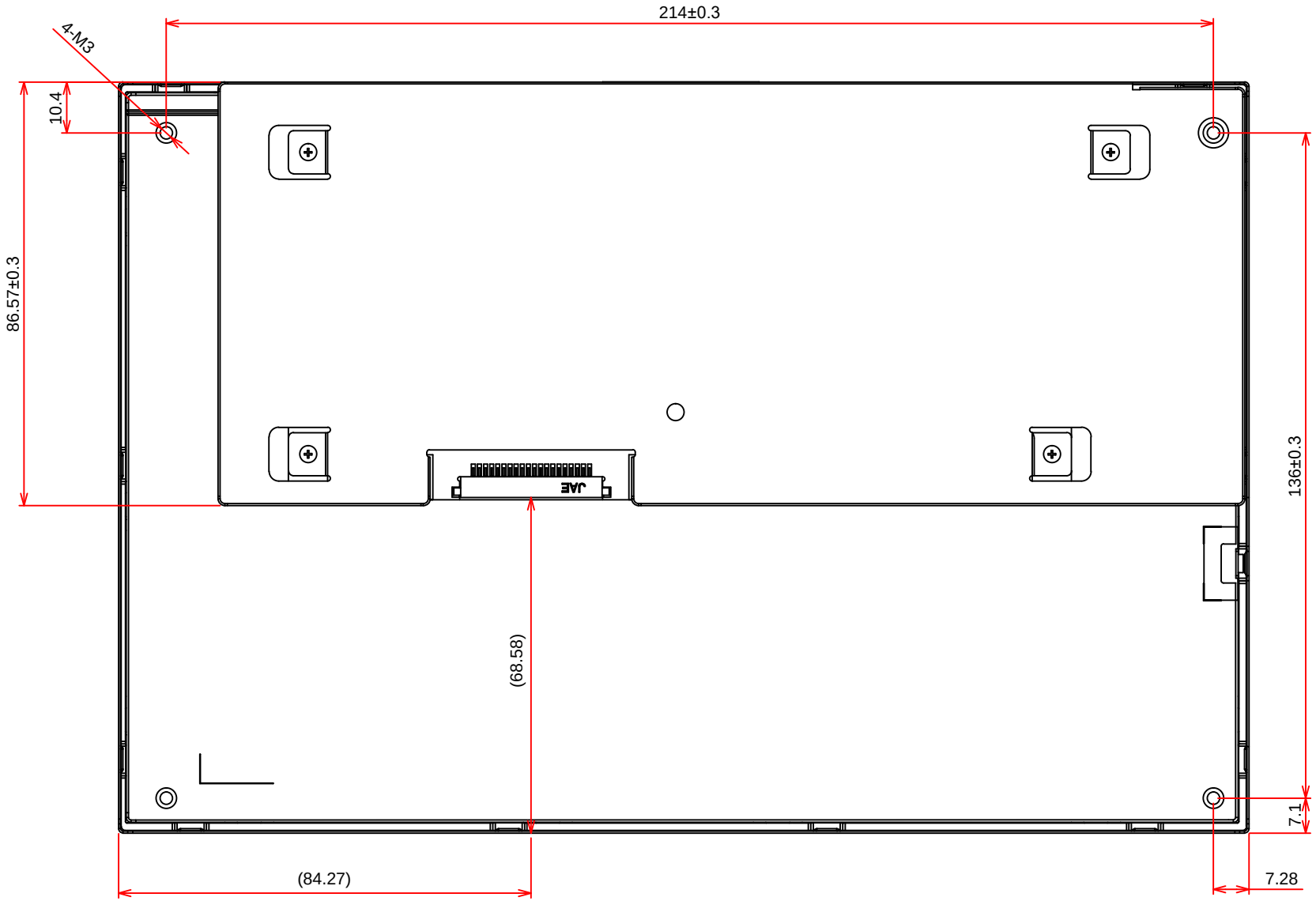
9. OUTLINE DIMENSIONS

9.1 FRONT VIEW (pixels are arranged as R(red), G(green), B(blue))



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

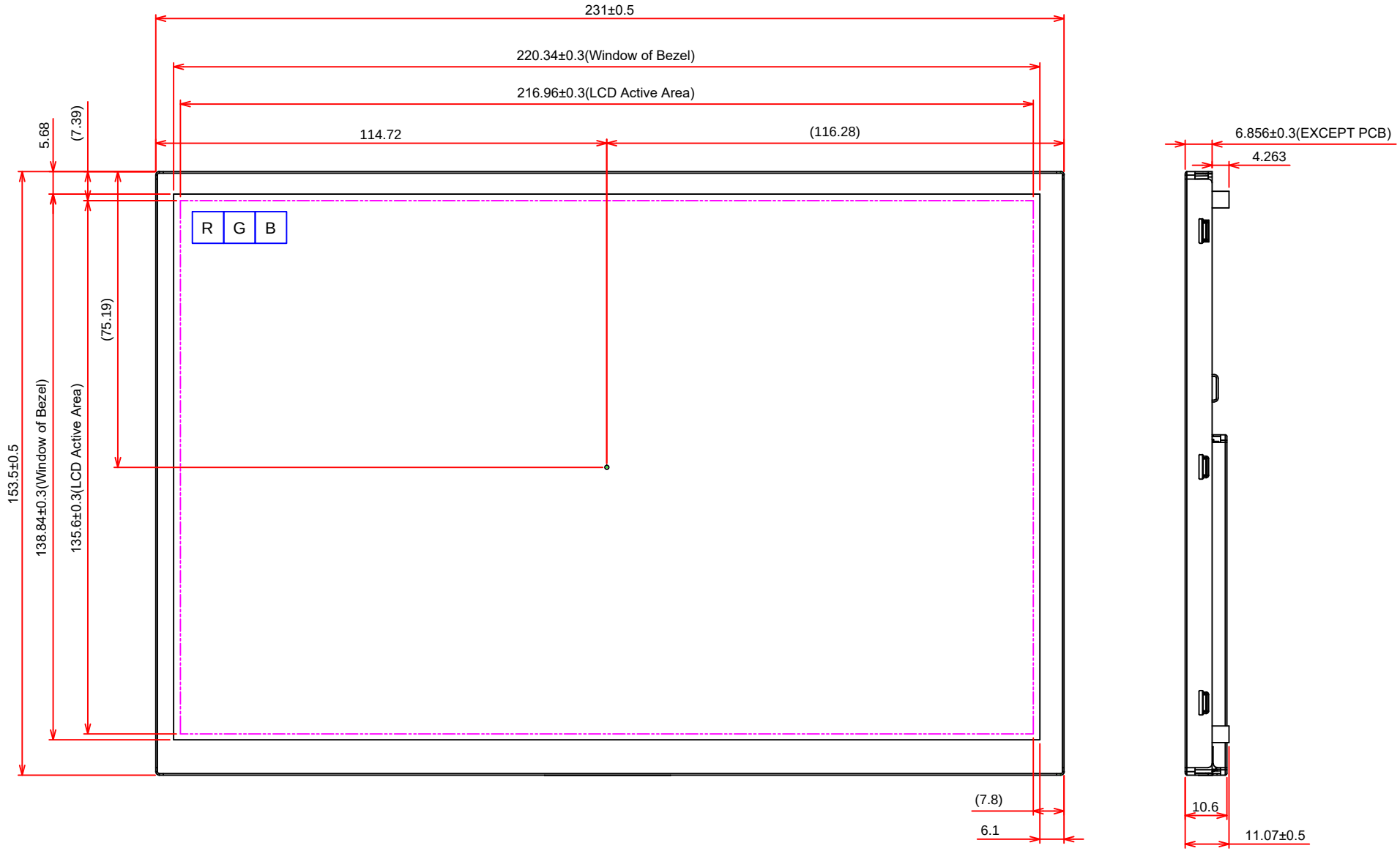
9.2 RAER VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

JDI Taiwan Inc. Kaohsiung Branch	SHEET No.	JDC900000024-1	PAGE	9-2/4

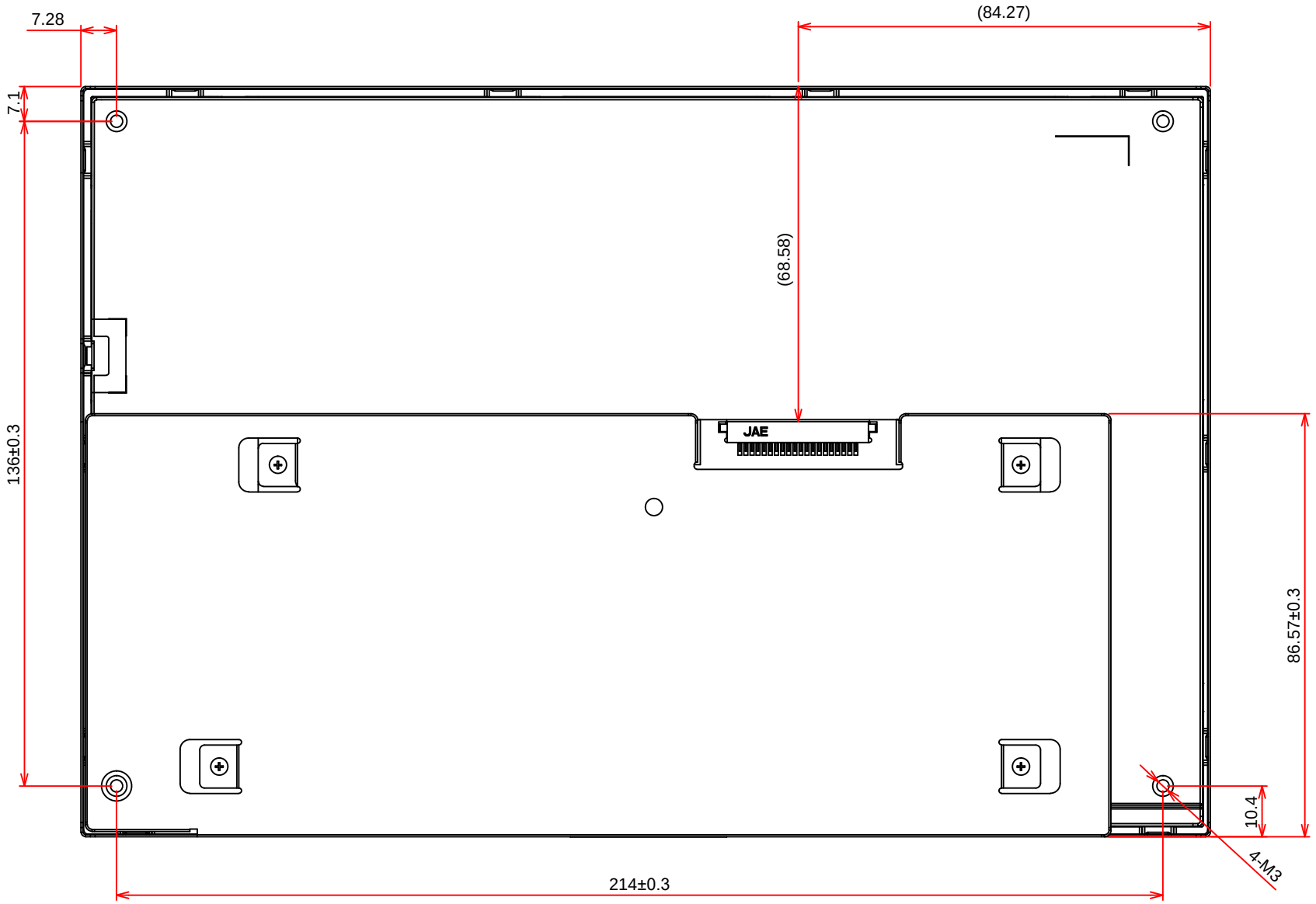
9.3 FRONT VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

JDI Taiwan Inc. Kaohsiung Branch	SHEET No.	JDC900000024-1	PAGE	9-3/4

9.4 RAER VIEW



General Tolerance:±0.5mm
Scale : NTS
Unit : mm

JDI Taiwan Inc. Kaohsiung Branch	SHEET No.	JDC900000024-1	PAGE	9-4/4

10. DESIGNATION of LOT MARK

1) The lot mark is showing in Fig.10.1. First 4 digits are used to represent production lot, T represented made in Taiwan, and the last 6 digits are the serial number.

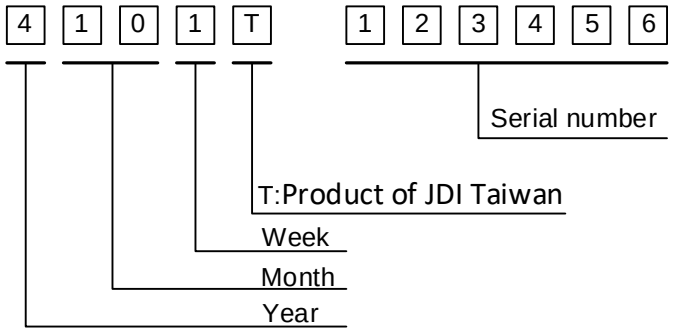


Fig. 10.1

2) The tables as below are showing what the first 4 digits of lot mark are shorted for.

Year	Lot Mark
2024	4
2025	5
2026	6
2027	7
2028	8

Month	Lot Mark	Month	Lot Mark
Jan.	01	Jul.	07
Feb.	02	Aug.	08
Mar.	03	Sep.	09
Apr.	04	Oct.	10
May	05	Nov.	11
Jun.	06	Dec.	12

Week	Lot Mark
1~7 days	1
8~14 days	2
15~21 days	3
22~28 days	4
29~31 days	5

3) Except letters I and O, revision number will be shown on lot mark and following letters A to Z.

REV No.	ITEM	REMARKS
A	-	-
B	Driver IC change	PCN 1104

4) The location of the lot mark is on the back of the display shown in Fig. 10.2

Label example :

Fig. 10.2

FORTEC
GROUP

Our company network supports you worldwide with offices in Germany, Austria, Switzerland, the UK and the USA. For more information please contact:

FORTEC
GROUP

FORTEC Elektronik AG | Augsburger Straße 2b | 82110 Germering
+49 89 894450-0
info@fortecag.de | www.fortecag.de

FORTEC
INTEGRATED

FORTEC Integrated GmbH | Augsburger Straße 2b | 82110 Germering
+49 89 894363-0
info@fortec-integrated.de | www.fortec-integrated.de

FORTEC
POWER

FORTEC Power GmbH | Lise-Meitner-Straße 3 | 64560 Riedstadt
+49 6158 8285-0
weborder@fortec-power.de | www.fortec-power.de

FORTEC
EGYPT

FORTEC Electronic Design and Solution Egypt SMLC | Linx Business Park
Unit B318 | Smart Village | Giza Governorate
info@fortec-integrated.de | www.fortec-integrated.de

Autronic Steuer- und Regeltechnik GmbH | Siemensstraße 17
74343 Sachsenheim
+49 7147 24-0
vertrieb@autronic.de | www.autronic.de

FORTEC
CZECH REPUBLIC

FORTEC Czech Republic s.r.o. | Přátelství 275 | 330 02 Dýšina
+49 89 894363-0
info@fortec.cz | www.fortec.cz

FORTEC
UNITED STATES

FORTEC United States, Corp. | 87 Raynor Avenue Unit 1 | Ronkonkoma
NY | 11779 | +1 631 5804360
info@fortec.us | www.fortec.us

FORTEC
SWITZERLAND

FORTEC Switzerland AG | Bahnhofstraße 3 | 5436 Würenlos
+41 44 7446111
info@fortec.ch | www.fortec.ch

FORTEC
UNITED KINGDOM

FORTEC Technology UK Ltd. | Osprey House | 1 Osprey Court
Hinchingbrooke Business Park | Huntingdon | Cambridgeshire | PE29 6FN
+44 1480 411600
info@fortec.uk | www.fortec.uk