Datasheet

Ortustech

COM70H7M24ULC

OR-20-014

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This product is under development and specifications are subject to change.

Specifications for

Blanview TFT-LCD Monitor (TENTATIVE)

(7.0" WVGA 800 x RGB x 480 Landscape)

Version 0.2
(Please be sure to check the specifications latest version.)

MODEL COM70H7M24ULC

Customer's Approval

Signature:

Name:

Section:

Title:

Date:

ORTUSTECH

ORTUS TECHNOLOGY CO., LTD.

Approved by

Checked by

Prepared by

Version History

Ver.	Date	Page	Description	
0.0	Dec. 21, 2017	-	-	Tentative issue
0.1 △A ×1	Feb. 20, 2018	13	change	7.2.1 LVDS DC Characteristics
0.2 △B ×8	Jun.6, 2018	11	change	5. Absolute Maximum Rating
		12	change	7.1 DC Characteristics(BackLight)
		18	change	8. LED Circuit
		19	change	9.1 Optical Characteristics
		20	change	9.2 Temperature Characteristics
		21	change	10.1 Defective Display and Screen Quality
		23	change	11. Reliability Test
		30	change	APPENDIX 1. Measurement Condition (Backlight ON)

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1. Application

This Specification is applicable to 178 mm (7.0 inch) Blanview TFT-LCD monitor for non-military use.

◎ ORTUS TECHNOLOGY makes no warranty or assume no liability that use of this Product and/or any information including drawings in this Specification by Purchaser is not infringing any patent or other intellectual property rights owned by third parties, and ORTUS TECHNOLOGY shall not grant to Purchaser any right to use any patent or other intellectual property rights owned by third parties. Since this Specification contains ORTUS TECHNOLOGY's confidential information and copy right, Purchaser shall use them with high degree of care to prevent any unauthorized use, disclosure, duplication, publication or dissemination of ORTUS TECHNOLOGY'S confidential information and copy right.

◎ If Purchaser intends to use this Products for an application which requires higher level of reliability and/or safety in functionality and/or accuracy such as transport equipment (aircraft, train, automobile, etc.), disaster-prevention/security equipment or various safety equipment, Purchaser shall consult ORTUS TECHNOLOGY on such use in advance.

◎ This Product shall not be used for application which requires extremely higher level of reliability and/or safety such as aerospace equipment, telecommunication equipment for trunk lines, control equipment for nuclear facilities or life-support medical equipment.

◎ It must be noted as an mechanical design manner, especial attention in housing design to prevent arcuation/flexureor caused by stress to the LCD module shall be considered.

◎ ORTUS TECHNOLOGY assumes no liability for any damage resulting from misuse, abuse, and/or miss-operation of the Product deviating from the operating conditions and precautions described in the Specification.

◎ It shall be mutually conferred if nonconforming defect which result from unspecified cause in this specification arises.

◎ If any issue arises as to information provided in this Specification or any other information, ORTUS TECHNOLOGY and Purchaser shall discuss them in good faith and seek solution.

◎ ORTUS TECHNOLOGY assumes no liability for defects such as electrostatic discharge failure occurred during peeling off the protective film or Purchaser's assembly process.

◎ This Product is compatible for RoHS directive.

Object substance	Maximum content [ppm]
Cadmium and its compound	100
Hexavalent Chromium Compound	1000
Lead & Lead compound	1000
Mercury & Mercury compound	1000
Polybrominated biphenyl series（PBB series）	1000
Polybrominated biphenyl ether series（PBDE series）	1000

2. Outline Specifications

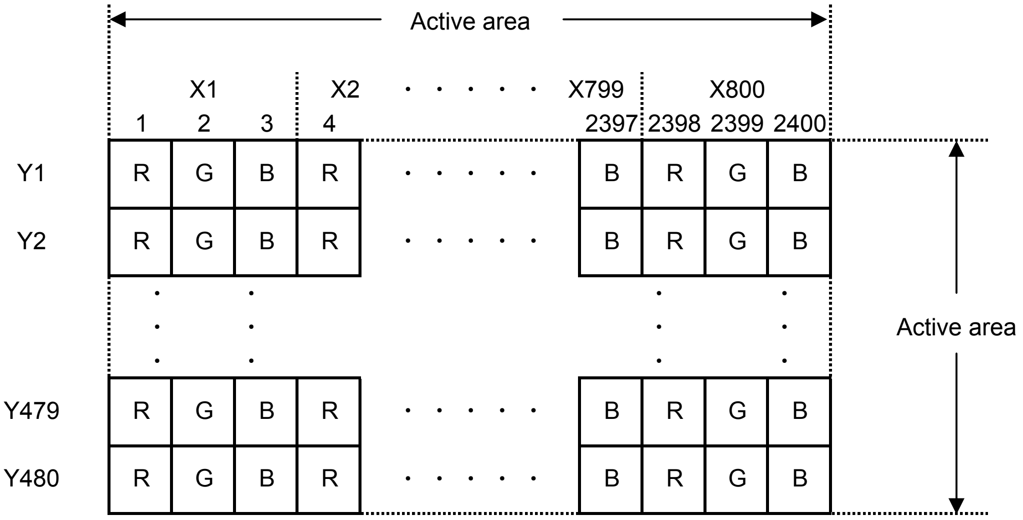
2.1 Features of the Product

- 7.0 inch diagonal display, 800 x RGB [H] x 480 [V] dots.
- 16.7 M colors (8-bit) / 262 K colors (6-bit).
- 3.3V voltage single power source.
- Timing generator [TG], Counter-electrode driving circuitry, Built-in power supply circuit.
- Long life & High bright white LED back-light.
- Blanview TFT-LCD, improved outdoor readability.

	Indoor		Outdoor	
	Readability	Power Efficiency (Battery Life)	Readability	Power Efficiency (Battery Life)
Transmissive	Good	Good	Fair	Poor
Transflective	Fair	Poor	Good	Good
Blanview	Good	Good	Good	Good

2.2 Display Method

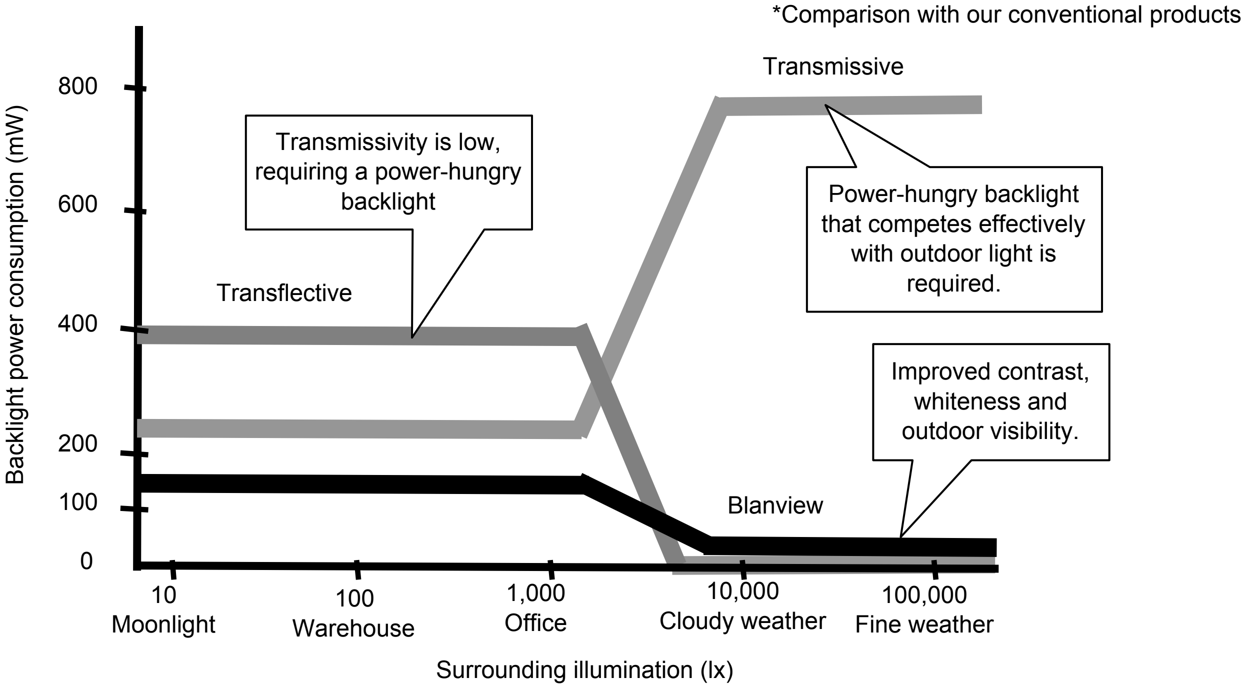
Items	Specifications	Remarks
Display type	VA 16.7 M colors. / 262 K colors. Blanview, Normally black.	
Driving method	a-Si TFT Active matrix. Line-scanning, Non-interlace.	
Dot arrangement	RGB stripe arrangement.	Refer to "Dot arrangement"
Signal input method	8-bit / 6-bit LVDS interface	
Backlight type	Long life & High bright white LED.	
NTSC ratio	50%	



Dot arrangement (FPC cable placed down side)

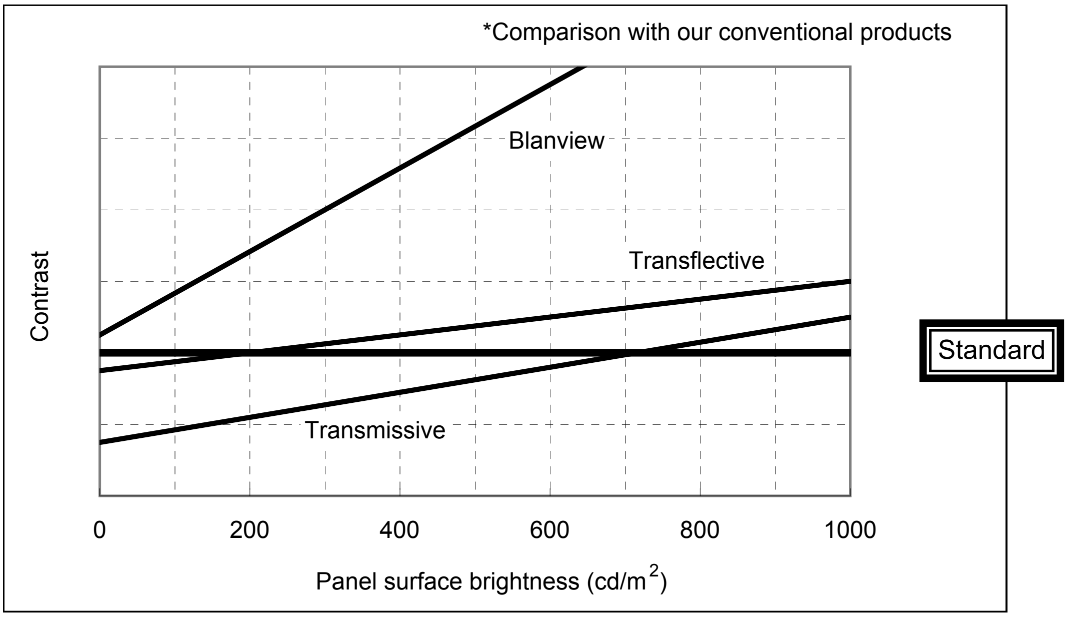
<Features of Blanview>

- Backlight power consumption required to assure visibility. (equivalent to 3.5”QVGA)



- Contrast characteristics under 100,000lx.（same condition as direct sunlight.）

With better contrast (higher contrast ratio), Blanview TFT-LCD has the best outdoor readability in three different types of TFT-LCD.
Below chart shows contrast value against panel surface brightness. (Horizontal: Panel surface brightness/ Vertical: Contrast value) LCD panel has enough outdoor readability above our Standard line.
(ORTUS TECHNOLOGY criteria)



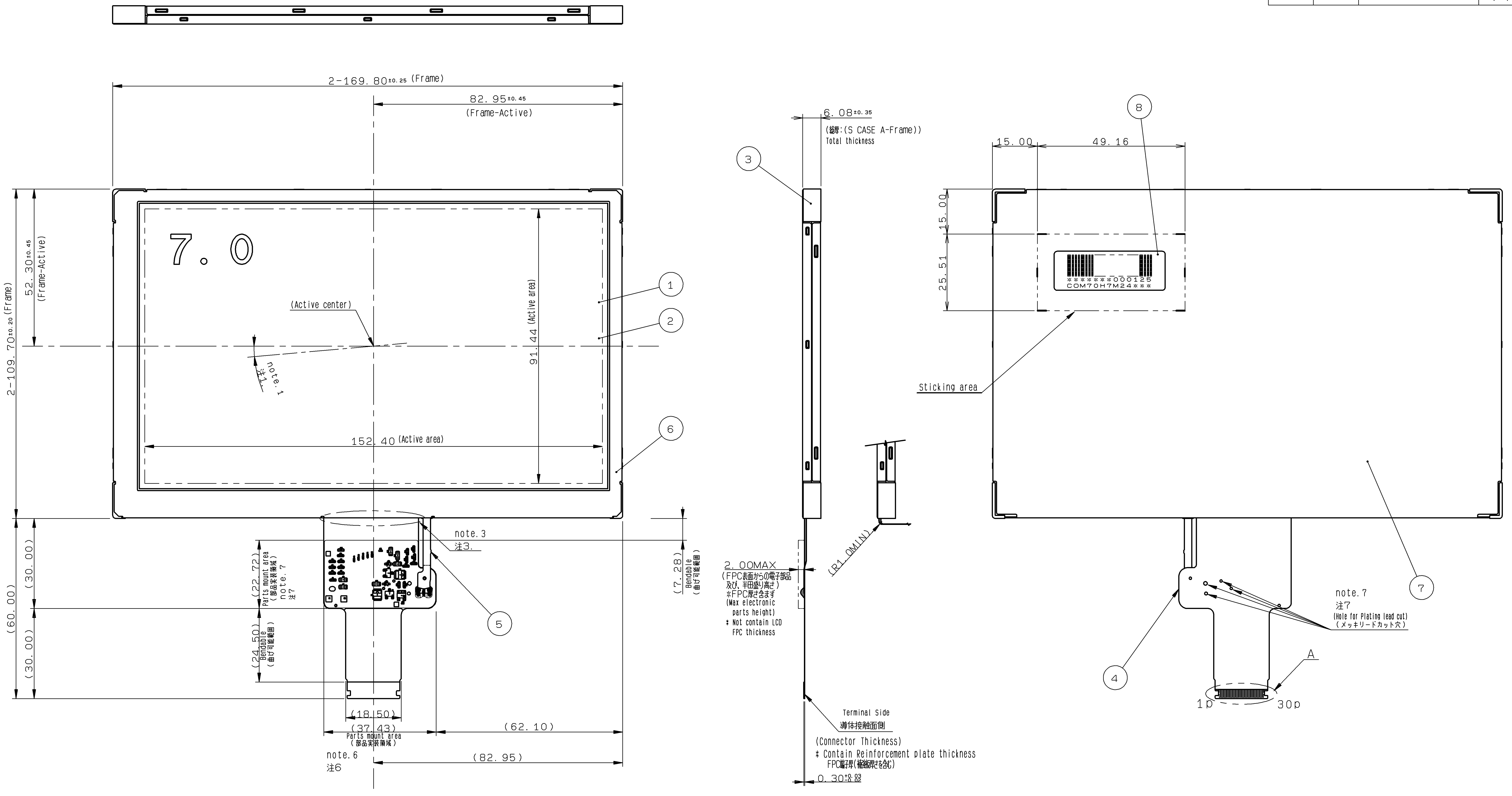
3. Dimensions and Outward Form

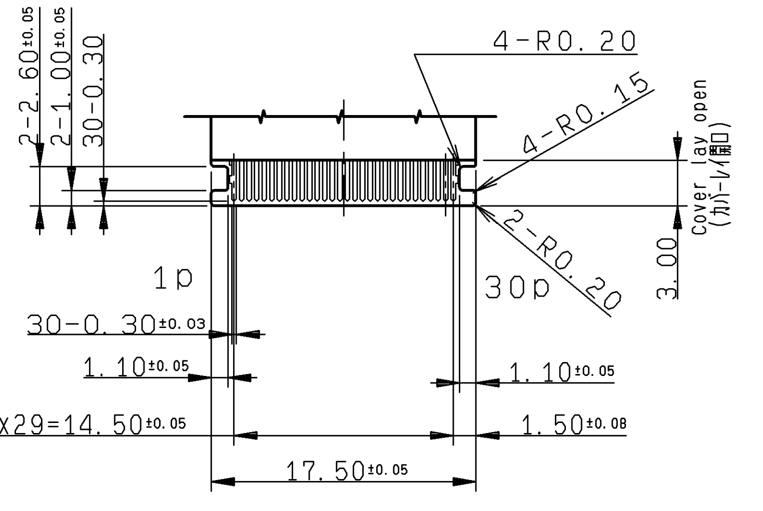
3.1 Dimensions

Items	Specifications	Unit	Remarks
Outline dimensions	169.80[H] × 109.70[V] × 6.08 [D]	mm	Exclude FPC cable and parts on FPC.
Active area	152.40[H] × 91.44[V]	mm	178mm diagonal
Number of dots	2400[H] × 480[V]	dot	
Dot pitch	63.5[H] × 190.5[V]	um	
Surface hardness of the polarizer	(TBD)	H	(TBD)
Weight	(TBD)	g	Include FPC cable

3.2 Outward Form

EC No.	REV. No.	REVISE	DATE (Y:M:D)	APPROVED	CHECKED	PREPARED
			: :			


2-169.80±0.25 (Frame)
82.95±0.45 (Frame-Active)
2-109.70±0.20 (Frame)
52.30±0.45 (Frame-Active)
7.0
(Active center)
note.1 注1
91.44 (Active area)
152.40 (Active area)
①
②
⑥
③
④
⑤
⑦
⑧
note.3 注3
(60.00)
(30.00)
(30.00)
(22.72) Parts mount area (部品実装領域) note.7 注7
(24.50) Bendable (曲げ可能範囲)
(7.28) Bendable (曲げ可能範囲)
(18.50)
(37.43) Parts mount area (部品実装領域)
(62.10)
note.6 注6
(82.95)
6.08±0.35 (総厚:(S CASE A-Frame)) Total thickness
2.00MAX (FPC表面からの電子部品及び、半田盛り高さ) ※FPC厚さ含まず (Max electronic parts height) * Not contain LCD FPC thickness
(R1.0MIN)
Terminal Side 導体接触面側
(Connector Thickness) * Contain Reinforcement plate thickness FPC端子厚(補強板厚さを含む)
0.30+0.03/-0.02
15.00
49.16
15.00
25.51
Sticking area
※※※※※※000125 COM70H7M24※※※
note.7 注7 (Hole for Plating lead cut) (メッキリードカット穴)
A
1p
30p


2-2.60±0.05
2-1.00±0.05
30-0.30
4-R0.20
4-R0.15
2-R0.20
Cover lay open (カバーレイ開口)
3.00
1p
30p
30-0.30±0.03
1.10±0.05
1.10±0.05
P0.5±0.05X29=14.50±0.05
1.50±0.08
17.50±0.05

DETAIL A
(S=2/1)
note.4
注4

note Angular deviation of LCD cell from the TFT-LCD monitor's reference axis shall be less than [±50'].
注1. TFT-LCD PANELの角度ズレは、モニター基準軸に対し【±50′】以下とする。

Protective film is affixed on front surface of the screen.
Protective film is not protrude from the outline of the monitor.
2. 保護膜が表面側に貼られる。
保護膜はモニター外形からはみ出さないものとする。

Don't stress to FPC bonding area & the solder area.
3. FPCの圧着部及び半田接続部には負荷の掛からないようご注意願います。

Recommended FPC connectors
: IRISO/IMSA-9699S-30A-GFN1(Lower contact)
4. FPCの推奨コネクターの規格を下記に示す。
イリソ電子工業 :9699 series (IMSA-9699S-30A-GFN1(下接点))

Burrs direction of S CASE is outward.(Burrs size is less than 0.03mm.)
5. S CASEのバリ方向は外側になります。(MAX0.03mm)

Don't touch any conductive material to conductive area.
6. 導体開口部は導体部品との接触を避けてください。

Don't touch any conductive material to plating cut area.
7. メッキリードカット位置は導体部品との接触を避けてください。

PART NAME	ITEM	PART CODE	MODEL NUMBER	REMARK
S LABEL	8	Barcode(CODE39)	For Monitor	(37X13X0.075t)
S CASE C	7		SUS (t=0.20)	
S CASE A	6		SUS (t=0.15)	
FPC B	5			Use of LED
FPC A	4		Au/Ni plating AU/Niメッキ	Use of LCD
Frame	3		PC	
Polarizer	2			
TFT-LCD PANEL	1		Glass thickness=0.50+0.50t	

APPROVED
CHECKED
CHECKED
DESIGN 前田創
DRAW 前田創
GENERAL TOLERANCE ±0.5
SCALE 1/1
UNIT mm
ISSUE (Y:M:D) 17:12:09
MODEL DX-7M24シサクソノタ
NAME OUTLINE-D7M24

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DO NOT DUPLICATE, CONFIDENTIAL AND PROPRIETARY

DRAWING No.	REV.	SHEET	DIV.
SJD512081D201		/	

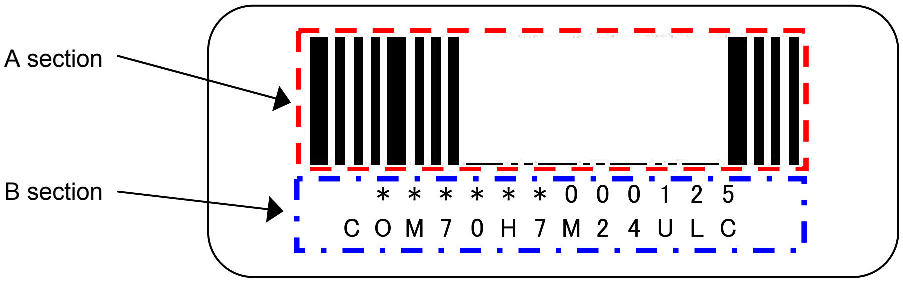
ASS'Y

3.3 Serial Label (S-LABEL)

1) Indication

A section : Bar code

B section : Combination of a character



Details of B section

Upper column: It indicates the least significant digit of manufacture year (1 digit), manufacture month with below alphabet (1letter), model code (4characters), serial number (6digits).

*	*	****	******
a	b	c	d

	Contents of display						
a	The least significant digit of manufacture year						
b	Manufacture month	Jan-A Feb-B	Mar-C Apr-D	May-E Jun-F	Jul-G Aug-H	Sep-I Oct-J	Nov-K Dec-L
c	Model code	70VC (Made in Japan) 70WC (Made in Malaysia)					
d	Serial number						

* Example of indication of Serial label (S-label)

・Made in Japan

8J70VC000125

means "manufactured in October 2018, 7" V type, C specifications, serial number 000125"

・Made in Malaysia

8J70WC000125

means "manufactured in October 2018, 7" W type, C specifications, serial number 000125"

Lower column: Model (13characters)

2) Location of Serial Label (S-label)

Refer to 3.2 "Outward Form".

3) Others

Bar code readability is excluded from quality assurance coverage.

4. Pin Assignment

No.	Symbol	Function	I/O
1	BLH	LED drive power source. (Anode side)	P
2	BLL2	LED drive power source . (Cathode side 2)	P
3	BLL1	LED drive power source . (Cathode side 1)	P
4	GND	Ground	P
5	VDD	Power supply input.	P
6	VDD	Power supply input.	P
7	TEST1	TEST input (Connect to VDD)	I
8	TEST2	TEST input (Connect to GND)	I
9	TEST3	TEST input (Connect to GND)	I
10	NC	No connection	-
11	UL/DR	Up & Left / Down & Right switching terminal (Low : DR , High or NC : UL)	I
12	IM	6 / 8 bit (based on VESA) switching terminal (Low : 6bit , High or NC : 8bit)	I
13	STBYB	Standby signal (Low:Standby operation,High:Normal operation)	I
14	GND	Ground	P
15	R0-	LVDS DATA0(-)	I
16	R0+	LVDS DATA0(+)	I
17	GND	Ground	P
18	R1-	LVDS DATA1(-)	I
19	R1+	LVDS DATA1(+)	I
20	GND	Ground	P
21	CLK-	LVDS CLK(-)	I
22	CLK+	LVDS CLK(+)	I
23	GND	Ground	P
24	R2-	LVDS DATA2(-)	I
25	R2+	LVDS DATA2(+)	I
26	GND	Ground	P
27	R3-	LVDS DATA3(-)	I
28	R3+	LVDS DATA3(+)	I
29	GND	Ground	P
30	NC	No connection	-

- Recommended connector : IRISO ELECTRONICS 9699 series [IMSA-9699S-30A-GFN1]
- Please be sure to check a consistency between pin assignment in "3.2 Outward Form" and your connector pin assignment when designing your circuit.
  Inconsistency in input signal assignment may cause a malfunction.
- Since FPC cable has gold plated terminals, gilt finish contact shoe connector is recommended.

5. Absolute Maximum Rating

B

GND=0V

Item	Symbol	Condition	Rating		Unit	Applicable terminal
			MIN	MAX		
Supply voltage	VDD		-0.3	3.9	V	VDD
Input voltage for logic	VI		-0.3	VDD+0.3	V	UL/DR , IM , STBYB
Forward current	IL1,IL2		--	(TBD)	mA	BLH-BLL1/BLL2
Storage temperature range	Tstg		-40	95	℃	

6. Recommended Operating Conditions

GND=0V

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Supply voltage	VDD		3.0	3.3	3.6	V	VDD
Input voltage for logic	VI		0	--	VDD	V	UL/DR , IM , STBYB
Operational temperature range	Top	Note1	-30	+25	+85	℃	Panel surface temperature

Note1: This monitor is operatable in this temperature range. With regard to optical characteristics, refer to Item 9."CHARACTERISTICS".

7. Electrical Characteristics

7.1 DC Characteristics

(Unless otherwise noted, Ta=25 ℃,VDD=3.3V,GND=0V)

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
High Level Input Voltage	VIH		0.7VDD	—	VDD	V	UL/DR,IM,STBYB
Low Level Input Voltage	VIL		0	—	0.3VDD	V	
Pull up/down resistor	RI		200	350	850	kΩ	Pull up : IM , STBYB
			100	175	425	kΩ	Pull up : UL/DR
Operating Current	IDD	Color Bar fclk = 27.2 MHz	(TBD)	(TBD)	(TBD)	mA	VDD

B

(BackLight)

Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Forward current	IL1	Ta=25 ℃ , Note1	--	(20)	(60)	mA	BLH - BLL1
	IL2		--	(20)	(60)	mA	BLH - BLL2
Forward voltage *Reference value	VL1	Ta=25 ℃ IL1=IL2=(20) mA	--	(21.9)	(25.0)	V	BLH - BLL1
	VL2		--	(21.9)	(25.0)	V	BLH - BLL2
Estimated Life of LED	LL	Ta=25 ℃ Note2 IL1=IL2=(20) mA	--	(100000)	--	hrs	

Note1: - Please control so that each current does not vary (IL1 = IL2).

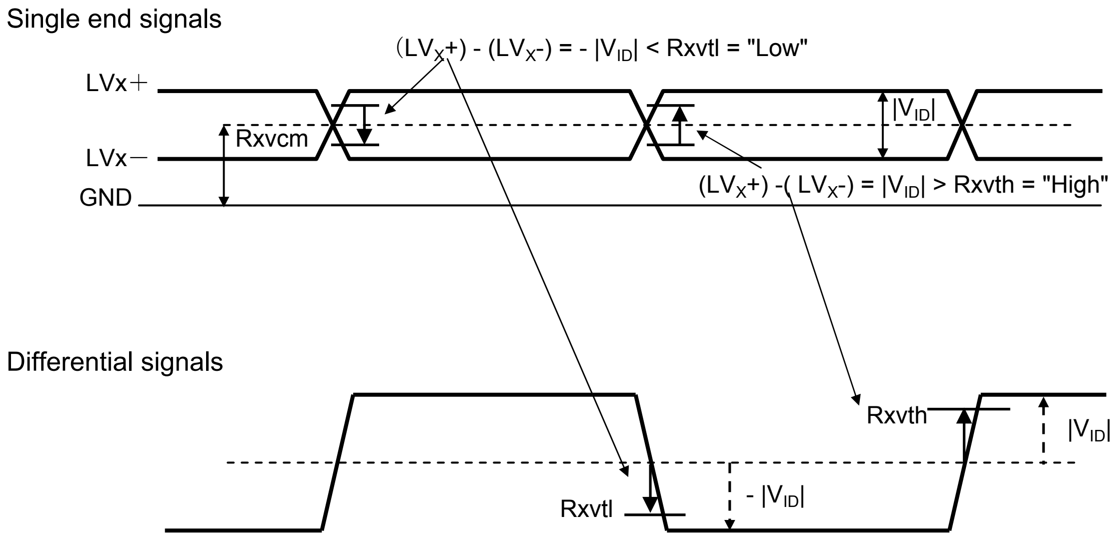
Note2:
- The lifetime of the LED is defined as a period till the brightness of the LED decreases to the half of its initial value.
- This figure is given as a reference purpose only, and not as a guarantee.
- This figure is estimated for an LED operating alone.
  As the performance of an LED may differ when assembled as a monitor together with a TFT panel due to different environmental temperature.
- Estimated lifetime could vary on a different temperature and usually higher temperature could reduce the life significantly.

7.2 LVDS interface

7.2.1 LVDS DC Characteristics

(Unless otherwise noted, Ta=25 ℃,VDD=3.3V,GND=0V)

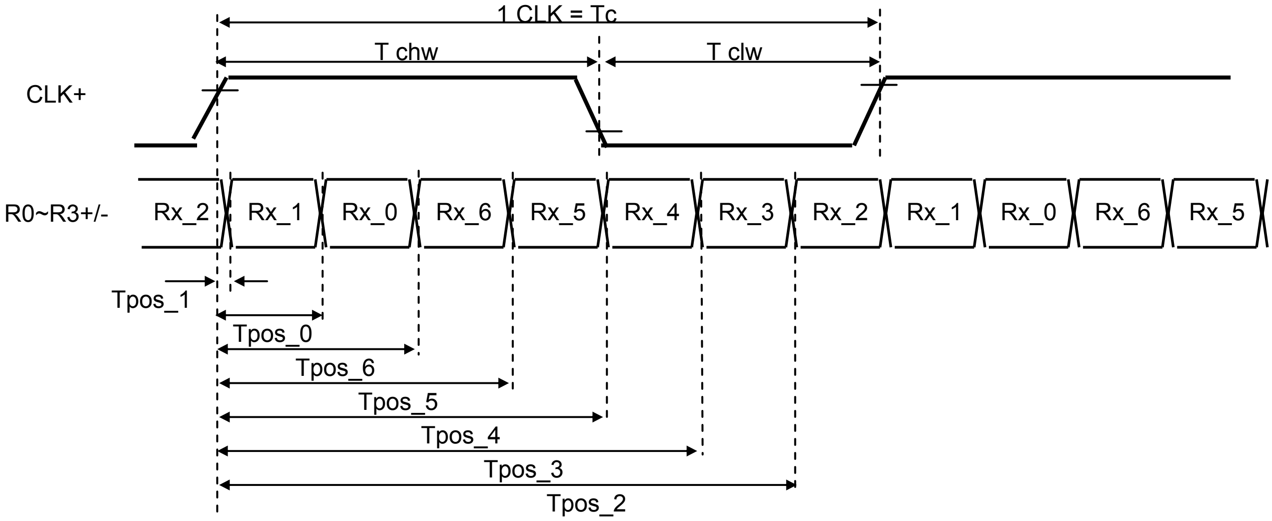
Item	Symbol	Condition	Rating			Unit	Applicable terminal
			MIN	TYP	MAX		
Differential input high threshold voltage	Rxvth	Rxvcm=1.2V	-	-	0.1	V	CLK+、CLK- R0+、R0-、R1+、R1- R2+、R2-、R3+、R3-
Differential input low threshold voltage	Rxvtl		-0.1	-	-	V	
Differential input common Mode voltage	Rxvcm		1.0	1.2	$1.8-\|V_{ID}\|/2$	V	
Differential input voltage	$\|V_{ID}\|$		0.2	-	0.6	V	
Differential input leakage current	RV_{leak}		-10	－	＋10	μA	

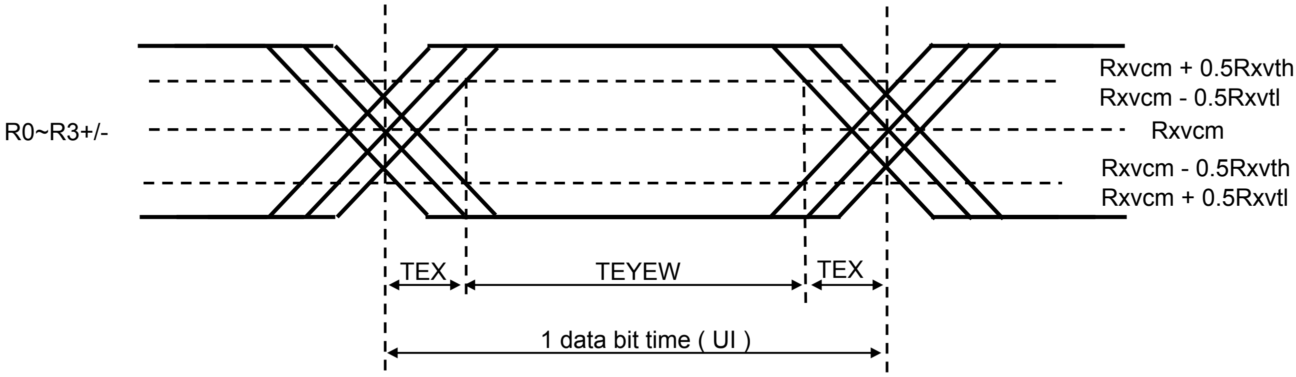

Single end signals
$(LV_X+) - (LV_X-) = -|V_{ID}| < Rxvtl$ = "Low"
LVx＋
Rxvcm
LVx－
GND
$|V_{ID}|$
$(LV_X+) - (LV_X-) = |V_{ID}| > Rxvth$ = "High"
Differential signals
Rxvth
$|V_{ID}|$
Rxvtl
$-|V_{ID}|$

7.2.2 LVDS AC Characteristics

(Unless otherwise noted, Ta=25 ℃,VDD=3.3V,GND=0V)

Item	Symbol	Rating			Unit	
		MIN	TYP	MAX		
CLK Frequency	f clk	25.2	27.2	30.5	MHz	
Clock period	Tc	32.8	36.8	39.7	ns	
1 data bit time	UI	-	1/7	-	Tc	
CLK High level Width	T chw	2.9	4	4.1	UI	
CLK Low level Width	T clw	2.9	3	4.1	UI	
Position 1	Tpos_1	-0.2	0	0.2	UI	
Position 0	Tpos_0	0.8	1	1.2	UI	
Position 6	Tpos_6	1.8	2	2.2	UI	
Position 5	Tpos_5	2.8	3	3.2	UI	
Position 4	Tpos_4	3.8	4	4.2	UI	
Position 3	Tpos_3	4.8	5	5.2	UI	
Position 2	Tpos_2	5.8	6	6.2	UI	
Reciever Strobe Position 7	TEYEW	0.6	-	-	UI	
Reciever Strobe Position 8	TEX	-	-	0.2	UI	

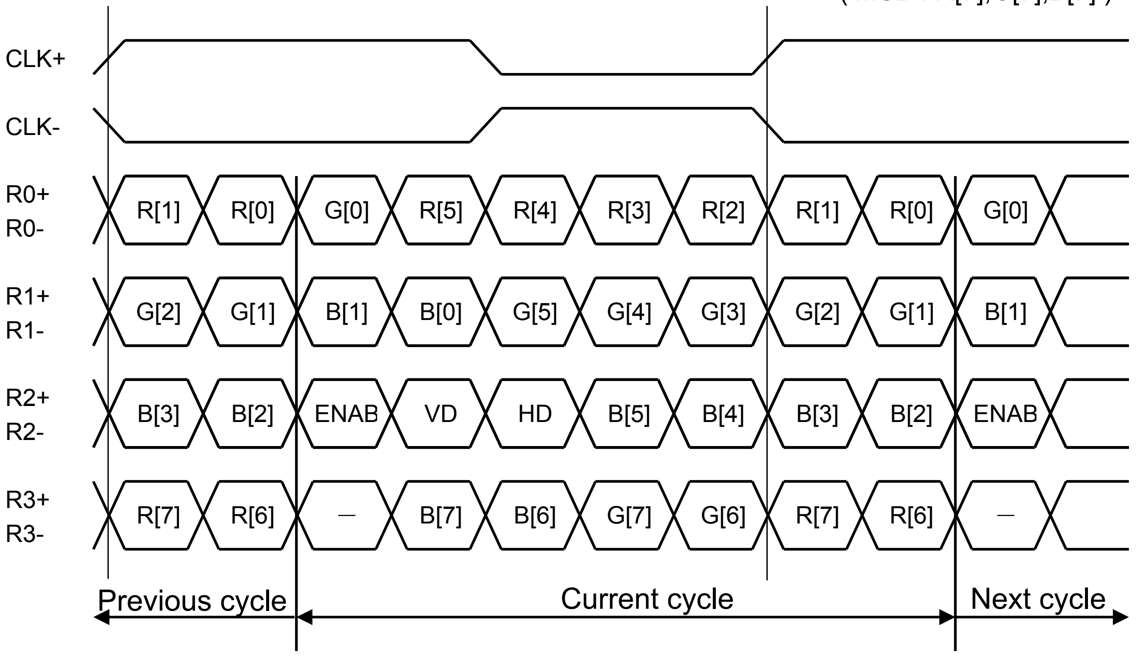

1 CLK = Tc
T chw
T clw
CLK+
R0~R3+/-
Rx_2
Rx_1
Rx_0
Rx_6
Rx_5
Rx_4
Rx_3
Rx_2
Rx_1
Rx_0
Rx_6
Rx_5
Tpos_1
Tpos_0
Tpos_6
Tpos_5
Tpos_4
Tpos_3
Tpos_2


R0~R3+/-
Rxvcm + 0.5Rxvth
Rxvcm - 0.5Rxvtl
Rxvcm
Rxvcm - 0.5Rxvth
Rxvcm + 0.5Rxvtl
TEX
TEYEW
TEX
1 data bit time (UI)

7.2.3 LVDS Data Format

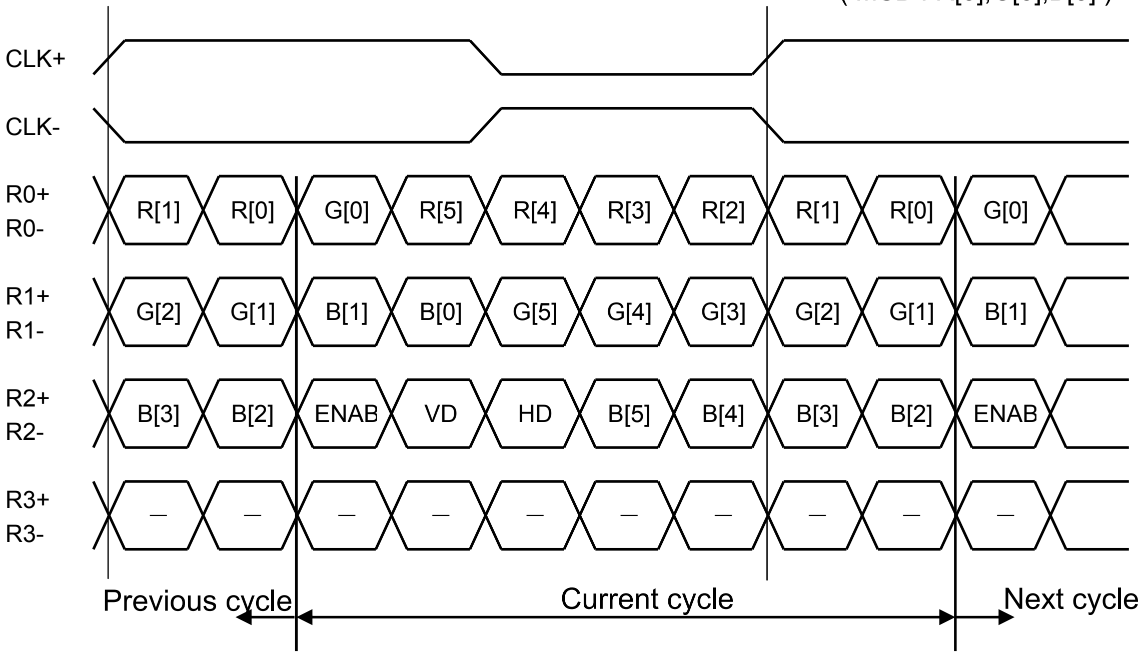
VESA Format 8bit

* (MSB : R[7],G[7],B[7])


CLK+
CLK-
R0+
R0-
R[1] R[0] G[0] R[5] R[4] R[3] R[2] R[1] R[0] G[0]
R1+
R1-
G[2] G[1] B[1] B[0] G[5] G[4] G[3] G[2] G[1] B[1]
R2+
R2-
B[3] B[2] ENAB VD HD B[5] B[4] B[3] B[2] ENAB
R3+
R3-
R[7] R[6] – B[7] B[6] G[7] G[6] R[7] R[6] –
Previous cycle
Current cycle
Next cycle

VESA Format 6bit

* (MSB : R[5],G[5],B[5])

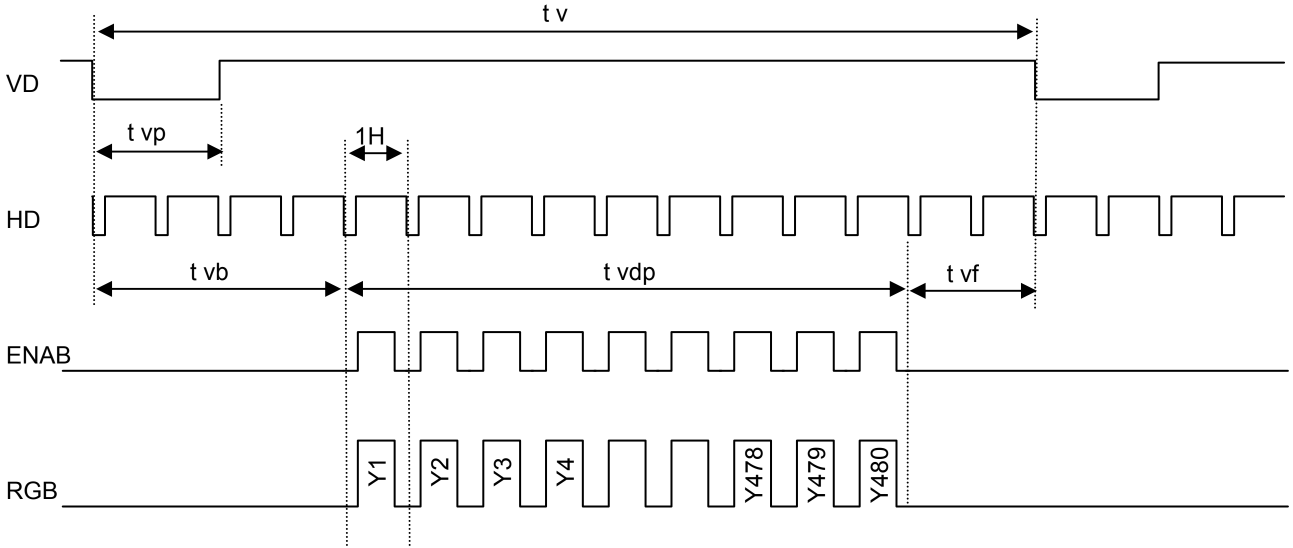

CLK+
CLK-
R0+
R0-
R[1] R[0] G[0] R[5] R[4] R[3] R[2] R[1] R[0] G[0]
R1+
R1-
G[2] G[1] B[1] B[0] G[5] G[4] G[3] G[2] G[1] B[1]
R2+
R2-
B[3] B[2] ENAB VD HD B[5] B[4] B[3] B[2] ENAB
R3+
R3-
– – – – – – – – – –
Previous cycle
Current cycle
Next cycle

7.3 Input timing

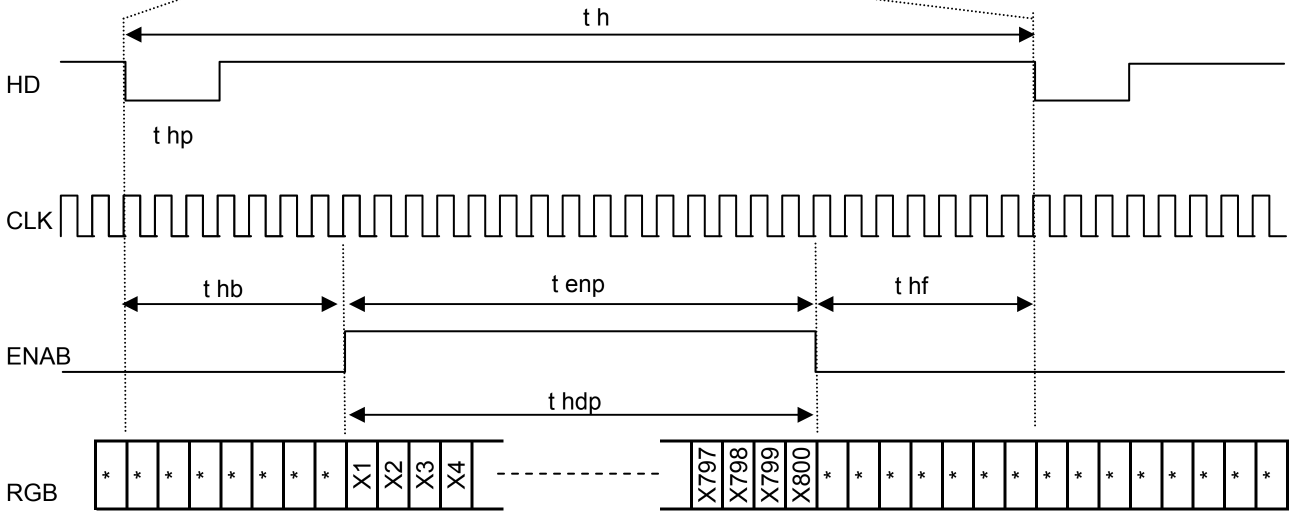
Item	Symbol	Rating			Unit	Signal (*)
		MIN	TYP	MAX		
CLK frequency	fCLK	25.2	27.2	30.5	MHz	CLK
VD frequency	fVD		60		Hz	VD
1 vertical field	tv	490	528	552	H	
VD pulse width	tvp	1	2	66	H	
VD back porch	tvb	5	10	67	H	VD,HD,ENAB R[7:0],G[7:0],B[7:0]
VD front porch	tvf	5	38	67	H	
Vertical valid data	tvdp	480			H	
HD frequency	fHD		28.8		kHz	HD
1 horizontal field	th	856	860	920	CLK	
HD pulse width	thp	1	2	100	CLK	
HD back porch	thb	5	16	101	CLK	CLK,HD,ENAB R[7:0],G[7:0],B[7:0]
HD front porch	thf	19	44	115	CLK	
ENAB pulse width	tenp	800			CLK	
Horizontal valid data	thdp	800			CLK	

(*) Input terminals are (R0+/- , R1+/- , R2+/- , R3+/- , CLK+/-).

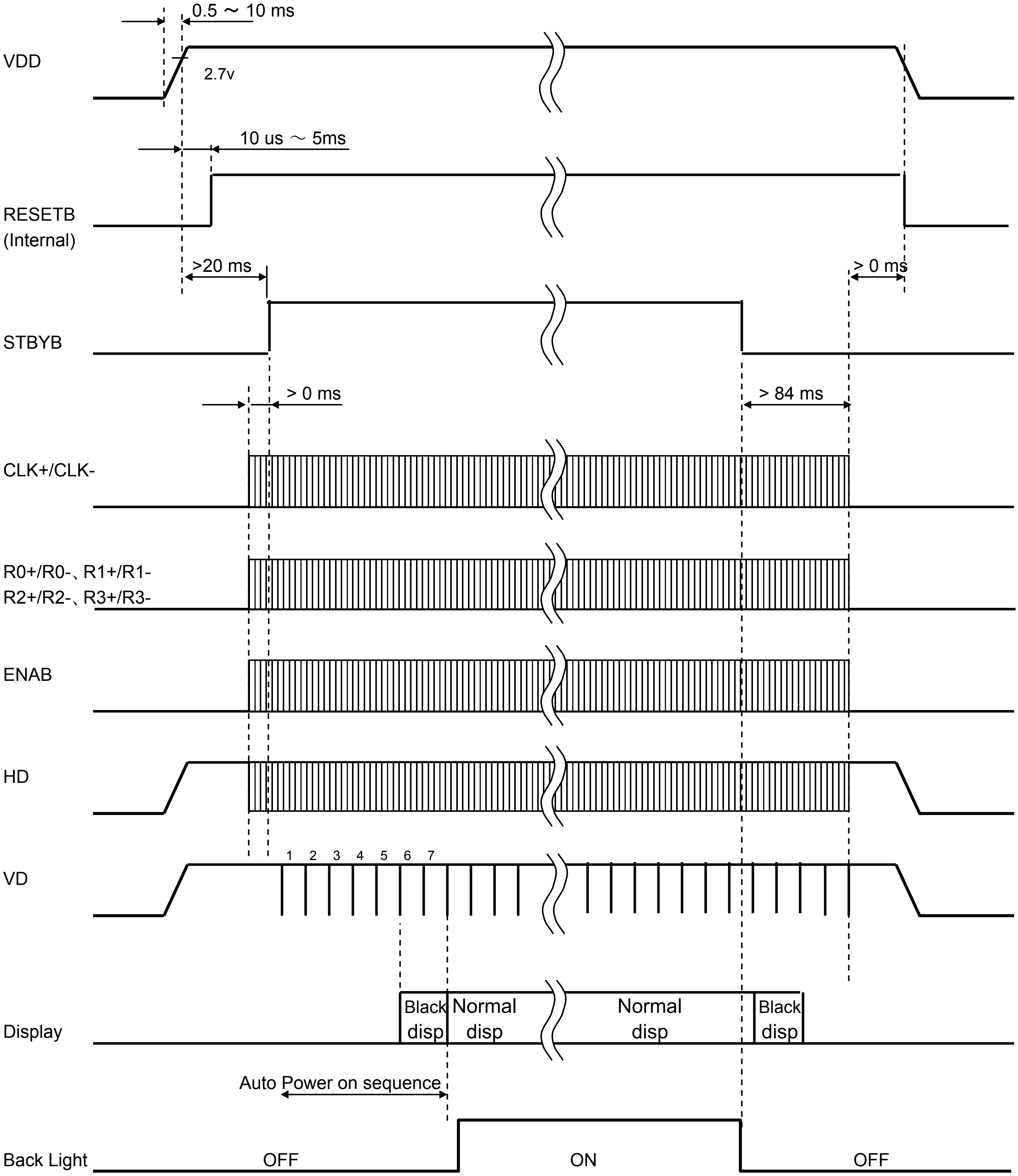
<Vertical timing>


t v
VD
t vp
1H
HD
t vb
t vdp
t vf
ENAB
RGB
Y1
Y2
Y3
Y4
Y478
Y479
Y480

<Horizontal timing>


t h
HD
t hp
CLK
t hb
t enp
t hf
ENAB
t hdp
RGB
*
X1
X2
X3
X4
X797
X798
X799
X800

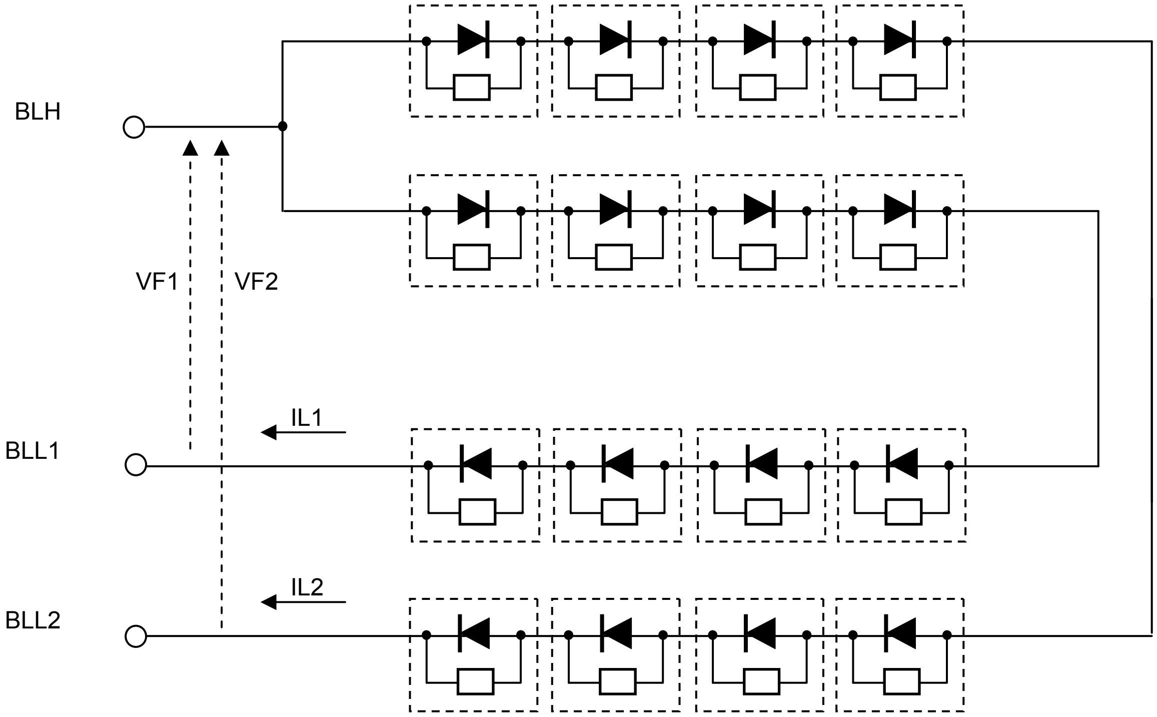
7.4 Power ON/OFF sequence

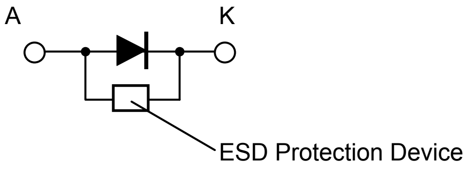

0.5 ～ 10 ms
VDD
2.7v
10 us ～ 5ms
RESETB
(Internal)
>20 ms
> 0 ms
STBYB
> 0 ms
> 84 ms
CLK+/CLK-
R0+/R0-、R1+/R1-
R2+/R2-、R3+/R3-
ENAB
HD
VD
1 2 3 4 5 6 7
Display
Black disp
Normal disp
Normal disp
Black disp
Auto Power on sequence
Back Light
OFF
ON
OFF

Note: ENAB, HD and VD are included in the R2 + / R2- terminals.

8. LED Circuit

△B


BLH
VF1
VF2
IL1
BLL1
IL2
BLL2


A
K
ESD Protection Device

* It is recommended to control currents of BLL1 / BLL2 to equal current values (IL1 = IL2).

9. Characteristics

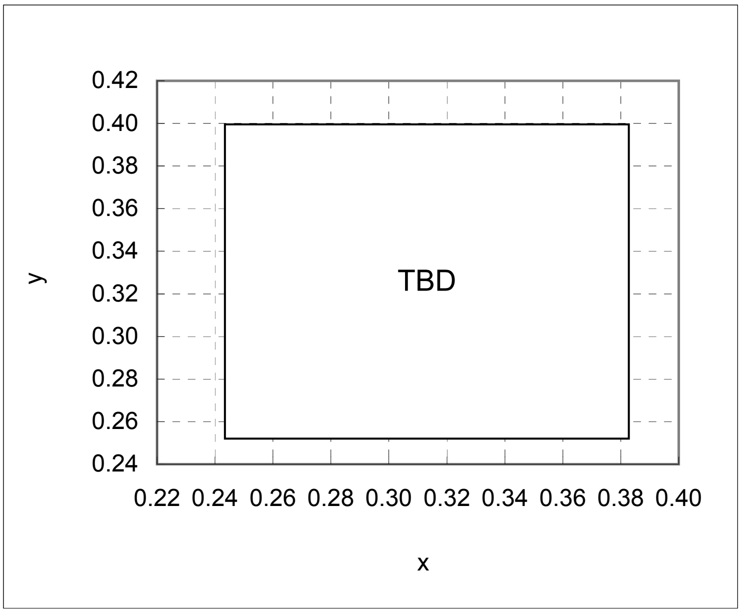
9.1 Optical Characteristics

< Measurement Condition >

Measuring instruments:	CS2000（KONICA MINOLTA）, LCD7200(OTSUKA ELECTRONICS）, EZcontrast160D（ELDIM）
Driving condition:	VDD=3.3V,GND=0V Optimized VCOMDC
Backlight:	IL1=IL2=(20.0)mA
Measured temperature:	Ta=25°C

Item		Symbol	Condition	MIN	TYP	MAX	Unit	Note No.	Remark
Response time	Rise time	TON	[Data]= 00h→FFh	—	—	(60)	ms	1	
	Fall time	TOFF	[Data]= FFh→00h	—	—	(40)	ms		
Contrast ratio	Backlight ON	CR	[Data]= FFh / 00h	(TBD)	(900)	—		2	
	Backlight OFF			—	(3.0)	—			
Viewing angle	Left	θL	[Data]= FFh / 00h CR≧10	(80)	—	—	deg	3	
	Right	θR		(80)	—	—	deg		
	Up	φU		(80)	—	—	deg		
	Down	φD		(80)	—	—	deg		
White Chromaticity		x		White chromaticity range				4	
		y							
Burn-in				No noticeable burn-in image shall be observed after 2 hours of window pattern display.				5	
Center brightness			[Data]=FFh	—	(790)	—	cd/m^2	6	IL1=IL2=(40.0)mA
				(TBD)	(400)	—			IL1=IL2=(20.0)mA
Brightness distribution			[Data]=FFh	(70)	—	—	%	7	

* Note number 1 to 7: Refer to the APPENDIX of "Reference Method for Measuring Optical Characteristics".



White Chromaticity Range

【White Chromaticity Range】

x	y
TBD	TBD
TBD	TBD
TBD	TBD
TBD	TBD
TBD	TBD
TBD	TBD
TBD	TBD

B

9.2 Temperature Characteristics

< Measurement Condition >

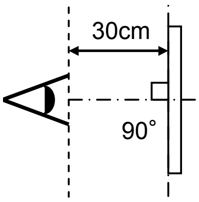
Measuring instruments:	CS2000（KONICA MINOLTA）, LCD7200(OTSUKA ELECTRONICS）
Driving condition:	VDD=3.3V, GND=0V Optimized VCOMDC
Backlight:	IL1=IL2=(20.0)mA

Item			Specification		Remark
			Ta＝(-20)°C	Ta＝70˚C	
Contrast ratio		CR	(200) or more	(200) or more	Backlight ON
Response time	Rise time	TON	(300) msec or less	(30) msec or less	
	Fall time	TOFF	(300) msec or less	(50) msec or less	
Display Quality			No noticeable display defect or ununiformity should be observed.		

10. Criteria of Judgment

B

10.1 Defective Display and Screen Quality



Test Condition:	Observed TFT-LCD monitor from front during operation with the following conditions
Driving Signal	Raster Patter (RGB, white, black)
Signal condition	[Data]:00h, (TBD)h, FFh (3steps)
Observation distance	30 cm
Illuminance	200 to 350 lx
Backlight	IL1=IL2=(20.0)mA

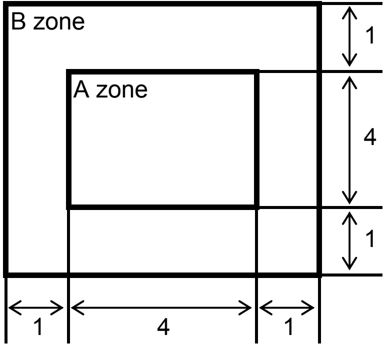
Defect item		Defect content		Criteria
Display Quality	Line defect	Black, white or color line, 3 or more neighboring defective dots		Not exists
	Dot defect	Uneven brightness on dot-by-dot base due to defective TFT or CF, or dust is counted as dot defect (brighter dot, darker dot) High bright dot: Visible through 2% ND filter at [Data]=00h Low bright dot: Visible through 5% ND filter at [Data]=00h Dark dot: Appear dark through white display at [Data]=(TBD)h		Refer to table 1
		Invisible through 5% ND filter at [Data]=00h		Acceptable
Screen Quality	Dirt	Uneven brightness (white stain, black stain etc)		Invisible through 5% ND filter at Black screen. Invisible through 1% ND filter at other screen.
	Foreign particle	Point-like	0.25mm< φ	N=0
			0.20mm< φ ≦0.25mm	N≦2
			φ ≦0.20mm	Acceptable
		Liner	3.0mm<length and 0.08mm<width	N=0
			length≦3.0mm or width≦0.08mm	Acceptable
	Others			Use boundary sample for judgment when necessary

φ(mm): Average diameter = (major axis + minor axis)/2
Permissible number: N

Table 1

Area	High bright dot	Low bright dot	Dark dot	Total	Criteria
A	0	2	2	3	Permissible distance between same color bright dots (includes neighboring dots): 3 mm or more Permissible distance between same color high bright dots (includes neighboring dots): 5 mm or more
B	2	4	4	6	
Total	2	4	4	7	

<Landscape model>



Division of A and B areas
B area: Active area
Dimensional ratio between A and B areas: 1: 4: 1
(Refer to the left figure)

Division of A and B areas

10.2 Screen and Other Appearance

Testing conditions

Observation distance	30cm
Illuminance	1200～2000 lx

Item		Criteria	Remark
Polarizer	Flaw Stain Bubble Dust Dent	Ignore invisible defect when the backlight is on.	Applicable area: Active area only (Refer to the section 3.2 "Outward form")
S-case		No functional defect occurs	
FPC cable		No functional defect occurs	

B

11. Reliability Test

	Test item	Test condition	number of failures /number of examinations
Durability test	High temperature storage	Ta=95°C (500)hrs	TBD
	Low temperature storage	Ta=-40°C (500)hrs	TBD
	High temperature & high humidity storage	Ta=60°C, RH=90% (500)hrs non condensing ※	TBD
	High temperature operation	Tp=85°C (500)hrs	TBD
	Low temperature operation	Tp=-30°C (500)hrs	TBD
	High temp & humid operation	Tp=40°C, RH=90% (500)hrs non condensing ※	TBD
	Thermal shock storage	-40←→95°C(30min/30min) (100)cycles	TBD
Mechanical environmental test	Electrostatic discharge test (Non operation)	Confirms to EIAJ ED-4701/300 C=200pF,R=0Ω,V=±200V Each 3 times of discharge on and power supply and other terminals.	TBD
	Surface discharge test (Non operation)	C=250pF, R=100Ω, V=±(TBD)kV Each 5 times of discharge in both polarities on the center of screen with the case grounded.	TBD
	FPC tension test	Pull the FPC with the force of 3N for 10 sec. in the direction - 90-degree to its original direction.	TBD
	FPC bend test	Pull the FPC with the force of 3N for 10 sec. in the direction -180-degree to its original direction. Reciprocate it 3 times.	TBD
	Vibration test	Total amplitude 1.5mm, f=10～55Hz, X,Y,Z directions for each 2 hours	TBD
	Impact test	Use ORTUS TECHNOLOGY original jig (see next page)and make an impact with peak acceleration of 1000m/s2 for 6 msec with half sine-curve at 3 times to each X, Y, Z directions in conformance with JIS C 60068-2-27-2011.	TBD
Packing test	Packing vibration-proof test	Acceleration of 19.6m/s^2 with frequency of 10→55→10Hz, X,Y, Zdirection for each 30 minutes	TBD
	Packing drop test	Drop from 75cm high. 1 time to each 6 surfaces, 3 edges, 1 corner	TBD

Note:Ta=ambient temperature Tp=Panel temperature

※ The profile of high temperature/humidity storage and High Temperature/humidity operation
(Pure water of over 10MΩ·cm shall be used.)

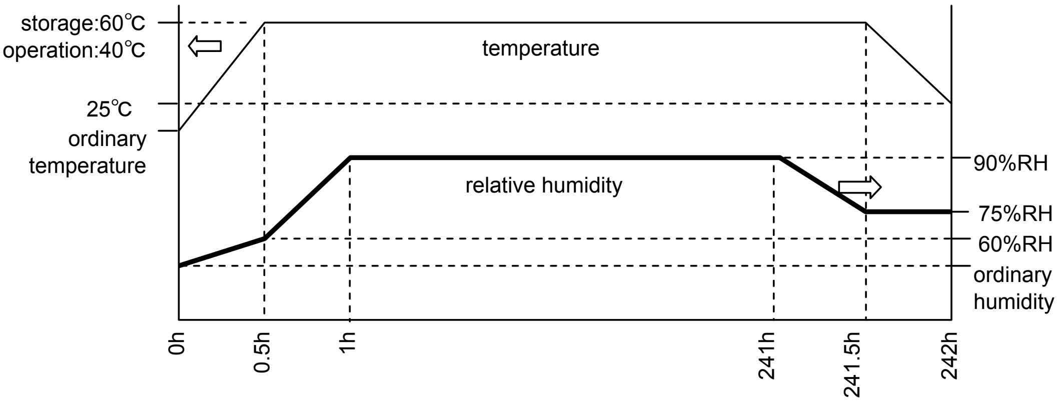
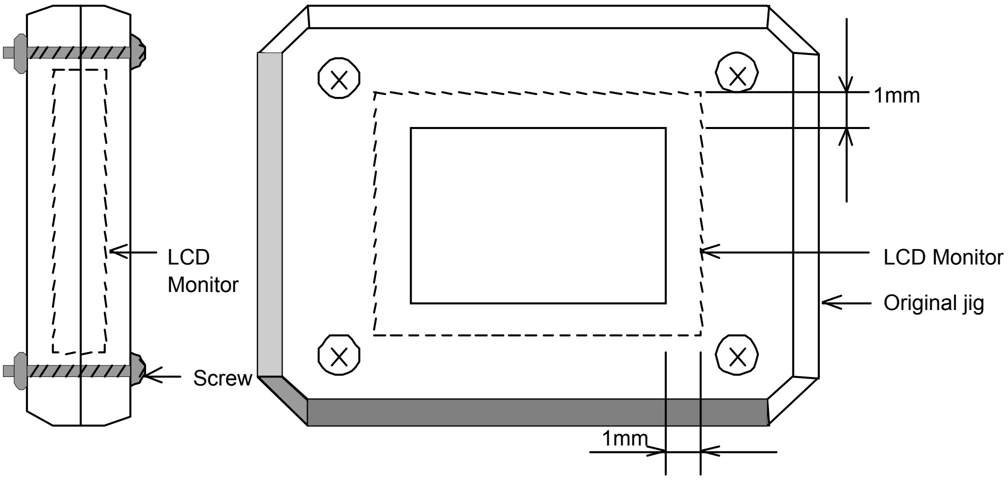


Table2.Reliability Criteria

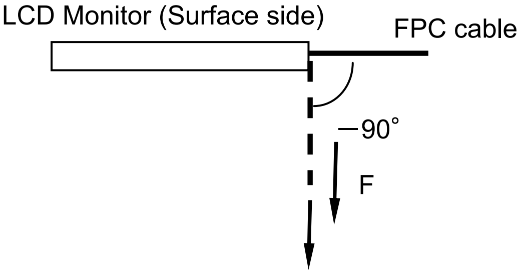
The parameters should be measured after leaving the monitor at the ordinary temperature for 24 hours or more after the test completion.

item	Standard	Remarks
Display quality	No visible abnormality shall be seen. (Except for unevenness by Pol deterioration.)	
Contrast ratio	200 or more	Backlight ON

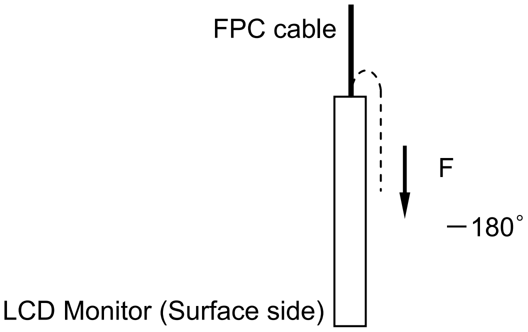
ORTUS TECHNOLOGY Original Jig


LCD
Monitor
Screw
1mm
LCD Monitor
Original jig
1mm

Tension Test Method for FPC cable


LCD Monitor (Surface side)
FPC cable
－90˚
F

Bend Test Method for FPC cable


FPC cable
F
－180˚
LCD Monitor (Surface side)

12. Packing Specifications

TBD

13. Handling Instruction

13.1 Cautions for Handling LCD panels

⚠ **Caution**

(1) Do not make an impact on the LCD panel glass because it may break and you may get injured from it.

(2) If the glass breaks, do not touch it with bare hands.
（Fragment of broken glass may stick you or you cut yourself on it.

(3) If you get injured, receive adequate first aid and consult a medial doctor.

(4) Do not let liquid crystal get into your mouth.
（If the LCD panel glass breaks, try not let liquid crystal get into your mouth even toxic property of liquid crystal has not been confirmed.)

(5) If liquid crystal adheres, rinse it out thoroughly.
（If liquid crystal adheres to your cloth or skin, wipe it off with rubbing alcohol or wash it thoroughly with soap. If liquid crystal gets into eyes, rinse it with clean water for at least 15 minutes and consult an eye doctor.

(6) If you scrap this products, follow a disposal standard of industrial waste that is legally valid in the community, country or territory where you reside.

(7) Do not connect or disconnect this product while its application products is powered on.

(8) Do not attempt to disassemble or modify this product as it is precision component.

(9) If a part of soldering part has been exposed, and avoid contact (short-circuit) with a metallic part of the case etc. about FPC of this model, please.
Please insulate it with the insulating tape etc. if necessary.
The defective operation is caused, and there is a possibility to generation of heat and the ignition.

(10) Since excess current protection circuit is not built in this TFT module, there is the possibility that LCD module or peripheral circuit become feverish and burned in case abnormal operation is generated.
We recommend you to add excess current protection circuit to power supply.

(11) The devices on the FPC are damageable to electrostatic discharge, because the terminals of the devices are exposed.
Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors.
Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.

⚠ **Caution** **This mark is used to indicate a precaution or an instruction which, if not correctly observed, may result in bodily injury, or material damages alone.**

13.2 Precautions for Handling

1) Wear finger tips at incoming inspection and for handling the TFT monitors to keep display quality and keep the working area clean.
Do not touch the surface of the monitor as it is easily scratched.

2) Wear grounded wrist-straps and use electrostatic neutralization blowers to prevent static charge and discharge when handling the TFT monitors as the LED in this TFT monitors is damageable to electrostatic discharge.
Designate an appropriate operating area, and set equipment, tools, and machines properly when handling this product.

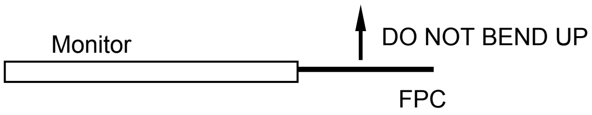
3) Avoid strong mechanical shock including knocking, hitting or dropping to the TFT monitors for protecting their glass parts. Do not use the TFT monitors that have been experienced dropping or strong mechanical shock.

4) Do not use or storage the TFT monitors at high temperature and high humidity environment.
Particularly, never use or storage the TFT monitors at a location where condensation builds up.

5) Avoid using and storing TFT monitors at a location where they are exposed to direct sunlight or ultraviolet rays to prevent the LCD panels from deterioration by ultraviolet rays.

6) Do not stain or damage the contacts of the FPC cable .
FPC cable needs to be inserted until it can reach to the end of connector slot.
During insertion, make sure to keep the cable in a horizontal position to avoid an oblique insertion.
Otherwise, it may cause poor contact or deteriorate reliability of the FPC cable.

7) Do not bend or pull the FPC cable or carry the TFT monitor by holding the FPC cable.
Especially, it will cause mechanical damage or critical defect if FPC is pull up or bent up to short of display.



8) Peel off the protective film on the TFT monitors during mounting process.
Refer to the section 13.5 on how to peel off the protective film.
We are not responsible for electrostatic discharge failures or other defects occur when peeling off the protective film.

13.3 Precautions for Operation

1) Since this TFT monitors are not equipped with light shielding for the driver IC, do not expose the driver IC to strong lights during operation as it may cause functional failures.

2) In case of powering up or powering off this LCD module, be sure to comply the sequence as instructed in this specification.

3) Do not plug in or out the FPC cable while power supply is switch on.
Plug the FPC cable in and out while power supply is switched off.

4) Do not operate the TFT monitors in the strong magnetic field. It may break the TFT monitors.

5) Do not display a fixed image on the screen for a long time.
Use a screen-saver or other measures to avoid a fixed image displayed on the screen for a long time.
Otherwise, it may cause burn-in image on the screen due the characteristics of liquid crystal.

13.4 Storage Condition for Shipping Cartons

Storage environment

・ Temperature	0 to 40˚C
・ Humidity	60%RH or less No-condensing occurs under low temperature with high humidity condition.
・ Atmosphere	No poisonous gas that can erode electronic components and/or wiring materials should be detected.
・ Time period	1 year
・ Unpacking	To prevent damages caused by static electricity, anti-static precautionary measures (e.g. earthing, anti-static mat) should be implemented. After unpack, keep product in the appropriate condition, otherwise bubble seal of Protective film may be printed on Polarizer.
・ Maximum piling up	7 cartons

*Conditions to storage after unpacking

Storage environment

・ Temperature	0 to 40˚C
・ Humidity	60%RH or less No-condensing occurs under low temperature with high humidity condition.
・ Atmosphere	No poisonous gas that can erode electronic components and/or wiring materials should be detected.
・ Time period	1 year (Shelf life)
・ Others	Keep/ store away from direct sunlight Storage goods on original tray made by ORTUS.

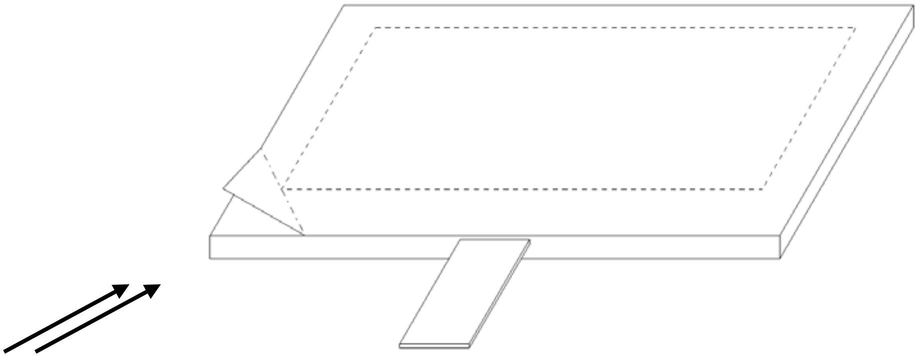
13.5 Precautions for Peeling off the Protective film

The followings work environment and work method are recommended to prevent the TFT monitors from static damage or adhesion of dust when peeling off the protective films.

A) Work Environment
- a) Humidity: 50 to 70 %RH, Temperature15 to 27 °C
- b) Operators should wear conductive shoes, conductive clothes, conductive finger tips and grounded wrist-straps.
  Use an electrostatic neutralization blower.
  Anti-static treatment should be implemented to work area's floor.
- c) Use a room shielded against outside dust with sticky floor mat laid at the entrance to eliminate dirt.

B) Work Method

The following procedures should taken to prevent the driver ICs from charging and discharging.
- a) Use an electrostatic neutralization blower to blow air on the TFT monitors to its lower left when FPC is placed at the bottom.
  Optimize direction of the blowing air and the distance between the TFT monitors and the electrostatic neutralization blower.
- b) Put an adhesive tape (Scotch tape, etc) at the lower left corner area of the protective film to prevent scratch on surface of TFT monitors.
- c) Peel off the adhesive tape slowly (spending more than 2 secs to complete) by pulling it to opposite direction.



Blower wind direction (Set an ion blower with its adequate conditions.)

13.6 Warranty

ORTUS is only liable to defective goods which is stored and used under the condition complying with this specifications and returned within 1 (one) year.

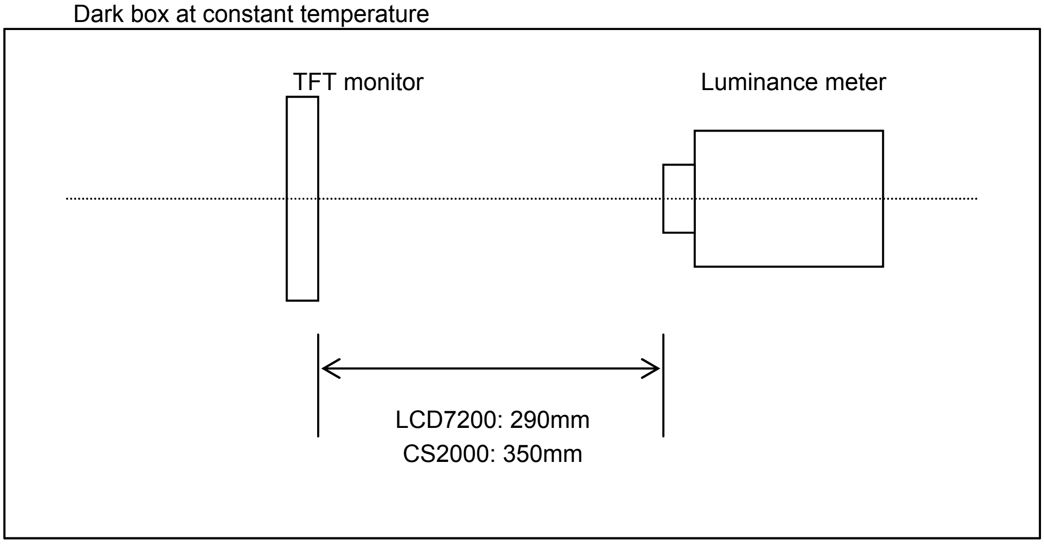
Warranty caused by manufacturing defect shall be conducted by replacement of goods or refundment at unit price.

APPENDIX

B Reference Method for Measuring Optical Characteristics and Performance

1. Measurement Condition (Backlight ON)

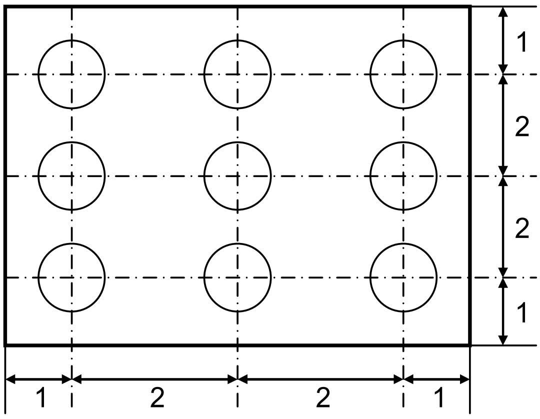
Measuring instruments:	CS2000（KONICA MINOLTA）, LCD7200(OTSUKA ELECTRONICS）,EZcontrast160D（ELDIM）
Driving condition:	Refer to the section "Optical Characteristics"
Measured temperature:	25°C unless specified
Measurement system:	See the chart below. The luminance meter is placed on the normal line of measurement system.
Measurement point:	At the center of the screen unless otherwise specified


Dark box at constant temperature
TFT monitor
Luminance meter
LCD7200: 290mm
CS2000: 350mm

Measurement is made after 30 minutes of lighting of the backlight.

Measurement point:	At the center point of the screen Brightness distribution: 9 points shown in the following drawing.

<Landscape model>

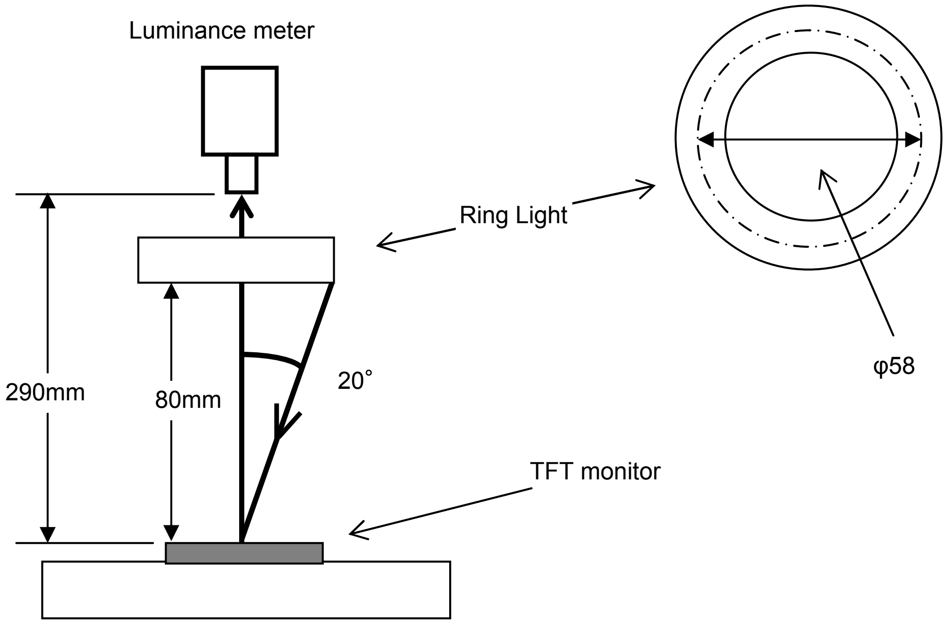


Dimensional ratio of active area

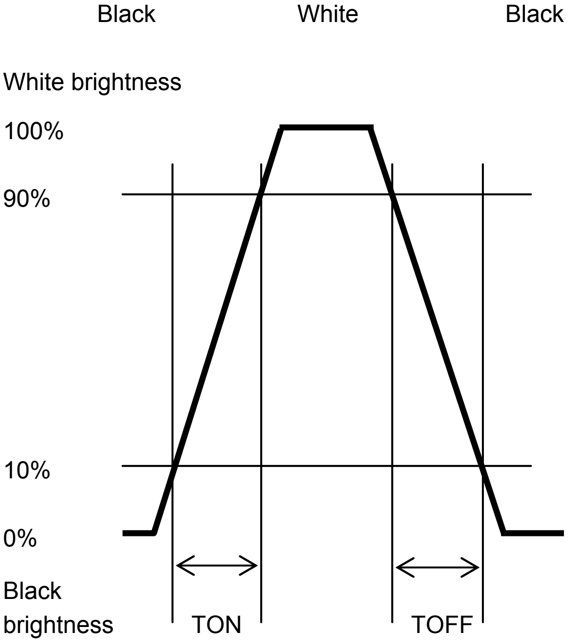
Backlight IL1=IL2=(20.0)mA

Measurement Condition (Contrast ratio Backlight OFF only)

Measuring instruments:	LCD7200(OTSUKA ELECTRONICS),Ring Light(40,000 lx,φ58)
Driving condition:	Refer to the section "Optical Characteristics"
Measured temperature:	25˚C unless specified
Measurement system:	See the chart below.
Measurement point:	At the center of the screen.


Luminance meter
Ring Light
290mm
80mm
20˚
φ58
TFT monitor

2. Test Method

Notice	Item	Test method	Measuring instrument	Remark
1	Response time	Measure output signal waves with a brightness meter when the raster or window pattern is changed over from white to black and from black to white  Black White Black White brightness 100% 90% 10% 0% Black brightness TON TOFF	LCD7200	Black display [Data]=00h White display [Data]=FFh TON Rise time TOFF Fall time
2	Contrast ratio	Measure maximum luminance Y1([Data]=FFh) and minimum luminance Y2([Data]=00h) at the center of the screen by displaying raster or window pattern. Then calculate the ratio between these two values. Contrast ratio = Y1/Y2 Diameter of measuring point: 1mmφ(CS2000) Diameter of measuring point: 3mmφ(LCD7200)	CS2000 LCD7200	Backlight ON Backlight OFF
3	Viewing angle Horizontalθ Verticalφ	Move the luminance meter from right to left and up and down and determine the angles where contrast ratio is 10.	EZcontrast160D	
4	White chromaticity	Measure chromaticity coordinates x and y of CIE1931 colorimetric system at [Data] = FFh Color matching function: 2°view	CS2000	
5	Burn-in	Visually check burn-in image on the screen after 2 hours of "window display" ([Data]=00h/FFh).		At optimized VCOMDC
6	Center brightness	Measure the brightness at the center of the screen.	CS2000	
7	Brightness distribution	(Brightness distribution) = 100 x B/A % A : max. brightness of the 9 points B : min. brightness of the 9 points	CS2000	

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